



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®2006 = 40.0

Acer AR320 F2 (Intel Core i3-2100)

SPECint_base2006 = 38.1

CPU2006 license: 97

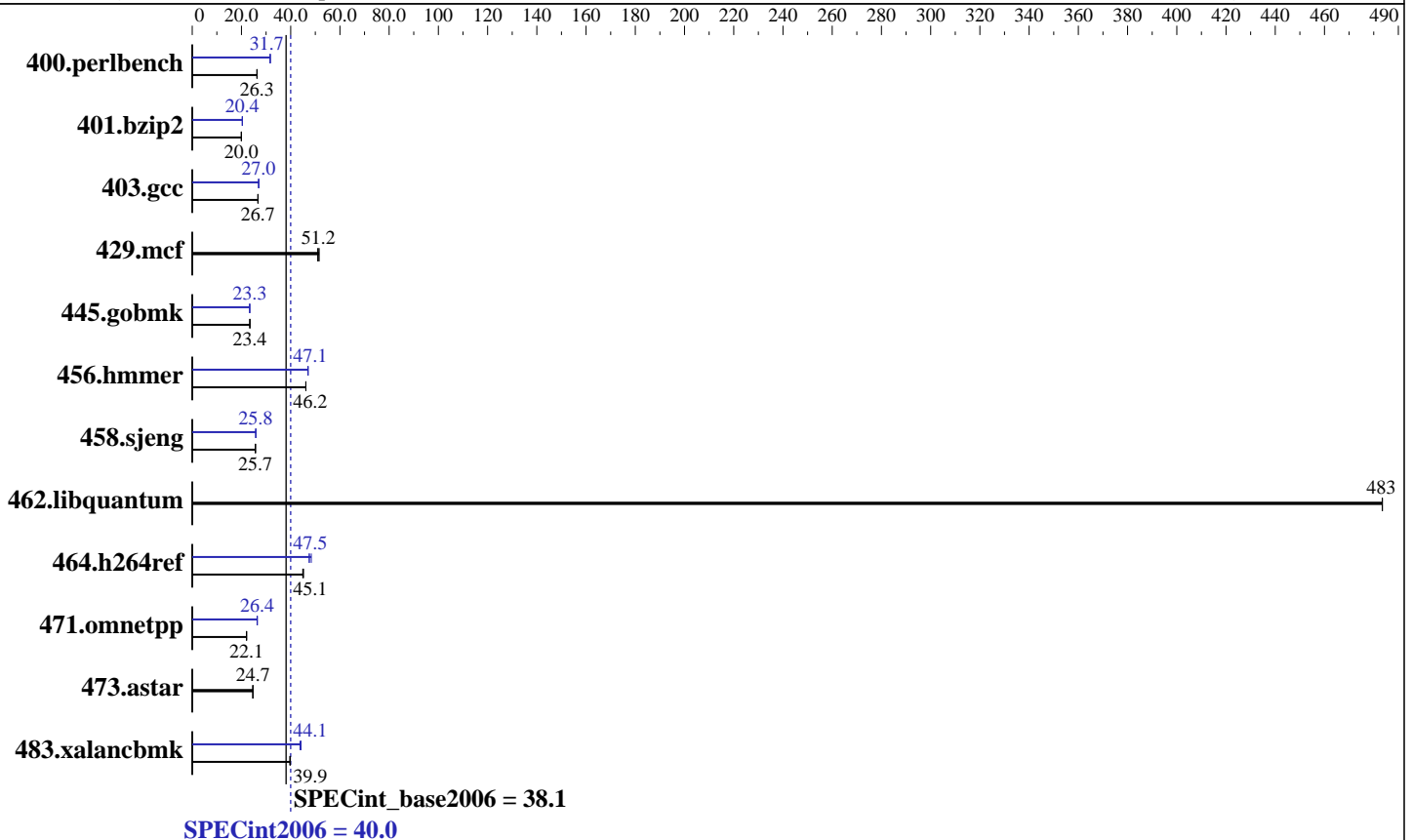
Test date: Mar-2012

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2012

Tested by: Acer Incorporated

Software Availability: Dec-2011



Hardware

CPU Name: Intel Core i3-2100
 CPU Characteristics:
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	372	26.3	371	26.4	<u>371</u>	<u>26.3</u>	309	31.6	<u>308</u>	<u>31.7</u>	308	31.7
401.bzip2	<u>483</u>	<u>20.0</u>	484	19.9	483	20.0	475	20.3	473	20.4	<u>474</u>	<u>20.4</u>
403.gcc	<u>301</u>	<u>26.7</u>	301	26.7	302	26.7	298	27.0	298	27.0	<u>298</u>	<u>27.0</u>
429.mcf	177	51.6	<u>178</u>	<u>51.2</u>	179	51.0	177	51.6	<u>178</u>	<u>51.2</u>	179	51.0
445.gobmk	448	23.4	<u>448</u>	<u>23.4</u>	448	23.4	449	23.4	<u>449</u>	<u>23.3</u>	449	23.3
456.hammer	202	46.2	202	46.2	<u>202</u>	<u>46.2</u>	198	47.1	<u>198</u>	<u>47.1</u>	198	47.0
458.sjeng	471	25.7	472	25.7	<u>472</u>	<u>25.7</u>	468	25.8	<u>468</u>	<u>25.8</u>	469	25.8
462.libquantum	42.9	483	42.9	484	<u>42.9</u>	<u>483</u>	42.9	483	42.9	484	<u>42.9</u>	<u>483</u>
464.h264ref	490	45.2	<u>491</u>	<u>45.1</u>	493	44.9	457	48.4	465	47.5	<u>465</u>	<u>47.5</u>
471.omnetpp	<u>283</u>	<u>22.1</u>	284	22.0	283	22.1	237	26.3	236	26.4	<u>237</u>	<u>26.4</u>
473.astar	<u>285</u>	<u>24.7</u>	285	24.6	285	24.7	<u>285</u>	<u>24.7</u>	285	24.6	285	24.7
483.xalancbmk	174	39.6	173	39.9	<u>173</u>	<u>39.9</u>	158	43.8	156	44.2	<u>156</u>	<u>44.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sat Mar 3 01:16:46 2012

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-2100 CPU @ 3.10GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 2
siblings : 4
physical 0: cores 0 1
cache size : 3072 KB

```

```

From /proc/meminfo
MemTotal:      8046960 kB
HugePages_Total: 0

```

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Platform Notes (Continued)

```

Hugepagesize:          2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
  15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 3 01:15

SPEC is set to: /usr/cpu2006
  Filesystem      Type      Size  Used Avail Use% Mounted on
  /dev/mapper/VolGroup-lv_root
                   ext4       50G   17G   31G   36% /

Additional information from dmidecode:
Memory:
  2x Nanya NT4GC72B8PG0NF-DI 4 GB 1333 MHz 2 rank

(End of data from sysinfo program)

```

General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "2"

```

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

```

Base Compiler Invocation

```

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

```



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
            -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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