



# SPEC<sup>®</sup> CFP2006 Result

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## Dell Inc.

**SPECfp<sup>®</sup>\_rate2006 = 225**

PowerEdge R620 (Intel Xeon E5-2609, 2.40 GHz)

**SPECfp\_rate\_base2006 = 219**

CPU2006 license: 55

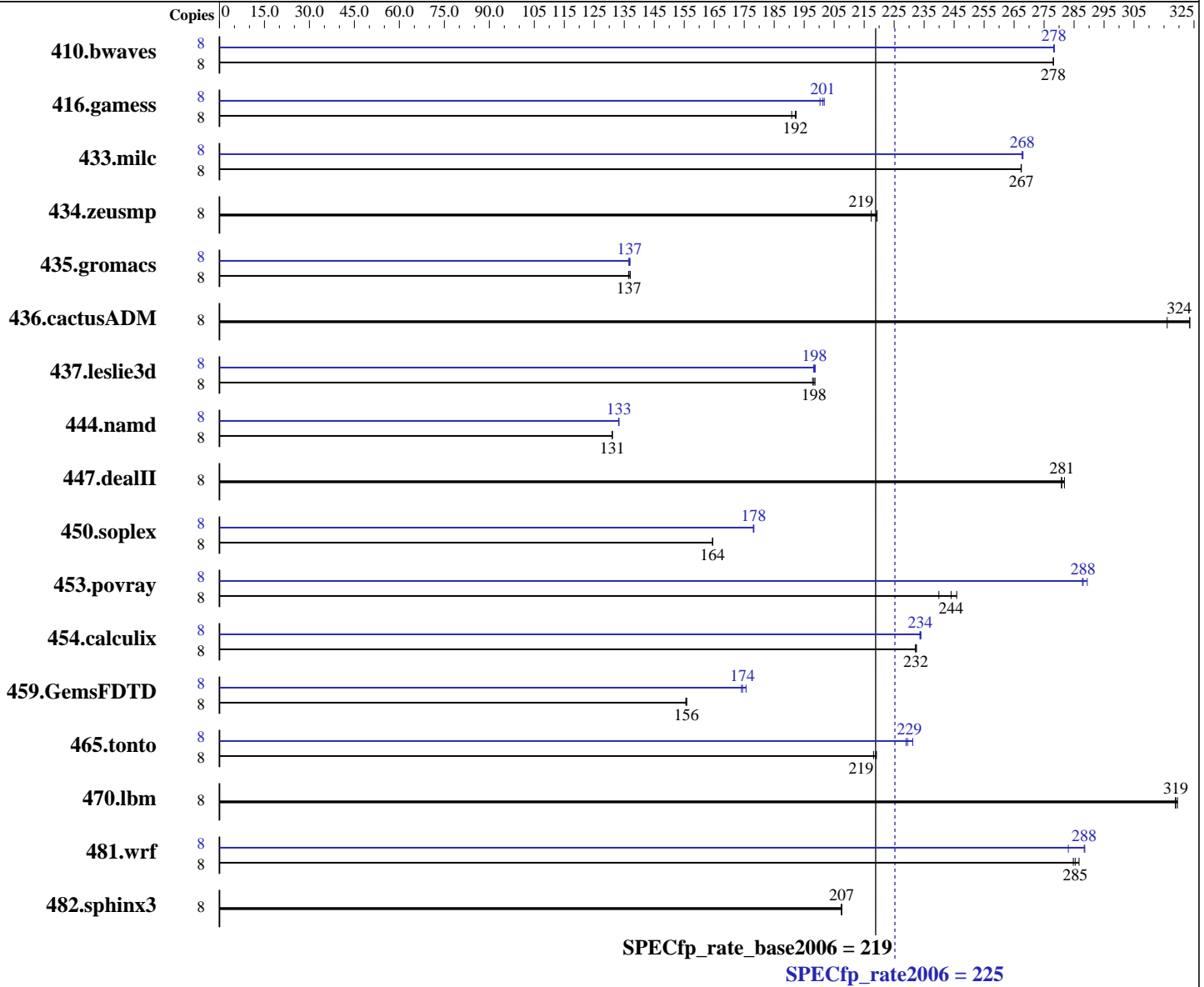
Test date: Apr-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2609  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.19-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)  
 Disk Subsystem: 2 x 146 GB 15000 RPM SAS, RAID 0  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	391	278	391	278	<b>391</b>	<b>278</b>	8	<b>391</b>	<b>278</b>	390	278	391	278
416.gamess	8	821	191	<b>816</b>	<b>192</b>	814	192	8	<b>778</b>	<b>201</b>	777	202	782	200
433.milc	8	275	267	<b>275</b>	<b>267</b>	275	267	8	274	268	274	268	<b>274</b>	<b>268</b>
434.zeusmp	8	<b>332</b>	<b>219</b>	332	219	335	217	8	<b>332</b>	<b>219</b>	332	219	335	217
435.gromacs	8	419	136	417	137	<b>418</b>	<b>137</b>	8	418	137	<b>418</b>	<b>137</b>	417	137
436.cactusADM	8	<b>295</b>	<b>324</b>	302	316	295	324	8	<b>295</b>	<b>324</b>	302	316	295	324
437.leslie3d	8	379	199	380	198	<b>379</b>	<b>198</b>	8	378	199	<b>379</b>	<b>198</b>	380	198
444.namd	8	<b>490</b>	<b>131</b>	490	131	490	131	8	482	133	482	133	<b>482</b>	<b>133</b>
447.dealII	8	<b>326</b>	<b>281</b>	326	281	325	282	8	<b>326</b>	<b>281</b>	326	281	325	282
450.soplex	8	406	164	406	165	<b>406</b>	<b>164</b>	8	375	178	<b>375</b>	<b>178</b>	374	178
453.povray	8	173	246	<b>174</b>	<b>244</b>	177	240	8	147	289	<b>148</b>	<b>288</b>	148	288
454.calculix	8	<b>284</b>	<b>232</b>	284	232	284	232	8	283	234	282	234	<b>282</b>	<b>234</b>
459.GemsFDTD	8	<b>545</b>	<b>156</b>	545	156	546	156	8	488	174	483	176	<b>487</b>	<b>174</b>
465.tonto	8	<b>360</b>	<b>219</b>	359	219	361	218	8	340	231	<b>343</b>	<b>229</b>	344	229
470.lbm	8	344	320	345	319	<b>345</b>	<b>319</b>	8	344	320	345	319	<b>345</b>	<b>319</b>
481.wrf	8	312	287	<b>313</b>	<b>285</b>	314	285	8	310	289	<b>310</b>	<b>288</b>	316	283
482.sphinx3	8	<b>752</b>	<b>207</b>	751	207	752	207	8	<b>752</b>	<b>207</b>	751	207	752	207

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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## Platform Notes

System Profile set to Custom  
 CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on unsvr Fri Apr 13 20:12:24 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal: 132089860 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsc_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux unsvr 3.0.13-0.19-default #1 SMP Fri Feb 3 15:38:23 UTC 2012 (7f256ae)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 13 11:26 last=S

```
SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext3  265G   68G  183G  27% /
```

Additional information from dmidecode:

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
The Dell PowerEdge R620 and the Bull NovaScale R440 F3 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R620 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

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## Peak Portability Flags (Continued)

```

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
         -opt-mem-layout-trans=3

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

```

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -static

```

```

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
          -inline-level=0 -scalar-rep- -static

```

434.zeusmp: basepeak = yes

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## Peak Optimization Flags (Continued)

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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