



SPEC® CINT2006 Result

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Supermicro

SPECint®2006 = 30.9

SuperServer 5017C-MF (X9SCL-F, Intel G530)

SPECint_base2006 = 29.4

CPU2006 license: 001176

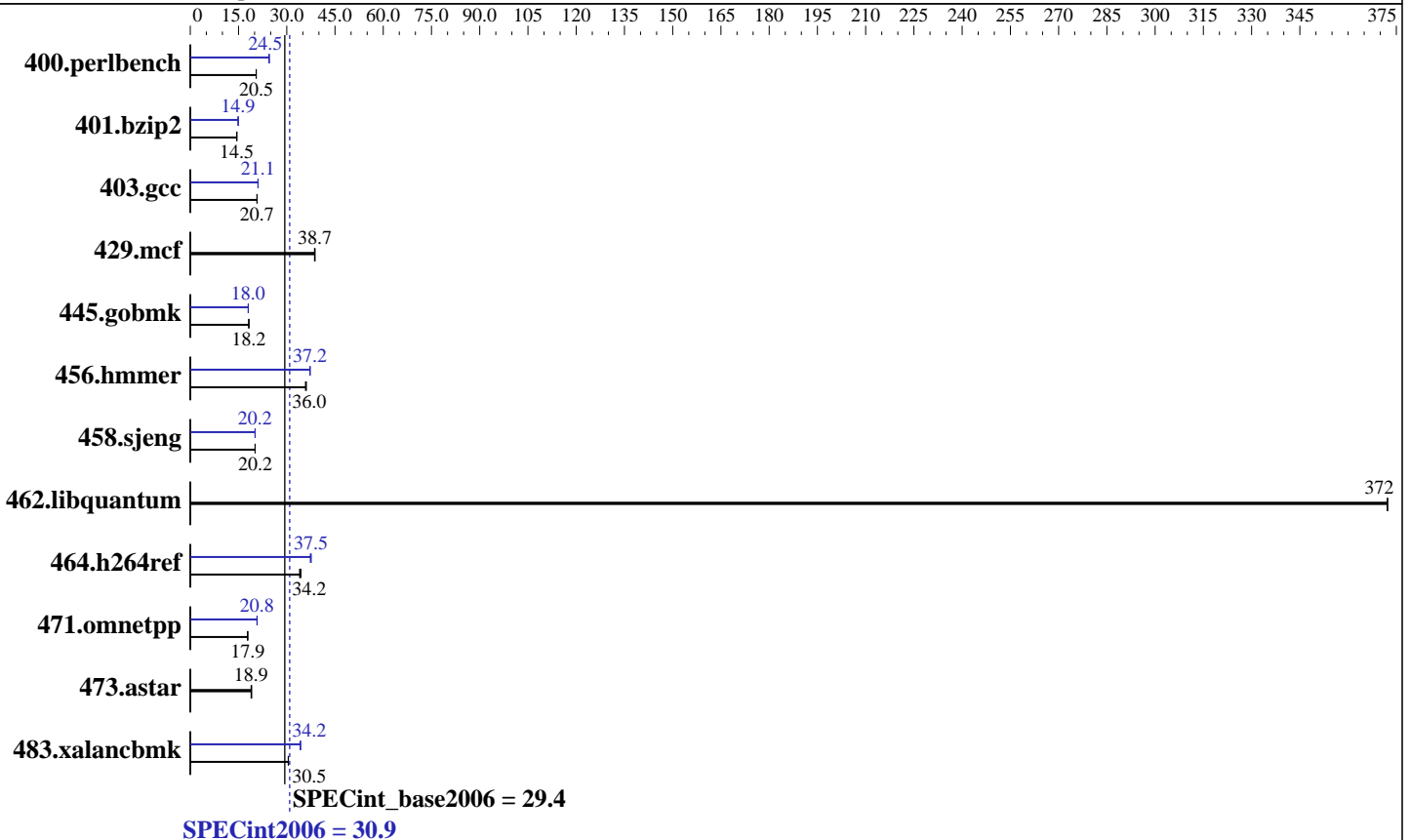
Test date: Apr-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Celeron G530
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	477	20.5	476	20.5	<u>476</u>	<u>20.5</u>	398	24.6	399	24.5	<u>398</u>	<u>24.5</u>
401.bzip2	669	14.4	666	14.5	<u>667</u>	<u>14.5</u>	647	14.9	649	14.9	<u>647</u>	<u>14.9</u>
403.gcc	<u>388</u>	<u>20.7</u>	387	20.8	388	20.7	383	21.0	<u>382</u>	<u>21.1</u>	382	21.1
429.mcf	<u>236</u>	<u>38.7</u>	235	38.8	236	38.6	<u>236</u>	<u>38.7</u>	235	38.8	236	38.6
445.gobmk	<u>576</u>	<u>18.2</u>	576	18.2	576	18.2	<u>582</u>	<u>18.0</u>	582	18.0	582	18.0
456.hammer	261	35.8	259	36.1	<u>259</u>	<u>36.0</u>	250	37.3	<u>251</u>	<u>37.2</u>	251	37.2
458.sjeng	<u>600</u>	<u>20.2</u>	599	20.2	601	20.1	<u>600</u>	<u>20.2</u>	600	20.2	601	20.1
462.libquantum	55.7	372	<u>55.7</u>	<u>372</u>	55.7	372	<u>55.7</u>	<u>372</u>	<u>55.7</u>	<u>372</u>	55.7	372
464.h264ref	651	34.0	<u>648</u>	<u>34.2</u>	643	34.4	<u>589</u>	<u>37.5</u>	593	37.3	589	37.6
471.omnetpp	349	17.9	<u>350</u>	<u>17.9</u>	352	17.8	300	20.8	302	20.7	<u>301</u>	<u>20.8</u>
473.astar	<u>371</u>	<u>18.9</u>	367	19.1	371	18.9	<u>371</u>	<u>18.9</u>	367	19.1	371	18.9
483.xalancbmk	226	30.5	226	30.5	<u>226</u>	<u>30.5</u>	202	34.2	<u>202</u>	<u>34.2</u>	201	34.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Fri Apr 6 20:34:44 2012
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Celeron(R) CPU G530 @ 2.40GHz
1 "physical id"s (chips)
2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 2
siblings : 2
physical 0: cores 0 1
cache size : 2048 KB
```

```
From /proc/meminfo
MemTotal: 8040724 kB
HugePages_Total: 0
```

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Platform Notes (Continued)

Hugepagesize: 2048 kB

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 6 20:34

```
SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   35G   13G   75% /
```

Additional information from dmidecode:

```
Memory:
2x Micron 18JSF51272AZ-1G4D1 4 GB 1067 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
           -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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