SPEC® CINT2006 Result

Supermicro

SuperServer 1017C-TF (X9SCL-F, Intel i3-2120)

SPECint®_rate2006 = 90.8

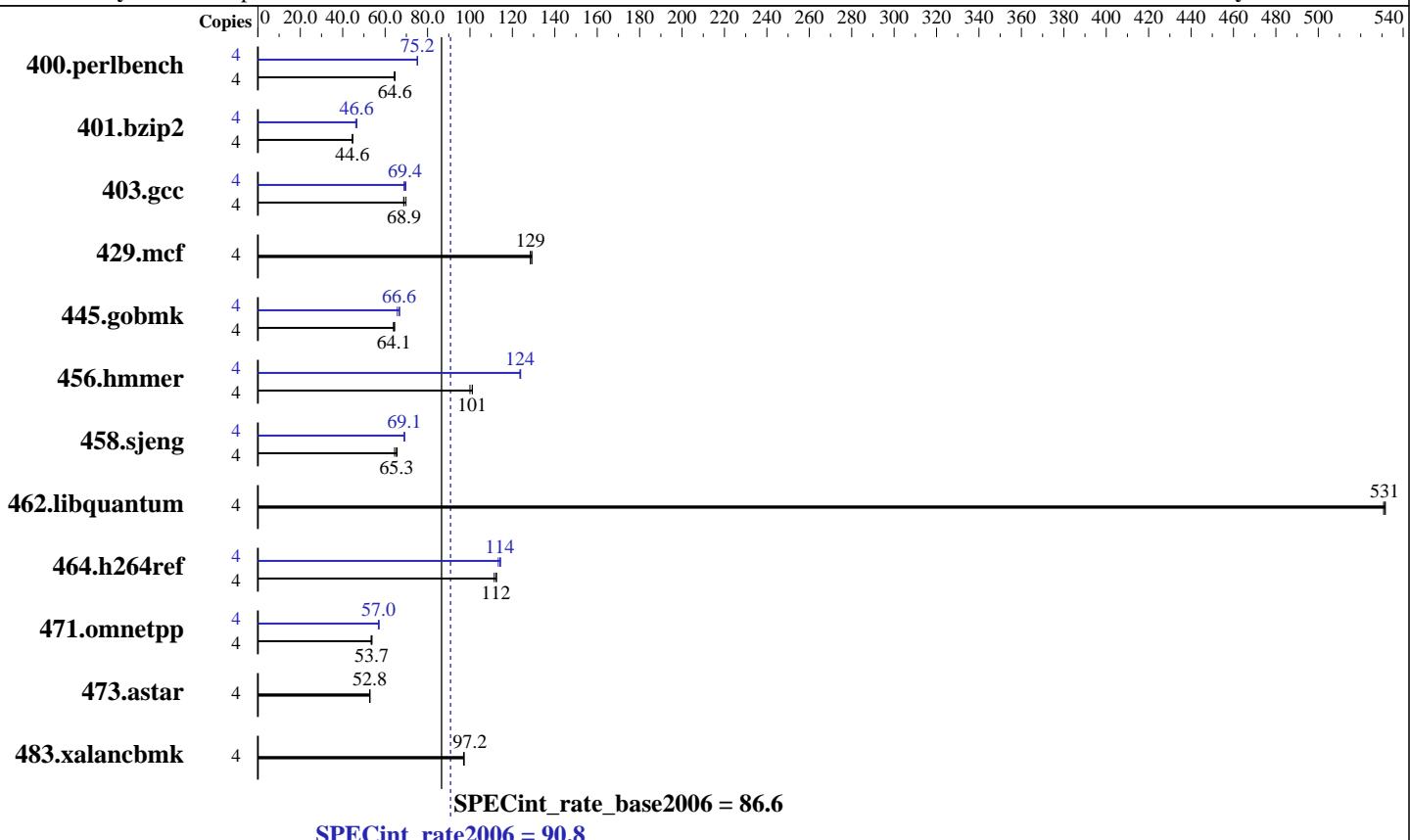SPECint_rate_base2006 = 86.6

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2012
Hardware Availability: Apr-2011
Software Availability: Oct-2011

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Hardware

CPU Name:	Intel Core i3-2120
CPU Characteristics:	
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem:	1 x 500 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 (Santiago) 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**605**	**64.6**	608	64.3	604	64.7	4	520	75.2	519	75.3	**520**	**75.2**
401.bzip2	4	866	44.6	865	44.6	**865**	**44.6**	4	**827**	**46.6**	827	46.7	837	46.1
403.gcc	4	461	69.8	468	68.7	**467**	**68.9**	4	**464**	**69.4**	467	68.9	461	69.8
429.mcf	4	**284**	**129**	284	128	282	129	4	**284**	**129**	284	128	282	129
445.gobmk	4	**654**	**64.1**	657	63.9	650	64.5	4	628	66.8	638	65.7	**630**	**66.6**
456.hmmer	4	373	100	**369**	**101**	369	101	4	301	124	302	124	**302**	**124**
458.sjeng	4	751	64.5	**741**	**65.3**	737	65.6	4	700	69.1	701	69.0	**700**	**69.1**
462.libquantum	4	156	532	156	531	**156**	**531**	4	156	532	156	531	**156**	**531**
464.h264ref	4	**788**	**112**	794	111	787	113	4	774	114	781	113	**776**	**114**
471.omnetpp	4	465	53.8	468	53.4	**466**	**53.7**	4	438	57.1	**439**	**57.0**	440	56.8
473.astar	4	533	52.7	532	52.8	**532**	**52.8**	4	533	52.7	532	52.8	**532**	**52.8**
483.xalancbmk	4	**284**	**97.2**	284	97.2	285	97.0	4	**284**	**97.2**	284	97.2	285	97.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    Sysinfo program /home/usr/cpu2006/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on localhost.localdomain Fri Mar 30 20:48:04 2012

    This section contains SUT (System Under Test) info as seen by
    some common utilities. To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Core(TM) i3-2120 CPU @ 3.30GHz
          1 "physical id"s (chips)
          4 "processors"
       cores, siblings (Caution: counting these is hw and system dependent. The
       following excerpts from /proc/cpuinfo might not be reliable. Use with
       caution.)
          cpu cores : 2
          siblings : 4

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Platform Notes (Continued)

```
     physical 0: cores 0 1
   cache size : 3072 KB

 From /proc/meminfo
   MemTotal:        8040476 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

 /usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.1 (Santiago)

 From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
   Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
   15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Mar 30 20:44

 SPEC is set to: /home/usr/cpu2006
   Filesystem     Type     Size  Used Avail Use% Mounted on
   /dev/mapper/VolGroup-lv_home
                  ext4     392G   18G  355G   5% /home

 Additional information from dmidecode:
   Memory:
     2x Hynix Semiconduc HMT351U7BFR8C-H9 4 GB 1333 MHz 2 rank

 (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/usr/cpu2006/libs/32:/home/usr/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
   icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

   401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias

     403.gcc: -xAVX -ipo -O3 -no-prec-div

     429.mcf: basepeak = yes

   445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

   456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap

     473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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