SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX300 S7, Intel Xeon E5-2637, 3.0 GHz

SPECint®_rate2006 = 101

SPECint_rate_base2006 = 96.7

CPU2006 license: 19	**Test date:** Jan-2012
Test sponsor: Fujitsu	**Hardware Availability:** Mar-2012
Tested by: Fujitsu	**Software Availability:** Dec-2011

Copies

Benchmark	Copies	Value
400.perlbench	4	82.1
400.perlbench	4	68.5
401.bzip2	4	54.1
401.bzip2	4	52.4
403.gcc	4	78.6
429.mcf	4	153
445.gobmk	4	71.4
445.gobmk	4	70.3
456.hmmer	4	141
456.hmmer	4	117
458.sjeng	4	73.3
458.sjeng	4	69.8
462.libquantum	4	558
464.h264ref	4	123
464.h264ref	4	121
471.omnetpp	4	65.4
471.omnetpp	4	61.4
473.astar	4	61.0
483.xalancbmk	4	106

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Hardware

CPU Name:	Intel Xeon E5-2637
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	5 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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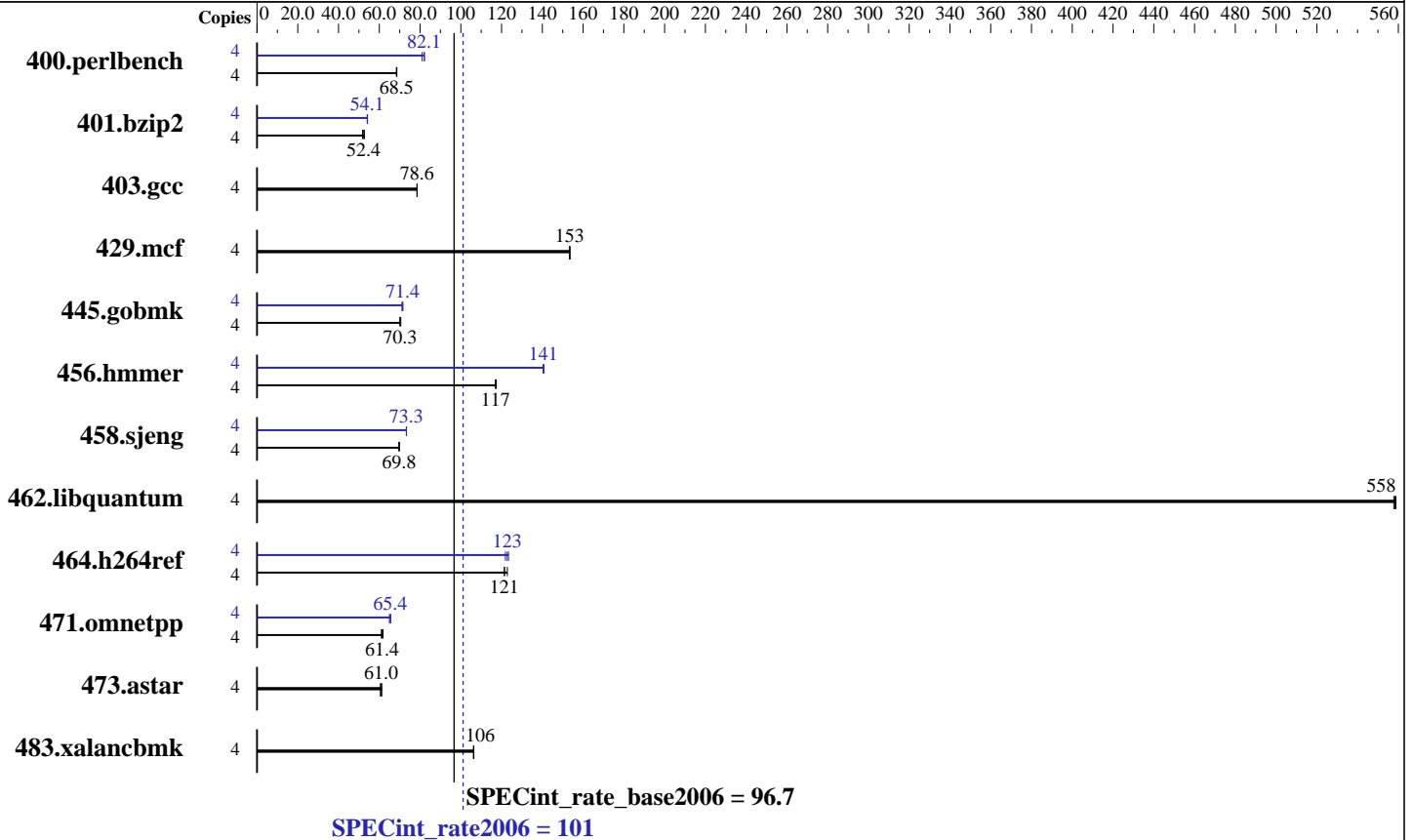
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	570	68.6	571	68.4	**571**	**68.5**		4	**476**	**82.1**	483	81.0	476	82.1
401.bzip2	4	733	52.7	**737**	**52.4**	746	51.8		4	712	54.2	714	54.1	**713**	**54.1**
403.gcc	4	410	78.6	**410**	**78.6**	410	78.5		4	410	78.6	**410**	**78.6**	410	78.5
429.mcf	4	238	153	**238**	**153**	238	154		4	238	153	**238**	**153**	238	154
445.gobmk	4	595	70.5	**597**	**70.3**	598	70.1		4	586	71.6	590	71.1	**588**	**71.4**
456.hmmer	4	318	117	319	117	**319**	**117**		4	**266**	**141**	266	140	265	141
458.sjeng	4	693	69.8	695	69.6	**694**	**69.8**		4	**660**	**73.3**	661	73.3	660	73.3
462.libquantum	4	149	558	148	559	**148**	**558**		4	149	558	148	559	**148**	**558**
464.h264ref	4	721	123	729	121	**729**	**121**		4	726	122	717	123	**721**	**123**
471.omnetpp	4	**407**	**61.4**	404	61.9	410	61.0		4	**383**	**65.4**	385	64.9	381	65.6
473.astar	4	464	60.5	459	61.2	**460**	**61.0**		4	464	60.5	459	61.2	**460**	**61.0**
483.xalancbmk	4	259	106	**260**	**106**	260	106		4	259	106	**260**	**106**	260	106

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
      icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

       401.bzip2: icc -m64

      456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

     403.gcc: basepeak = yes

     429.mcf: basepeak = yes

  445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

  456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap

    473.astar: basepeak = yes
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For other inquiries, please contact webmaster@spec.org.

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