



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®\_rate2006 = 88.9**

### Acer AR320 F2 (Intel Core i3-2120)

**SPECint\_rate\_base2006 = 85.0**

CPU2006 license: 97

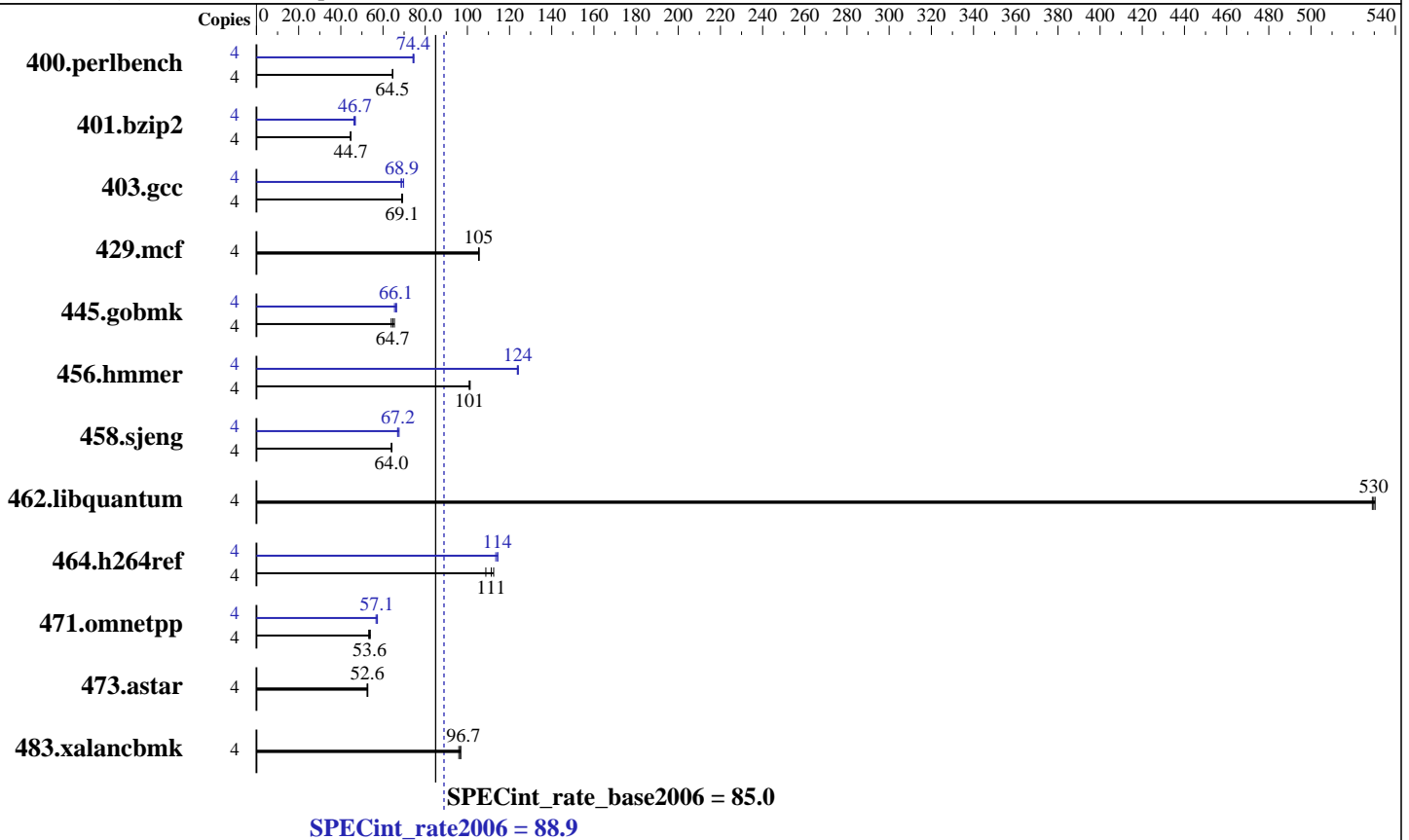
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2011

Hardware Availability: Jan-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Core i3-2120  
 CPU Characteristics:  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>606</b>	<b>64.5</b>	606	64.5	604	64.7	4	523	74.7	<b>525</b>	<b>74.4</b>	527	74.2
401.bzip2	4	866	44.6	<b>863</b>	<b>44.7</b>	863	44.7	4	823	46.9	<b>826</b>	<b>46.7</b>	835	46.2
403.gcc	4	<b>466</b>	<b>69.1</b>	468	68.9	465	69.3	4	470	68.6	<b>467</b>	<b>68.9</b>	461	69.8
429.mcf	4	345	106	346	105	<b>346</b>	<b>105</b>	4	345	106	346	105	<b>346</b>	<b>105</b>
445.gobmk	4	657	63.9	<b>649</b>	<b>64.7</b>	641	65.4	4	<b>635</b>	<b>66.1</b>	631	66.4	641	65.5
456.hmmer	4	<b>369</b>	<b>101</b>	370	101	368	101	4	<b>301</b>	<b>124</b>	302	124	301	124
458.sjeng	4	753	64.2	757	63.9	<b>756</b>	<b>64.0</b>	4	716	67.6	723	66.9	<b>721</b>	<b>67.2</b>
462.libquantum	4	156	530	<b>156</b>	<b>530</b>	157	529	4	156	530	<b>156</b>	<b>530</b>	157	529
464.h264ref	4	<b>795</b>	<b>111</b>	787	112	814	109	4	780	113	<b>774</b>	<b>114</b>	774	114
471.omnetpp	4	470	53.2	463	54.0	<b>466</b>	<b>53.6</b>	4	441	56.7	<b>438</b>	<b>57.1</b>	436	57.4
473.astar	4	532	52.8	<b>533</b>	<b>52.6</b>	535	52.5	4	532	52.8	<b>533</b>	<b>52.6</b>	535	52.5
483.xalancbmk	4	285	96.8	288	96.0	<b>285</b>	<b>96.7</b>	4	285	96.8	288	96.0	<b>285</b>	<b>96.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Thu Dec 29 01:37:41 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Core(TM) i3-2120 CPU @ 3.30GHz  
1 "physical id"s (chips)  
4 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 2  
siblings : 4

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## Platform Notes (Continued)

```
physical 0: cores 0 1
cache size : 3072 KB
```

```
From /proc/meminfo
MemTotal:      8046960 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 29 01:37
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   23G   25G  48% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzp2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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