



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 295

SuperServer 5027R-WRF (X9SRW-F, Intel E5-1660)

SPECint_rate_base2006 = 282

CPU2006 license: 001176

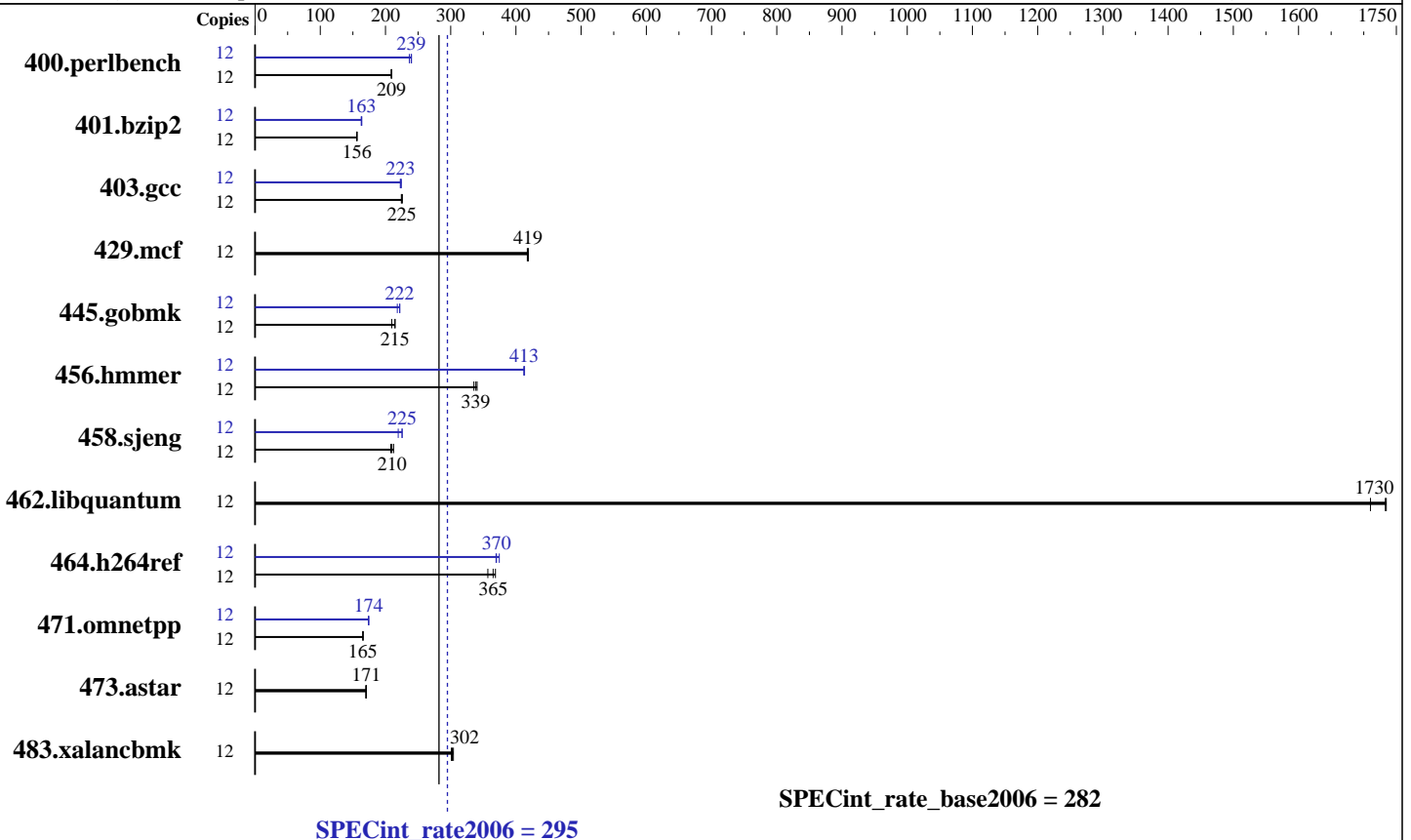
Test date: Mar-2012

Test sponsor: Supermicro

Hardware Availability: Mar-2012

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E5-1660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|------------|-------------|------------|------------|--------|------------|------------|------------|-------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 12 | <u>561</u> | <u>209</u> | 560 | 209 | 561 | 209 | 12 | 489 | 240 | <u>490</u> | <u>239</u> | 496 | 237 |
| 401.bzip2 | 12 | <u>741</u> | <u>156</u> | 740 | 157 | 743 | 156 | 12 | <u>710</u> | <u>163</u> | 707 | 164 | 711 | 163 |
| 403.gcc | 12 | 428 | 226 | <u>429</u> | <u>225</u> | 431 | 224 | 12 | 431 | 224 | <u>433</u> | <u>223</u> | 434 | 223 |
| 429.mcf | 12 | 262 | 417 | <u>261</u> | <u>419</u> | 261 | 419 | 12 | 262 | 417 | <u>261</u> | <u>419</u> | 261 | 419 |
| 445.gobmk | 12 | 601 | 210 | <u>587</u> | <u>215</u> | 587 | 215 | 12 | 577 | 218 | <u>567</u> | <u>222</u> | 567 | 222 |
| 456.hammer | 12 | 334 | 336 | 329 | 340 | <u>331</u> | <u>339</u> | 12 | <u>271</u> | <u>413</u> | 272 | 412 | 271 | 413 |
| 458.sjeng | 12 | <u>692</u> | <u>210</u> | 698 | 208 | 683 | 212 | 12 | 644 | 226 | <u>644</u> | <u>225</u> | 661 | 220 |
| 462.libquantum | 12 | 145 | 1710 | <u>143</u> | <u>1730</u> | 143 | 1730 | 12 | 145 | 1710 | <u>143</u> | <u>1730</u> | 143 | 1730 |
| 464.h264ref | 12 | <u>727</u> | <u>365</u> | 720 | 369 | 744 | 357 | 12 | <u>717</u> | <u>370</u> | 719 | 370 | 709 | 375 |
| 471.omnetpp | 12 | 452 | 166 | 454 | 165 | <u>454</u> | <u>165</u> | 12 | 430 | 174 | <u>430</u> | <u>174</u> | 431 | 174 |
| 473.astar | 12 | 493 | 171 | <u>494</u> | <u>171</u> | 497 | 170 | 12 | 493 | 171 | <u>494</u> | <u>171</u> | 497 | 170 |
| 483.xalancbmk | 12 | 275 | 301 | <u>274</u> | <u>302</u> | 272 | 304 | 12 | 275 | 301 | <u>274</u> | <u>302</u> | 272 | 304 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzp2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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