



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

### SPECfp<sup>®</sup>\_rate2006 = 114

PRIMERGY BX924 S3, Intel Xeon E5-2609, 2.40 GHz

### SPECfp\_rate\_base2006 = 111

CPU2006 license: 19

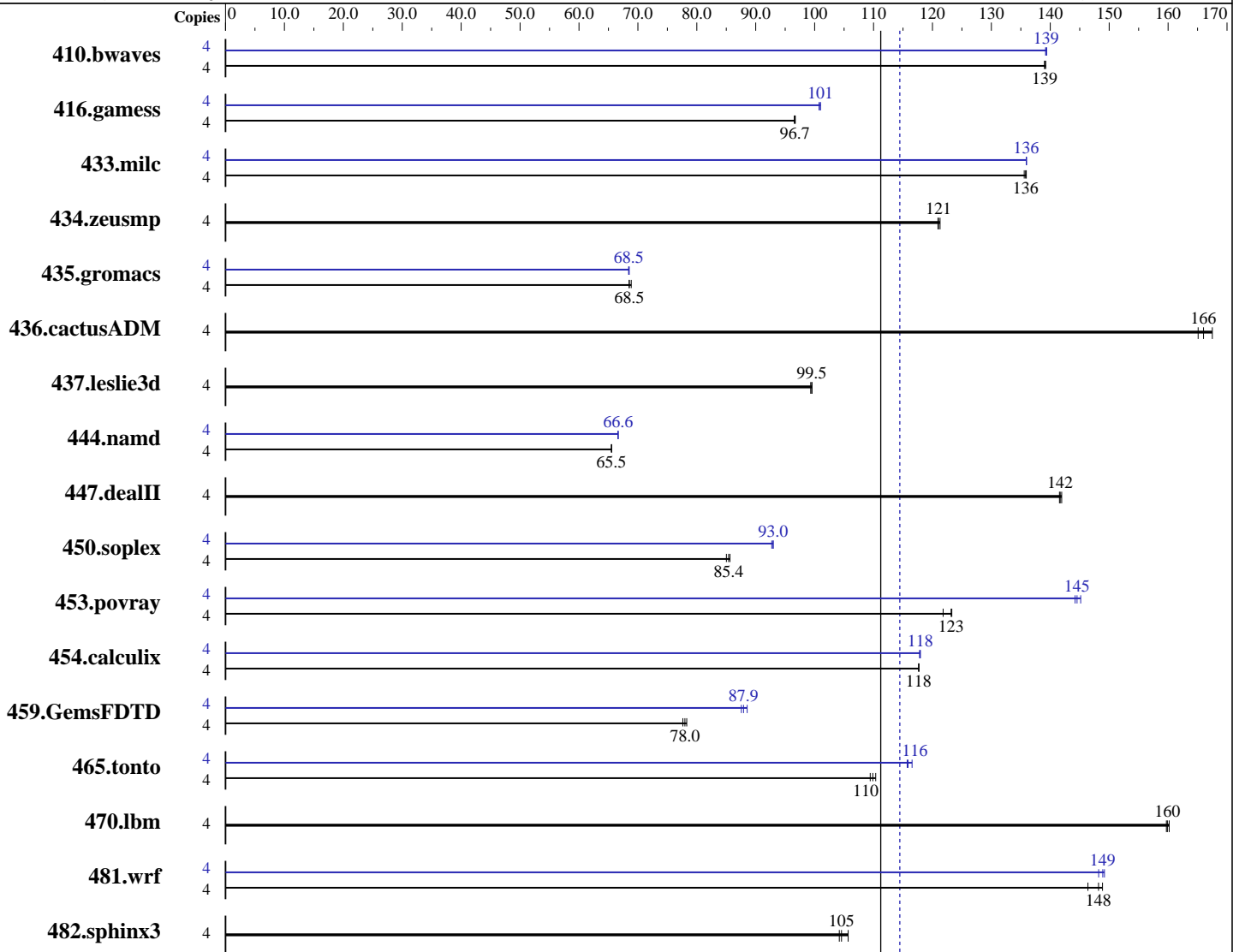
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



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### Hardware

CPU Name: Intel Xeon E5-2609  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1067 MHz and CL7)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: PRIMERGY SX940 S1 for hw\_disk

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	391	139	390	139	<b><u>391</u></b>	<b><u>139</u></b>	4	391	139	<b><u>390</u></b>	<b><u>139</u></b>	390	139		
416.gamess	4	811	96.5	810	96.7	<b><u>810</u></b>	<b><u>96.7</u></b>	4	777	101	<b><u>776</u></b>	<b><u>101</u></b>	776	101		
433.milc	4	<b><u>270</u></b>	<b><u>136</u></b>	270	136	271	136	4	270	136	<b><u>270</u></b>	<b><u>136</u></b>	270	136		
434.zeusmp	4	300	121	<b><u>301</u></b>	<b><u>121</u></b>	301	121	4	300	121	<b><u>301</u></b>	<b><u>121</u></b>	301	121		
435.gromacs	4	417	68.5	<b><u>417</u></b>	<b><u>68.5</u></b>	415	68.9	4	<b><u>417</u></b>	<b><u>68.5</u></b>	417	68.4	417	68.5		
436.cactusADM	4	<b><u>288</u></b>	<b><u>166</u></b>	285	168	290	165	4	<b><u>288</u></b>	<b><u>166</u></b>	285	168	290	165		
437.leslie3d	4	379	99.3	378	99.5	<b><u>378</u></b>	<b><u>99.5</u></b>	4	379	99.3	378	99.5	<b><u>378</u></b>	<b><u>99.5</u></b>		
444.namd	4	490	65.5	490	65.5	<b><u>490</u></b>	<b><u>65.5</u></b>	4	481	66.6	481	66.6	<b><u>481</u></b>	<b><u>66.6</u></b>		
447.dealII	4	<b><u>323</u></b>	<b><u>142</u></b>	323	142	322	142	4	<b><u>323</u></b>	<b><u>142</u></b>	323	142	322	142		
450.soplex	4	392	85.0	390	85.6	<b><u>391</u></b>	<b><u>85.4</u></b>	4	360	92.7	359	93.0	<b><u>359</u></b>	<b><u>93.0</u></b>		
453.povray	4	173	123	175	122	<b><u>173</u></b>	<b><u>123</u></b>	4	147	145	148	144	<b><u>147</u></b>	<b><u>145</u></b>		
454.calculix	4	280	118	<b><u>280</u></b>	<b><u>118</u></b>	280	118	4	280	118	280	118	<b><u>280</u></b>	<b><u>118</u></b>		
459.GemsFDTD	4	<b><u>544</u></b>	<b><u>78.0</u></b>	542	78.3	547	77.6	4	479	88.6	<b><u>483</u></b>	<b><u>87.9</u></b>	485	87.6		
465.tonto	4	357	110	<b><u>358</u></b>	<b><u>110</u></b>	360	109	4	340	116	<b><u>340</u></b>	<b><u>116</u></b>	338	117		
470.lbm	4	<b><u>344</u></b>	<b><u>160</u></b>	343	160	344	160	4	<b><u>344</u></b>	<b><u>160</u></b>	343	160	344	160		
481.wrf	4	300	149	305	146	<b><u>302</u></b>	<b><u>148</u></b>	4	299	149	301	148	<b><u>300</u></b>	<b><u>149</u></b>		
482.sphinx3	4	<b><u>746</u></b>	<b><u>105</u></b>	738	106	748	104	4	<b><u>746</u></b>	<b><u>105</u></b>	738	106	748	104		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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