SPEC® CFP2006 Result

ACTION S.A.

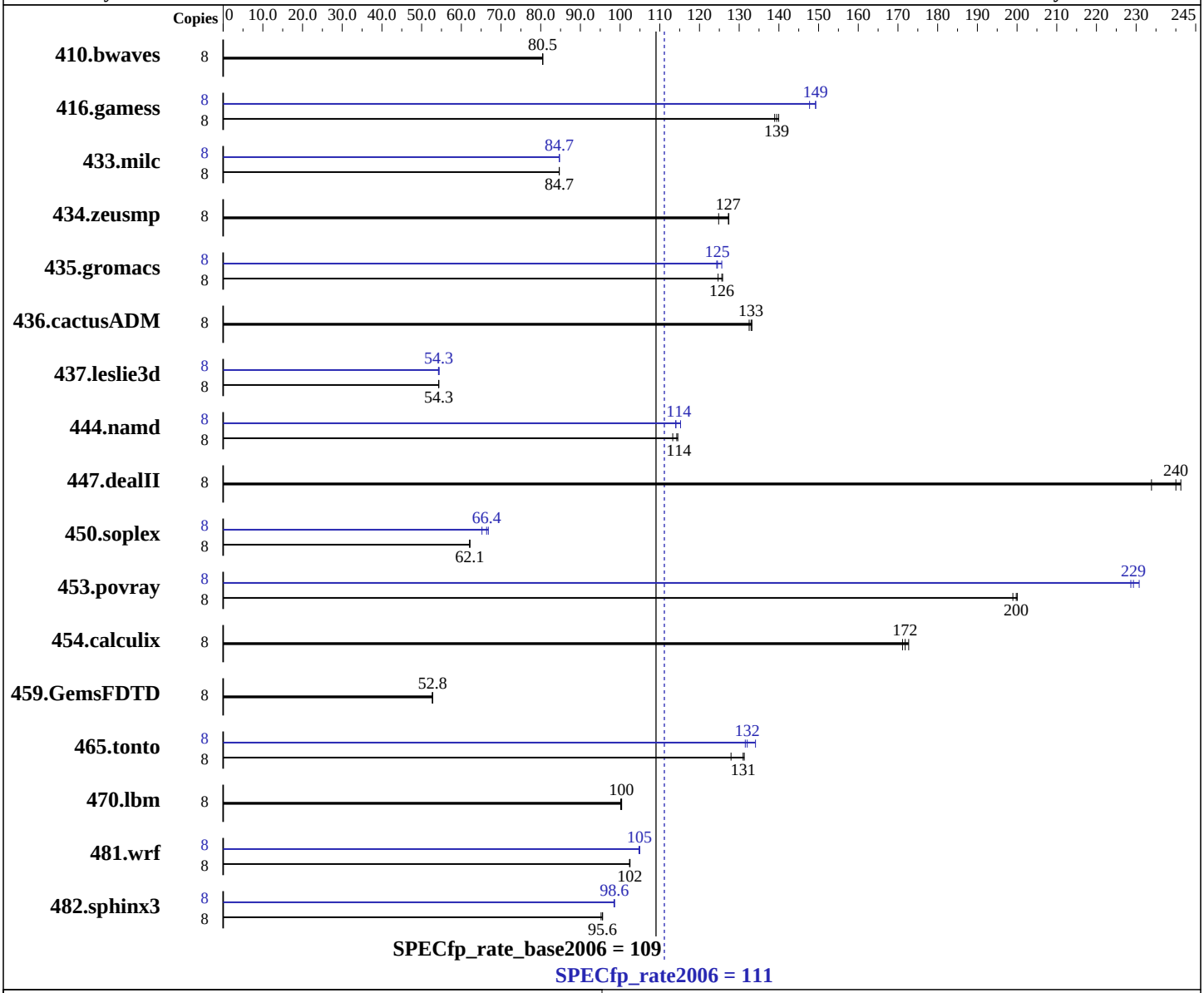
ACTINA SOLAR 100 S5 (Intel Xeon E3-1240)

SPECfp®_rate2006 = 111

SPECfp_rate_base2006 = 109

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jan-2012
Hardware Availability: Apr-2011
Software Availability: Oct-2011



Hardware

CPU Name:	Intel Xeon E3-1240
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 (Santiago) 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 320 GB SATA II, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1350	80.5	1351	80.5	**1351**	**80.5**	8	1350	80.5	1351	80.5	**1351**	**80.5**
416.gamess	8	1127	139	**1123**	**139**	1119	140	8	1060	148	**1050**	**149**	1049	149
433.milc	8	**867**	**84.7**	867	84.7	867	84.7	8	**867**	**84.7**	867	84.7	867	84.7
434.zeusmp	8	583	125	571	127	**572**	**127**	8	583	125	571	127	**572**	**127**
435.gromacs	8	**455**	**126**	454	126	458	125	8	455	126	**459**	**125**	459	124
436.cactusADM	8	**719**	**133**	721	133	718	133	8	**719**	**133**	721	133	718	133
437.leslie3d	8	1385	54.3	**1385**	**54.3**	1384	54.3	8	1386	54.2	1382	54.4	**1385**	**54.3**
444.namd	8	**561**	**114**	566	113	560	115	8	563	114	**562**	**114**	557	115
447.dealII	8	**381**	**240**	379	241	391	234	8	**381**	**240**	379	241	391	234
450.soplex	8	**1074**	**62.1**	1073	62.2	1075	62.1	8	1023	65.2	**1005**	**66.4**	999	66.8
453.povray	8	214	199	**213**	**200**	213	200	8	**186**	**229**	184	231	186	229
454.calculix	8	386	171	**384**	**172**	382	173	8	386	171	**384**	**172**	382	173
459.GemsFDTD	8	1608	52.8	**1609**	**52.8**	1611	52.7	8	1608	52.8	**1609**	**52.8**	1611	52.7
465.tonto	8	600	131	**601**	**131**	615	128	8	**596**	**132**	598	132	587	134
470.lbm	8	1095	100	**1095**	**100**	1098	100	8	1095	100	**1095**	**100**	1098	100
481.wrf	8	873	102	872	103	**872**	**102**	8	853	105	852	105	**852**	**105**
482.sphinx3	8	1638	95.2	**1631**	**95.6**	1631	95.6	8	1584	98.5	**1582**	**98.6**	1581	98.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

447.dealII: `basepeak = yes`

450.soplex: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3`

453.povray: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -inline-level=0 -scalar-rep- -static`

434.zeusmp: `basepeak = yes`

437.leslie3d: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `basepeak = yes`

481.wrf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 02:13:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 February 2012.