



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 141

ACTINA SOLAR 100 S5 (Intel Xeon E3-1220)

SPECint_rate_base2006 = 136

CPU2006 license: 9008

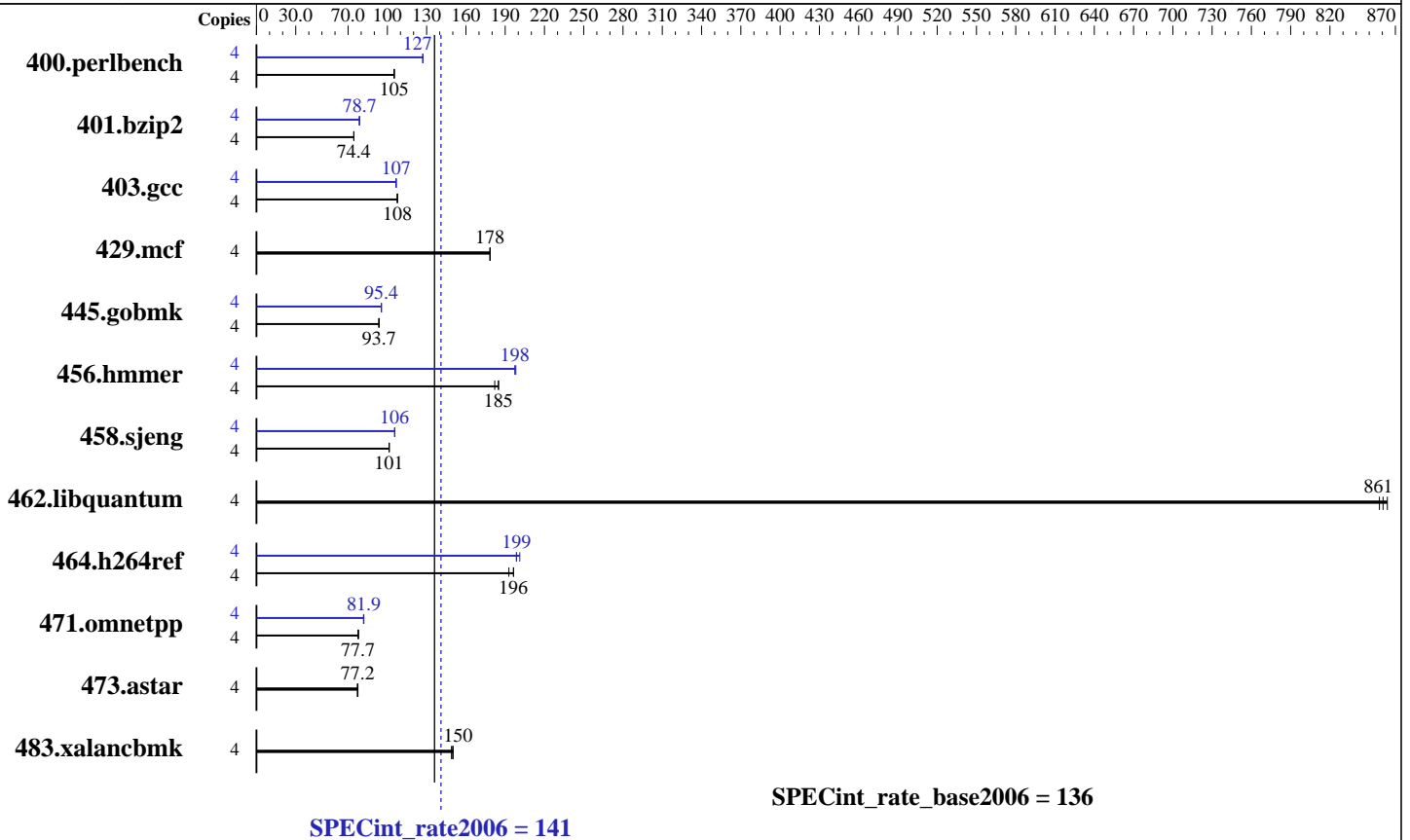
Test date: Feb-2012

Test sponsor: ACTION S.A.

Hardware Availability: Apr-2011

Tested by: ACTION S.A.

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 320 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	372	105	371	105	<u>371</u>	<u>105</u>	4	308	127	307	127	<u>307</u>	<u>127</u>
401.bzip2	4	<u>519</u>	<u>74.4</u>	518	74.5	519	74.3	4	<u>490</u>	<u>78.7</u>	492	78.5	489	78.9
403.gcc	4	299	108	299	108	<u>299</u>	<u>108</u>	4	301	107	302	107	<u>302</u>	<u>107</u>
429.mcf	4	205	178	<u>204</u>	<u>178</u>	204	179	4	205	178	<u>204</u>	<u>178</u>	204	179
445.gobmk	4	448	93.7	<u>448</u>	<u>93.7</u>	449	93.4	4	440	95.4	440	95.4	<u>440</u>	<u>95.4</u>
456.hammer	4	202	185	<u>202</u>	<u>185</u>	205	182	4	<u>189</u>	<u>198</u>	189	197	188	198
458.sjeng	4	<u>477</u>	<u>101</u>	478	101	477	101	4	458	106	<u>458</u>	<u>106</u>	459	106
462.libquantum	4	<u>96.3</u>	<u>861</u>	95.9	864	96.6	858	4	<u>96.3</u>	<u>861</u>	95.9	864	96.6	858
464.h264ref	4	459	193	451	196	<u>451</u>	<u>196</u>	4	440	201	<u>446</u>	<u>199</u>	446	199
471.omnetpp	4	321	78.0	<u>322</u>	<u>77.7</u>	322	77.7	4	305	82.1	<u>305</u>	<u>81.9</u>	306	81.7
473.astar	4	362	77.5	364	77.1	<u>364</u>	<u>77.2</u>	4	362	77.5	364	77.1	<u>364</u>	<u>77.2</u>
483.xalancbmk	4	184	150	<u>184</u>	<u>150</u>	185	149	4	184	150	<u>184</u>	<u>150</u>	185	149

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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