



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 426

Servidor Itautec MX214 (Intel Xeon X5680)

SPECint\_rate\_base2006 = 410

CPU2006 license: 9001

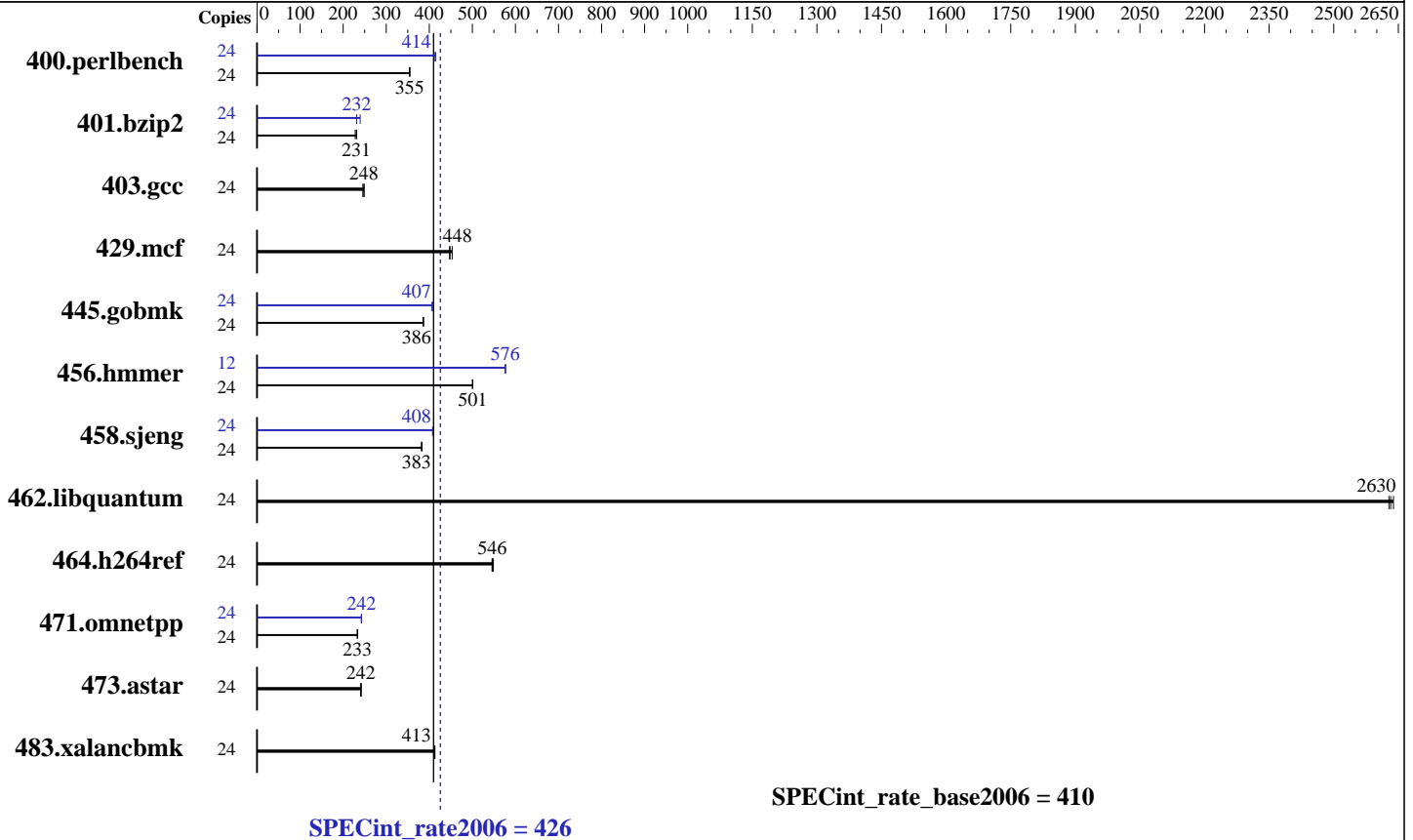
Test date: Dec-2011

Test sponsor: Itautec

Hardware Availability: Jul-2011

Tested by: Itautec

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 146 GB, SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 6.2  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE  
 Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>661</b>	<b>355</b>	661	354	661	355	24	566	414	<b>566</b>	<b>414</b>	566	414
401.bzip2	24	1016	228	<b>1003</b>	<b>231</b>	1002	231	24	967	239	<b>1000</b>	<b>232</b>	1001	231
403.gcc	24	786	246	<b>779</b>	<b>248</b>	777	249	24	786	246	<b>779</b>	<b>248</b>	777	249
429.mcf	24	<b>489</b>	<b>448</b>	483	453	490	447	24	<b>489</b>	<b>448</b>	483	453	490	447
445.gobmk	24	653	386	651	387	<b>652</b>	<b>386</b>	24	617	408	<b>619</b>	<b>407</b>	619	407
456.hammer	24	448	500	447	501	<b>447</b>	<b>501</b>	12	194	576	194	577	<b>194</b>	<b>576</b>
458.sjeng	24	760	382	<b>759</b>	<b>383</b>	759	383	24	712	408	<b>712</b>	<b>408</b>	712	408
462.libquantum	24	<b>189</b>	<b>2630</b>	188	2640	189	2630	24	<b>189</b>	<b>2630</b>	188	2640	189	2630
464.h264ref	24	968	549	973	546	<b>972</b>	<b>546</b>	24	968	549	973	546	<b>972</b>	<b>546</b>
471.omnetpp	24	643	233	<b>643</b>	<b>233</b>	644	233	24	<b>619</b>	<b>242</b>	618	243	619	242
473.astar	24	697	242	<b>698</b>	<b>242</b>	700	241	24	697	242	<b>698</b>	<b>242</b>	700	241
483.xalancbmk	24	403	411	401	413	<b>401</b>	<b>413</b>	24	403	411	401	413	<b>401</b>	<b>413</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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