SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

$SPECint^®\_rate2006 = 435$

Servidor Itautec MX214 (Intel Xeon X5690)

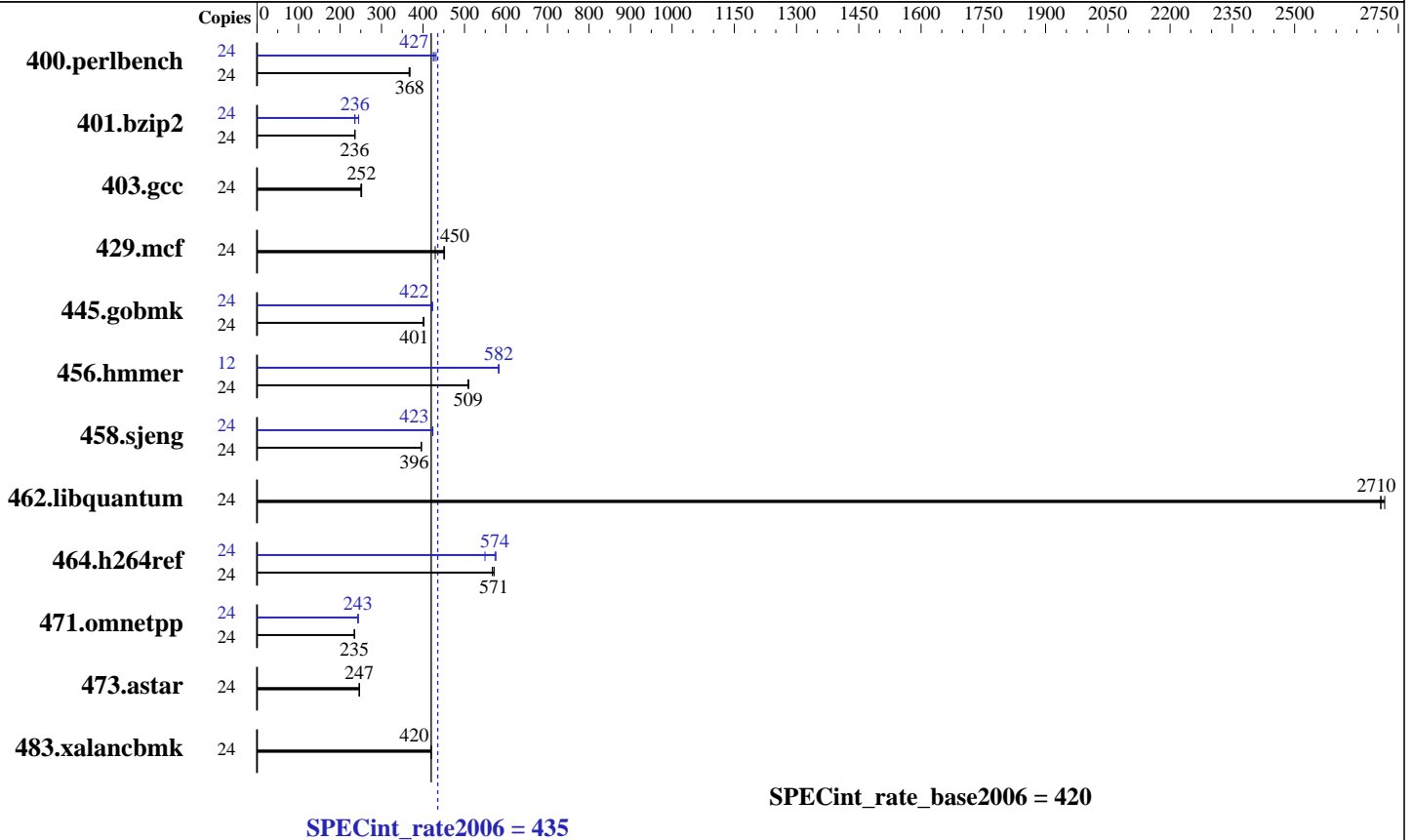$SPECint\_rate\_base2006 = 420$

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Dec-2011

Benchmark results (Copies):

- **400.perlbench** — 24 copies: 427 (peak); 24 copies: 368 (base)
- **401.bzip2** — 24 copies: 236 (peak); 24 copies: 236 (base)
- **403.gcc** — 24 copies: 252
- **429.mcf** — 24 copies: 450
- **445.gobmk** — 24 copies: 422 (peak); 24 copies: 401 (base)
- **456.hmmer** — 12 copies: 582 (peak); 24 copies: 509 (base)
- **458.sjeng** — 24 copies: 423 (peak); 24 copies: 396 (base)
- **462.libquantum** — 24 copies: 2710
- **464.h264ref** — 24 copies: 574 (peak); 24 copies: 571 (base)
- **471.omnetpp** — 24 copies: 243 (peak); 24 copies: 235 (base)
- **473.astar** — 24 copies: 247
- **483.xalancbmk** — 24 copies: 420

SPECint_rate_base2006 = 420
SPECint_rate2006 = 435

Hardware

CPU Name:	Intel Xeon X5690
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3467
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	146 GB, SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint_rate2006 = 435

SPECint_rate_base2006 = 420

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	637	368	640	366	**637**	**368**		24	552	425	**549**	**427**	543	431
401.bzip2	24	**983**	**236**	980	236	983	236		24	945	245	**981**	**236**	984	235
403.gcc	24	766	252	772	250	**767**	**252**		24	766	252	772	250	**767**	**252**
429.mcf	24	510	429	484	452	**486**	**450**		24	510	429	484	452	**486**	**450**
445.gobmk	24	626	402	**627**	**401**	629	400		24	595	423	**597**	**422**	598	421
456.hmmer	24	439	510	**440**	**509**	440	508		12	**192**	**582**	192	583	192	582
458.sjeng	24	733	396	734	395	**734**	**396**		24	**686**	**423**	685	424	686	423
462.libquantum	24	184	2710	**184**	**2710**	183	2720		24	184	2710	**184**	**2710**	183	2720
464.h264ref	24	**930**	**571**	930	571	937	567		24	923	576	**926**	**574**	967	549
471.omnetpp	24	640	234	639	235	**639**	**235**		24	616	243	**616**	**243**	615	244
473.astar	24	682	247	**683**	**247**	685	246		24	682	247	**683**	**247**	685	246
483.xalancbmk	24	395	420	396	419	**395**	**420**		24	395	420	396	419	**395**	**420**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
     Large pages were not enabled for this run

Platform Notes

    Data Reuse disabled in BIOS.

Base Compiler Invocation

C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint_rate2006 = 435

SPECint_rate_base2006 = 420

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Dec-2011

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
```

Itautec

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint_rate2006 = 435

SPECint_rate_base2006 = 420

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Dec-2011

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Itautec

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint_rate2006 = 435

SPECint_rate_base2006 = 420

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 03:46:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 February 2012.