SPEC® CINT2006 Result
2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated
(Test Sponsor: Acer Incorporation)

Acer AR320 F2 (Xeon E3-1280)

SPECint®_rate2006 = 177

SPECint_rate_base2006 = 170
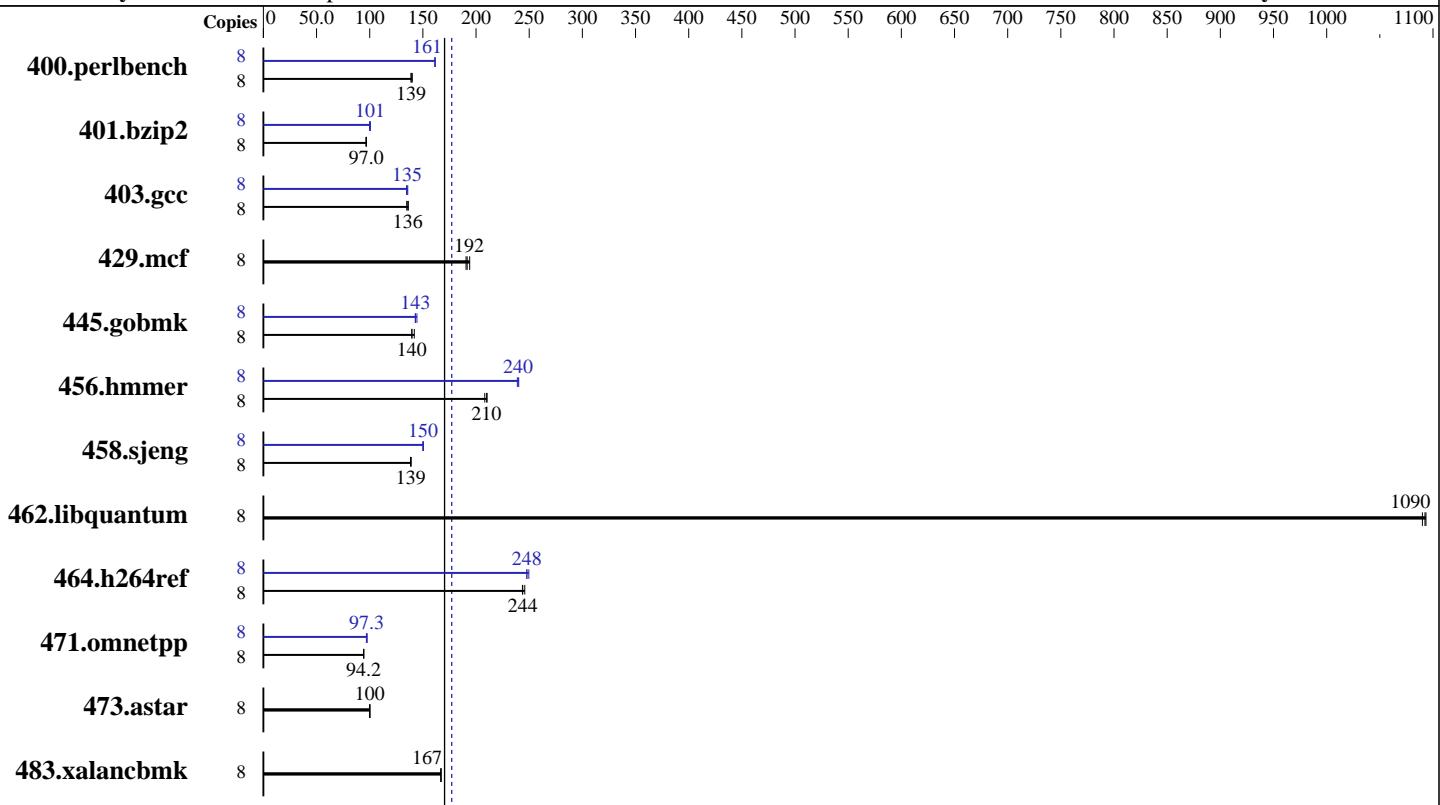
CPU2006 license: 97
Test sponsor: Acer Incorporation
Tested by: Acer Incorporation

Test date: Dec-2011
Hardware Availability: Jan-2012
Software Availability: Dec-2011

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Hardware

CPU Name:	Intel Xeon E3-1280
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 Kernel 2.6.32-131.0.15.el6.x86_64-default
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	558	140	563	139	**562**	**139**	8	484	162	**484**	**161**	484	161
401.bzip2	8	796	97.0	**796**	**97.0**	801	96.4	8	**768**	**101**	774	99.8	768	101
403.gcc	8	478	135	473	136	**474**	**136**	8	474	136	478	135	**476**	**135**
429.mcf	8	376	194	383	191	**381**	**192**	8	376	194	383	191	**381**	**192**
445.gobmk	8	**601**	**140**	591	142	601	140	8	588	143	581	144	**587**	**143**
456.hmmer	8	358	208	355	210	**355**	**210**	8	313	239	**312**	**240**	311	240
458.sjeng	8	**698**	**139**	699	138	697	139	8	**644**	**150**	643	150	645	150
462.libquantum	8	**152**	**1090**	152	1090	152	1090	8	**152**	**1090**	152	1090	152	1090
464.h264ref	8	**726**	**244**	727	244	720	246	8	715	248	709	250	**714**	**248**
471.omnetpp	8	**531**	**94.2**	531	94.2	529	94.6	8	513	97.5	**514**	**97.3**	516	96.9
473.astar	8	**561**	**100**	561	100	562	99.9	8	**561**	**100**	561	100	562	99.9
483.xalancbmk	8	330	167	**330**	**167**	331	167	8	330	167	**330**	**167**	331	167

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    numactl was used to bind copies to the cores
    The config file option 'submit' was used.

Operating System Notes

    Large pages were disabled for this run.

General Notes

    Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

      401.bzip2: icc -m64

     456.hmmer: icc -m64

      458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch`
`-auto-ilp32 -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4`
`-auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias`
`-opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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