



SPEC[®] CFP2006 Result

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IBM Corporation

SPECfp[®]_rate2006 = 109

IBM System x3100 M4 (Intel Xeon E3-1220)

SPECfp_rate_base2006 = 107

CPU2006 license: 11

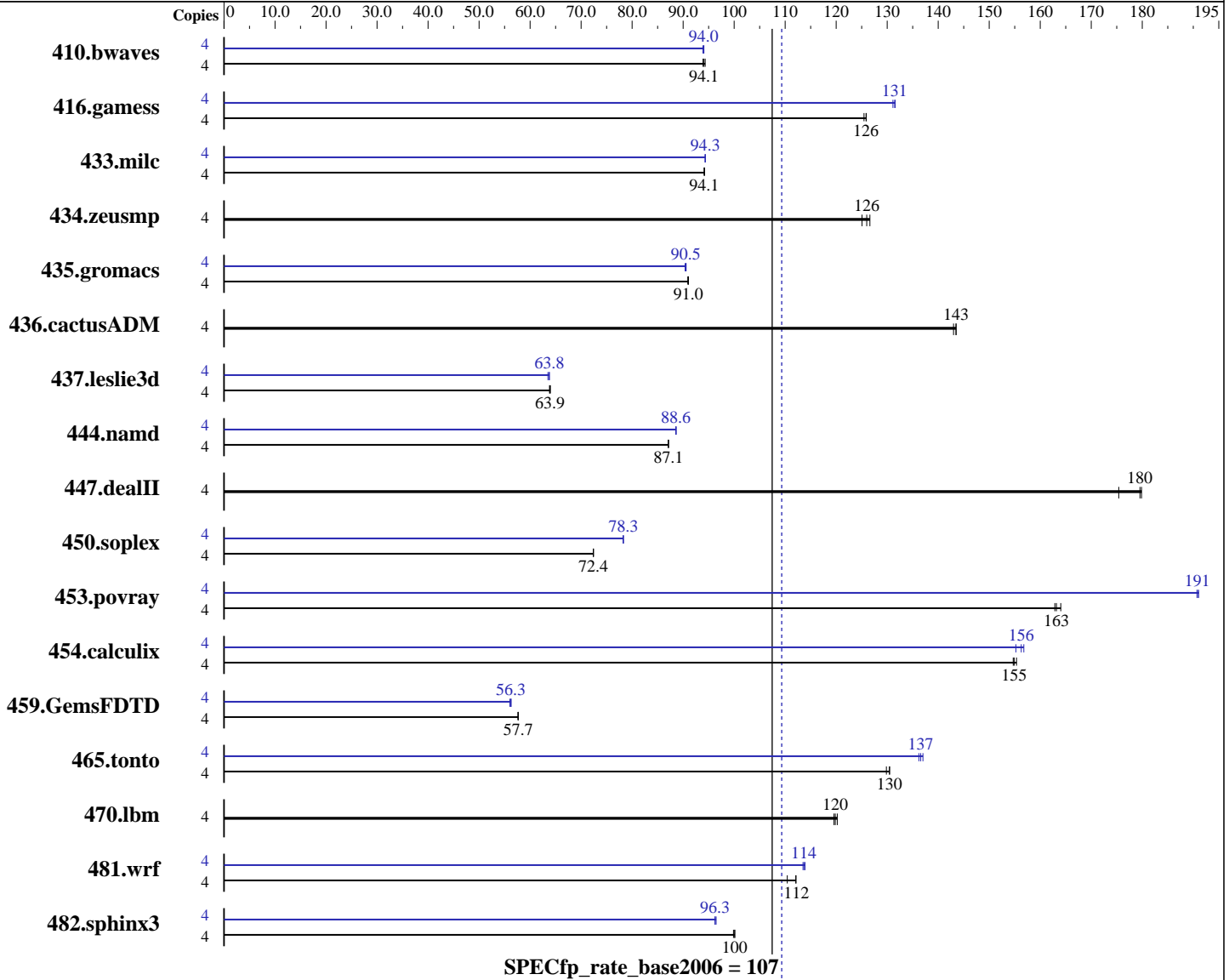
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2011

Hardware Availability: Oct-2011

Software Availability: Aug-2011



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	576	94.3	<u>578</u>	<u>94.1</u>	579	93.9	4	579	93.9	578	94.1	<u>578</u>	<u>94.0</u>		
416.gamess	4	622	126	<u>622</u>	<u>126</u>	624	125	4	<u>596</u>	<u>131</u>	595	132	597	131		
433.milc	4	390	94.2	<u>390</u>	<u>94.1</u>	390	94.1	4	389	94.3	<u>389</u>	<u>94.3</u>	389	94.3		
434.zeusmp	4	287	127	<u>289</u>	<u>126</u>	291	125	4	287	127	<u>289</u>	<u>126</u>	291	125		
435.gromacs	4	<u>314</u>	<u>91.0</u>	314	91.1	314	90.9	4	315	90.6	316	90.4	<u>315</u>	<u>90.5</u>		
436.cactusADM	4	333	144	<u>333</u>	<u>143</u>	334	143	4	333	144	<u>333</u>	<u>143</u>	334	143		
437.leslie3d	4	588	64.0	589	63.8	<u>589</u>	<u>63.9</u>	4	<u>590</u>	<u>63.8</u>	592	63.5	590	63.8		
444.namd	4	368	87.1	<u>368</u>	<u>87.1</u>	368	87.1	4	<u>362</u>	<u>88.6</u>	362	88.6	362	88.6		
447.dealII	4	<u>255</u>	<u>180</u>	254	180	261	175	4	<u>255</u>	<u>180</u>	254	180	261	175		
450.soplex	4	<u>461</u>	<u>72.4</u>	461	72.4	461	72.4	4	426	78.3	426	78.2	<u>426</u>	<u>78.3</u>		
453.povray	4	130	164	131	163	<u>130</u>	<u>163</u>	4	<u>112</u>	<u>191</u>	111	191	112	191		
454.calculix	4	212	155	213	155	<u>213</u>	<u>155</u>	4	213	155	<u>211</u>	<u>156</u>	210	157		
459.GemsFDTD	4	<u>736</u>	<u>57.7</u>	736	57.7	737	57.6	4	754	56.3	<u>754</u>	<u>56.3</u>	757	56.0		
465.tonto	4	302	130	<u>302</u>	<u>130</u>	303	130	4	<u>288</u>	<u>137</u>	289	136	287	137		
470.lbm	4	457	120	<u>459</u>	<u>120</u>	460	120	4	457	120	<u>459</u>	<u>120</u>	460	120		
481.wrf	4	405	110	<u>399</u>	<u>112</u>	399	112	4	392	114	<u>393</u>	<u>114</u>	394	113		
482.sphinx3	4	779	100	<u>779</u>	<u>100</u>	781	99.9	4	<u>810</u>	<u>96.3</u>	808	96.5	810	96.3		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

BIOS Settings:
Turbo Mode enabled in BIOS
C-State enabled in BIOS



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/SPECcpu12.1/smartheap:/root/SPECcpu12.1/ic12.1-libs/ia32:/root/SPECcpu12.1/ic12.1-libs/intel64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.deallI: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
 -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

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Peak Optimization Flags (Continued)

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.xml>

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Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 01:24:20 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 December 2011.