SPEC® CINT2006 Result

Hewlett-Packard Company

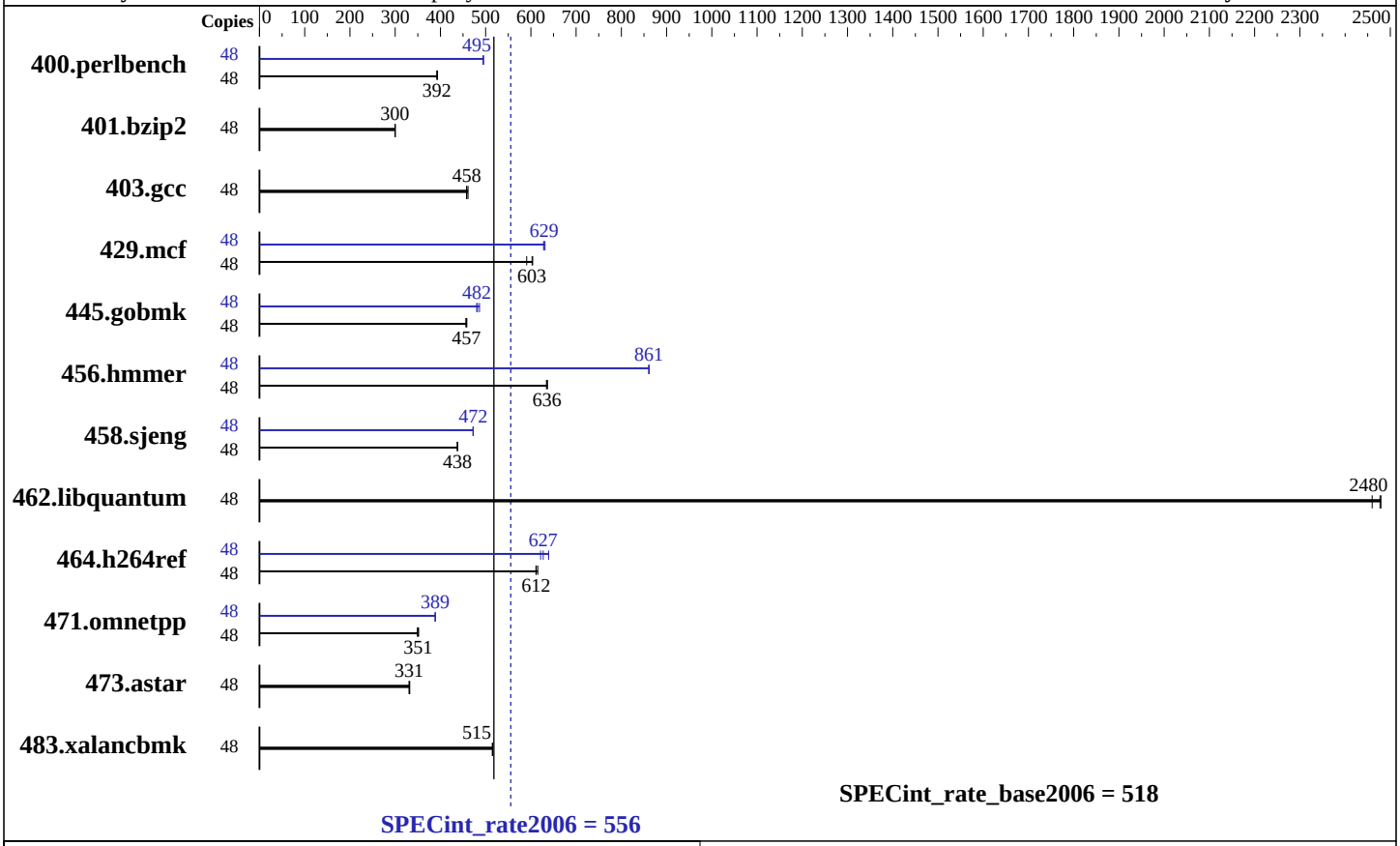
ProLiant DL580 G7
(2.00 GHz, Intel Xeon E7540)

SPECint®_rate2006 = 556

SPECint_rate_base2006 = 518

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2011
Hardware Availability: Jul-2010
Software Availability: Dec-2010



Hardware

CPU Name:	Intel Xeon E7540
CPU Characteristics:	Intel Turbo Boost Technology up to 2.27 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 4Rx4 PC3-8500R-7, ECC)
Disk Subsystem:	1 x 300 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	C/C++: Version 12.0.1.116 of Intel C++ Compiler XE Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	**1195**	**392**	1191	394	1196	392	48	948	494	**947**	**495**	947	495
401.bzip2	48	1543	300	1546	300	**1545**	**300**	48	1543	300	1546	300	**1545**	**300**
403.gcc	48	844	458	838	461	**843**	**458**	48	844	458	838	461	**843**	**458**
429.mcf	48	725	604	**726**	**603**	741	591	48	**695**	**629**	694	631	697	628
445.gobmk	48	1098	458	**1101**	**457**	1104	456	48	1049	480	1035	486	**1045**	**482**
456.hmmer	48	**704**	**636**	706	634	703	637	48	**520**	**861**	520	862	520	860
458.sjeng	48	**1327**	**438**	1328	437	1326	438	48	1229	473	1230	472	**1230**	**472**
462.libquantum	48	401	2480	**401**	**2480**	404	2460	48	401	2480	**401**	**2480**	404	2460
464.h264ref	48	1727	615	**1736**	**612**	1738	611	48	1662	639	1709	622	**1693**	**627**
471.omnetpp	48	853	352	860	349	**856**	**351**	48	772	389	**772**	**389**	773	388
473.astar	48	1019	331	**1018**	**331**	1014	332	48	1019	331	**1018**	**331**	1014	332
483.xalancbmk	48	**643**	**515**	643	515	643	515	48	**643**	**515**	643	515	643	515

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

Operating System Notes

```
'nodev   /mnt/hugepages   hugetlbfs  defaults   0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 21600  > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS configuration:
 HP Power Profile set to Maximum Performance
 Thermal Configuration set to Increased Cooling
```

General Notes

```
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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