SPEC® CINT2006 Result

Bull SAS

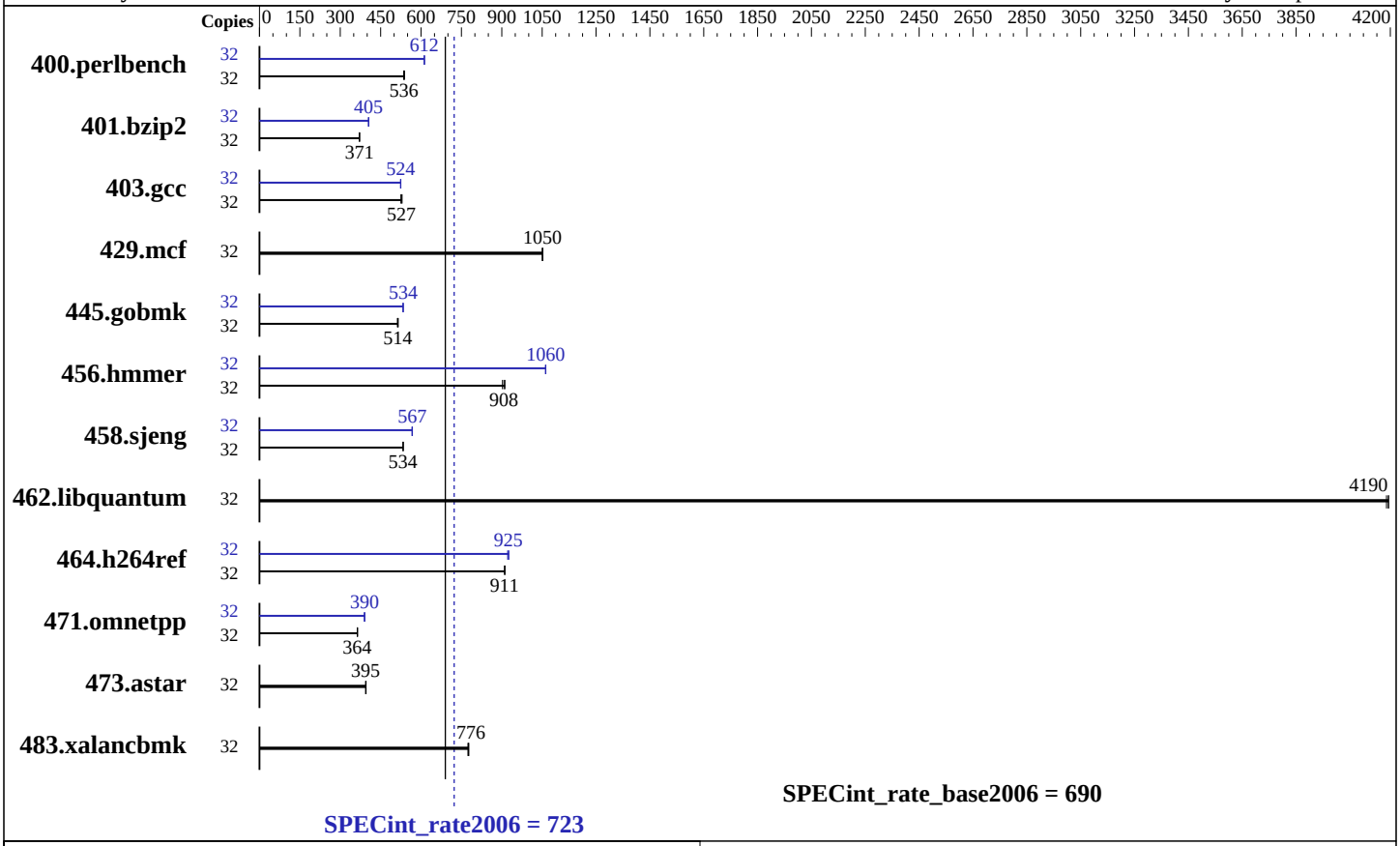
Bullion 1 module Intel X7560 1 thread/core

SPECint®_rate2006 = 723

SPECint_rate_base2006 = 690

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Oct-2011
Hardware Availability: Apr-2010
Software Availability: Sep-2011



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 4Rx8 PC3-8500R-7, ECC)
Disk Subsystem:	1 x 500 GB 7200RPM SATA
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel Compiler XE for applications on IA-32 Build 20110803
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	**583**	**536**	580	539	583	536	32	510	613	511	612	**511**	**612**
401.bzip2	32	831	371	831	371	**831**	**371**	32	**762**	**405**	762	405	762	405
403.gcc	32	490	525	**489**	**527**	486	530	32	**491**	**524**	491	524	491	525
429.mcf	32	278	1050	**278**	**1050**	277	1050	32	278	1050	**278**	**1050**	277	1050
445.gobmk	32	651	515	**653**	**514**	655	512	32	**629**	**534**	629	534	630	533
456.hmmer	32	**329**	**908**	327	912	331	902	32	**281**	**1060**	281	1060	281	1060
458.sjeng	32	726	533	**726**	**534**	725	534	32	682	568	683	567	**683**	**567**
462.libquantum	32	158	4190	158	4190	**158**	**4190**	32	158	4190	158	4190	**158**	**4190**
464.h264ref	32	**778**	**911**	778	911	777	911	32	**766**	**925**	768	922	765	926
471.omnetpp	32	550	364	**550**	**364**	549	364	32	513	390	512	391	**513**	**390**
473.astar	32	569	395	**569**	**395**	567	396	32	569	395	**569**	**395**	567	396
483.xalancbmk	32	285	774	**284**	**776**	284	778	32	285	774	**284**	**776**	284	778

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 SPEC files placed in /spec2006, with /spec2006
 mounted as tmpfs with mpol=interleave, size=256G
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec2006/smartheap:/spec2006/ic12.1-libs/ia32/:/spec2006/ic12.1-libs/intel64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5
 Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

 Hyperthreading was disabled in the BIOS
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.20111110.html`
`http://www.spec.org/cpu2006/flags/Bull-Intel-Platform-linux64-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.20111110.xml`
`http://www.spec.org/cpu2006/flags/Bull-Intel-Platform-linux64-revC.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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