



SPEC[®] CFP2006 Result

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Itaotec

SPECfp[®]_rate2006 = 266

Servidor Itaotec MX214 (Intel Xeon X5675)

SPECfp_rate_base2006 = 258

CPU2006 license: 9001

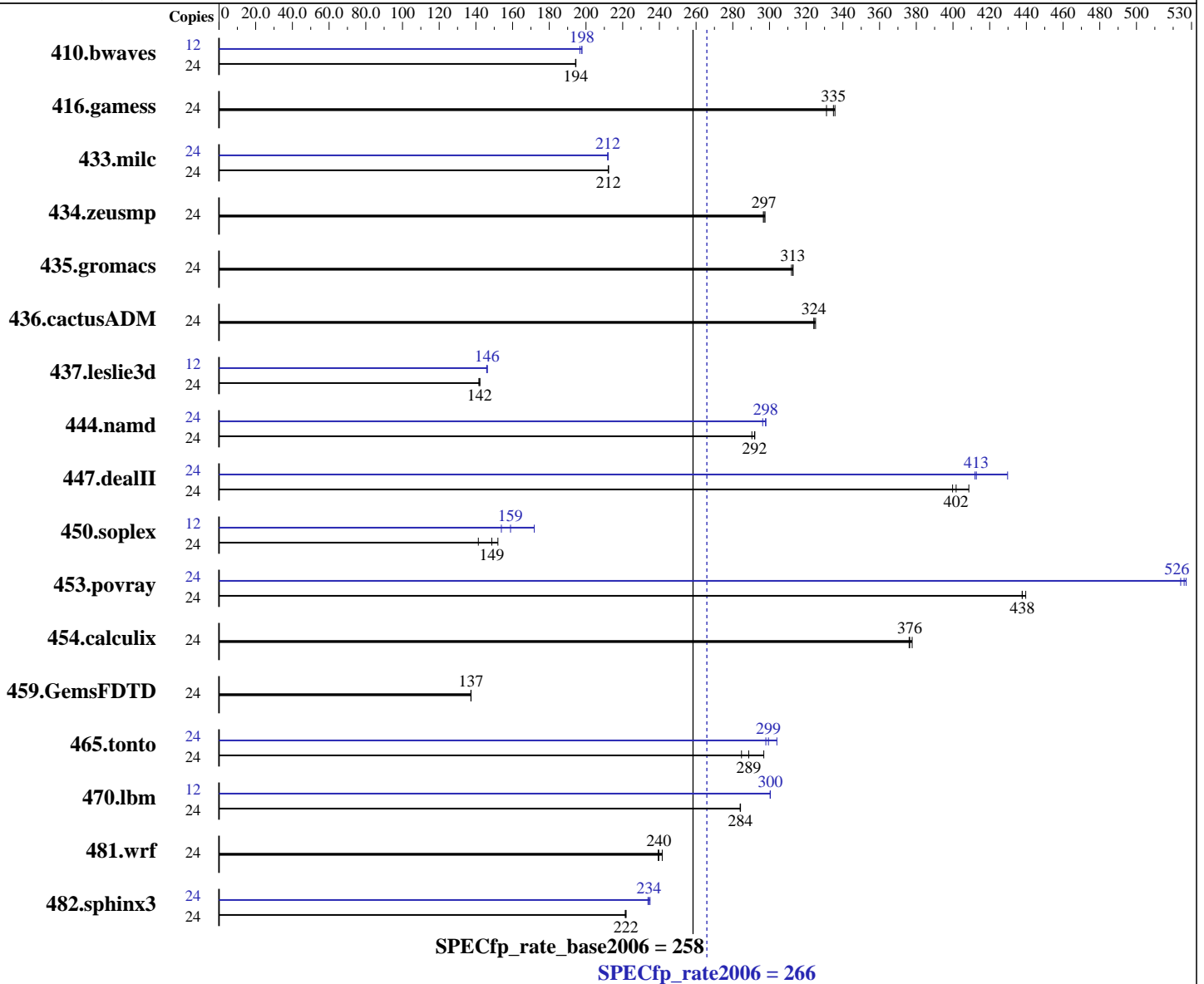
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Sep-2011

Hardware Availability: Jul-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.2 Build 20110112
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1677	195	1678	194	<u>1678</u>	<u>194</u>	12	825	198	<u>825</u>	<u>198</u>	829	197
416.gamess	24	1400	336	1419	331	<u>1403</u>	<u>335</u>	24	1400	336	1419	331	<u>1403</u>	<u>335</u>
433.milc	24	1038	212	<u>1038</u>	<u>212</u>	1039	212	24	1040	212	1039	212	<u>1039</u>	<u>212</u>
434.zeusmp	24	736	297	<u>735</u>	<u>297</u>	734	298	24	736	297	<u>735</u>	<u>297</u>	734	298
435.gromacs	24	<u>548</u>	<u>313</u>	549	312	548	313	24	<u>548</u>	<u>313</u>	549	312	548	313
436.cactusADM	24	882	325	<u>884</u>	<u>324</u>	885	324	24	882	325	<u>884</u>	<u>324</u>	885	324
437.leslie3d	24	1587	142	1593	142	<u>1589</u>	<u>142</u>	12	<u>772</u>	<u>146</u>	773	146	771	146
444.namd	24	659	292	<u>659</u>	<u>292</u>	663	290	24	<u>646</u>	<u>298</u>	650	296	646	298
447.dealII	24	<u>683</u>	<u>402</u>	672	409	687	400	24	666	412	639	430	<u>665</u>	<u>413</u>
450.soplex	24	1416	141	<u>1346</u>	<u>149</u>	1317	152	12	650	154	<u>630</u>	<u>159</u>	582	172
453.povray	24	<u>292</u>	<u>438</u>	292	438	290	440	24	<u>243</u>	<u>526</u>	244	524	242	527
454.calculix	24	524	378	526	376	<u>526</u>	<u>376</u>	24	524	378	526	376	<u>526</u>	<u>376</u>
459.GemsFDTD	24	<u>1854</u>	<u>137</u>	1855	137	1853	137	24	<u>1854</u>	<u>137</u>	1855	137	1853	137
465.tonto	24	829	285	<u>818</u>	<u>289</u>	795	297	24	792	298	777	304	<u>789</u>	<u>299</u>
470.lbm	24	<u>1161</u>	<u>284</u>	1160	284	1161	284	12	549	300	<u>549</u>	<u>300</u>	549	300
481.wrf	24	1110	242	1120	239	<u>1118</u>	<u>240</u>	24	1110	242	1120	239	<u>1118</u>	<u>240</u>
482.sphinx3	24	2113	221	2109	222	<u>2110</u>	<u>222</u>	24	2001	234	<u>1996</u>	<u>234</u>	1991	235

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

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Itautec

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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