



# SPEC® CINT2006 Result

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## NEC Corporation

SPECint®\_rate2006 = 918

Express5800/A1080a-S (Intel Xeon E7-8850)

SPECint\_rate\_base2006 = 867

CPU2006 license: 9006

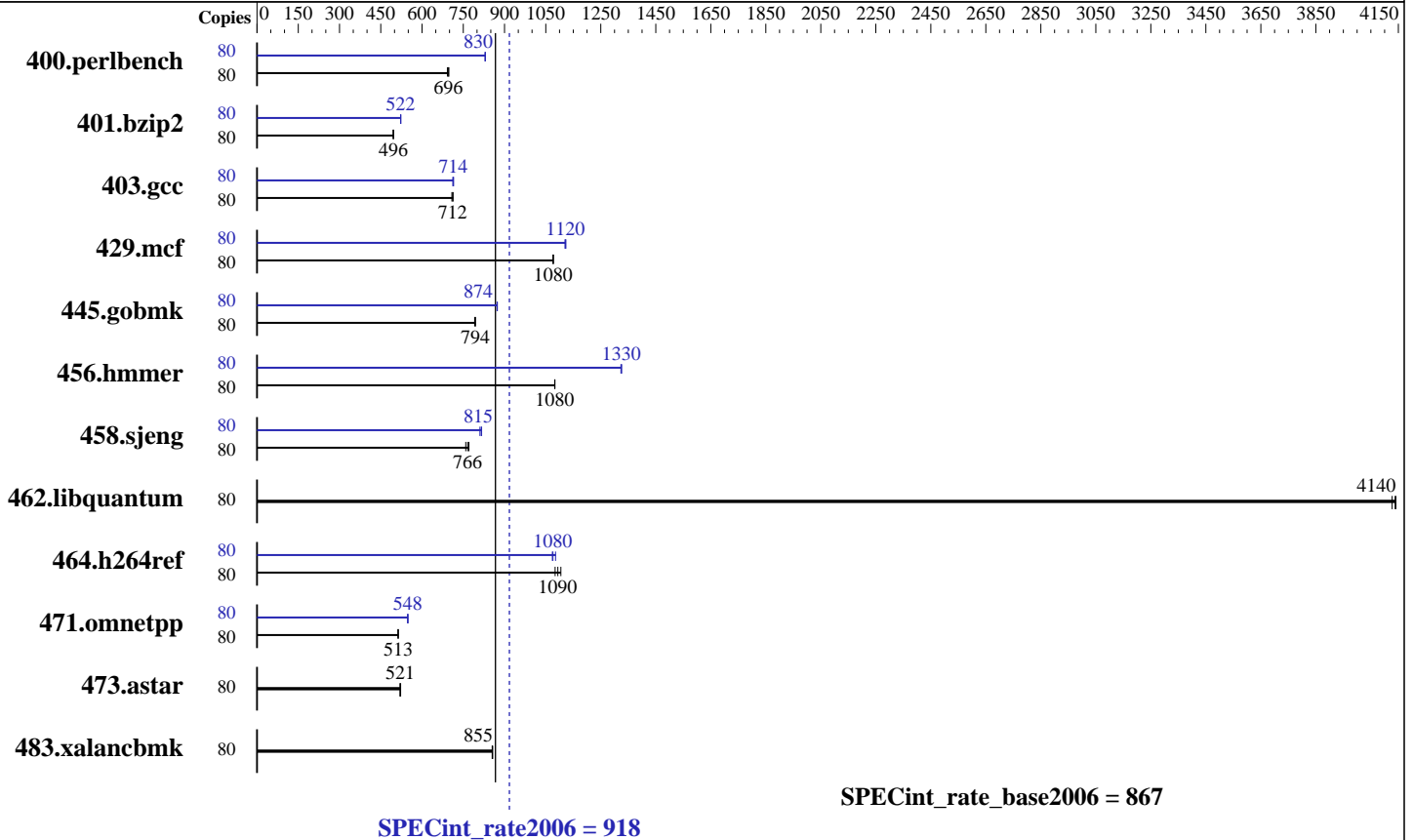
Test date: Aug-2011

Test sponsor: NEC Corporation

Hardware Availability: Nov-2011

Tested by: NEC Corporation

Software Availability: May-2011



### Hardware

CPU Name: Intel Xeon E7-8850  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (64 x 4 GB 2Rx4 PC3-8500R-7, ECC)  
 Disk Subsystem: 2x300 GB SAS, 10000 RPM, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86\_64 on an x86\_64  
 Compiler: C/C++: Version 12.0.4.191 of Intel Compiler XE for applications on IA-32 Build 20110427  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	1121	697	<b><u>1123</u></b>	<b><u>696</u></b>	1129	692	80	943	829	941	830	<b><u>942</u></b>	<b><u>830</u></b>
401.bzip2	80	1559	495	1556	496	<b><u>1557</u></b>	<b><u>496</u></b>	80	1477	523	1478	522	<b><u>1478</u></b>	<b><u>522</u></b>
403.gcc	80	909	709	<b><u>905</u></b>	<b><u>712</u></b>	903	713	80	901	715	<b><u>902</u></b>	<b><u>714</u></b>	905	712
429.mcf	80	677	1080	<b><u>677</u></b>	<b><u>1080</u></b>	678	1080	80	650	1120	652	1120	<b><u>650</u></b>	<b><u>1120</u></b>
445.gobmk	80	1057	794	<b><u>1057</u></b>	<b><u>794</u></b>	1060	791	80	962	873	<b><u>961</u></b>	<b><u>874</u></b>	961	874
456.hammer	80	689	1080	<b><u>689</u></b>	<b><u>1080</u></b>	690	1080	80	<b><u>563</u></b>	<b><u>1330</u></b>	564	1320	563	1330
458.sjeng	80	1275	759	1256	771	<b><u>1264</u></b>	<b><u>766</u></b>	80	1194	810	1185	817	<b><u>1188</u></b>	<b><u>815</u></b>
462.libquantum	80	<b><u>401</u></b>	<b><u>4140</u></b>	402	4130	400	4140	80	<b><u>401</u></b>	<b><u>4140</u></b>	402	4130	400	4140
464.h264ref	80	<b><u>1619</u></b>	<b><u>1090</u></b>	1633	1080	1602	1100	80	<b><u>1645</u></b>	<b><u>1080</u></b>	1649	1070	1631	1090
471.omnetpp	80	<b><u>974</u></b>	<b><u>513</u></b>	974	514	975	513	80	913	548	<b><u>912</u></b>	<b><u>548</u></b>	911	549
473.astar	80	<b><u>1079</u></b>	<b><u>521</u></b>	1080	520	1075	522	80	<b><u>1079</u></b>	<b><u>521</u></b>	1080	520	1075	522
483.xalancbmk	80	644	857	645	855	<b><u>645</u></b>	<b><u>855</u></b>	80	644	857	645	855	<b><u>645</u></b>	<b><u>855</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode  
Huge pages were not configured for this run

## Platform Notes

Patrol Scrubbing set to disabled in Maintenance Console

## General Notes

The Express5800/A1080a-S and the Express5800/A1080a-D models are electronically equivalent.  
The results have been measured on the Express5800/A1080a-S model.  
Binaries were compiled on RHEL 5.6

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/SmartHeap\_9/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/opt/SmartHeap\_9/lib -lsmarheap

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110705.html>

<http://www.spec.org/cpu2006/flags/NEC-platform-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110705.xml>

<http://www.spec.org/cpu2006/flags/NEC-platform-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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