



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itaotec

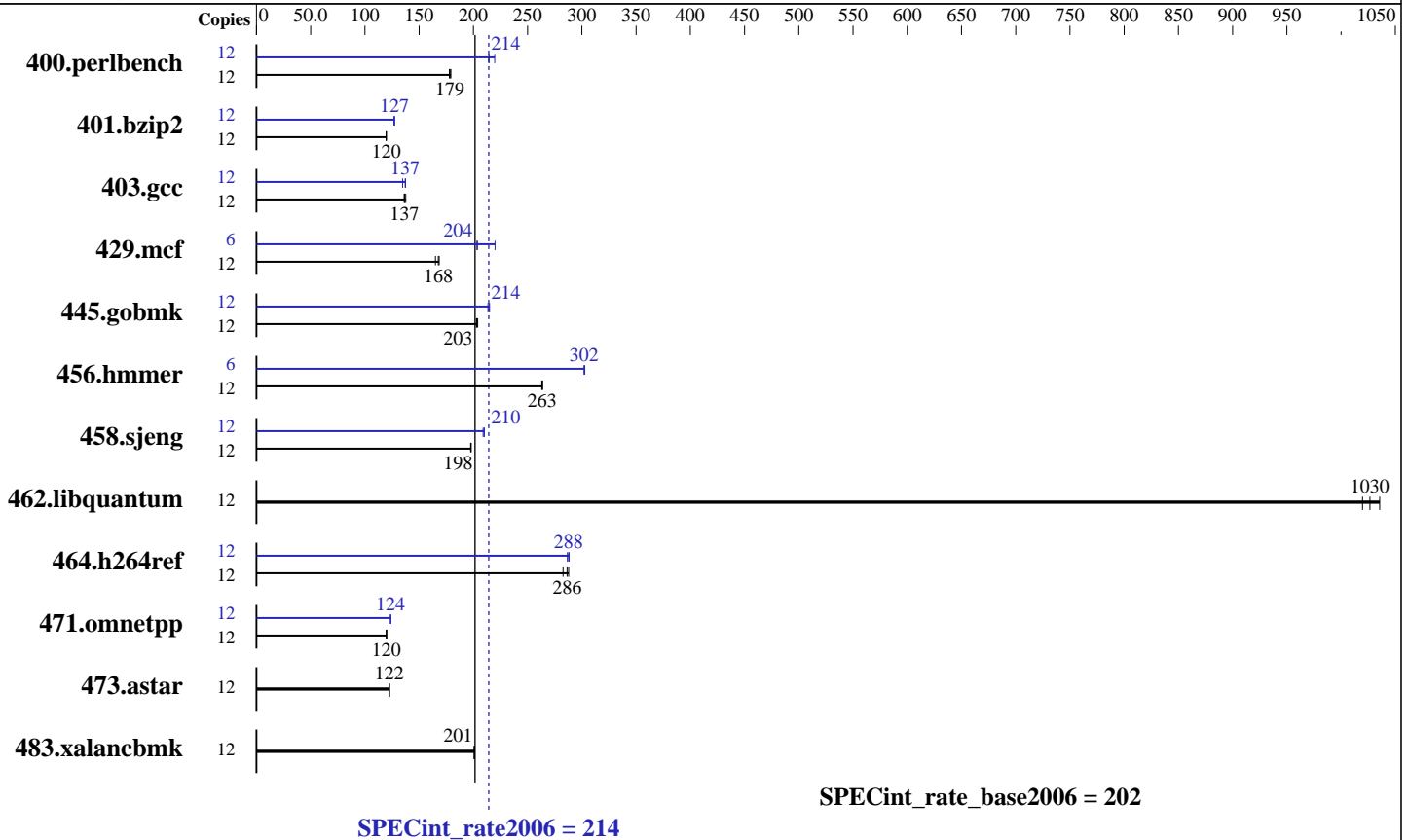
### SPECint®\_rate2006 = 214

### Servidor Itaotec MX214 (Intel Xeon X5690)

### SPECint\_rate\_base2006 = 202

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Aug-2011  
Hardware Availability: Jul-2011  
Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X5690  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3467  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.2 Build 20110112  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 214

Servidor Itaotec MX214 (Intel Xeon X5690)

SPECint\_rate\_base2006 = 202

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Aug-2011  
Hardware Availability: Jul-2011  
Software Availability: Jan-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	<b><u>657</u></b>	<b><u>179</u></b>	659	178	654	179	12	<b><u>547</u></b>	<b><u>214</u></b>	547	214	534	220
401.bzip2	12	969	120	967	120	<b><u>968</u></b>	<b><u>120</u></b>	12	910	127	913	127	<b><u>910</u></b>	<b><u>127</u></b>
403.gcc	12	<b><u>707</u></b>	<b><u>137</u></b>	709	136	702	138	12	703	137	<b><u>704</u></b>	<b><u>137</u></b>	717	135
429.mcf	12	663	165	<b><u>653</u></b>	<b><u>168</u></b>	650	168	6	<b><u>269</u></b>	<b><u>204</u></b>	269	203	249	220
445.gobmk	12	<b><u>620</u></b>	<b><u>203</u></b>	621	203	617	204	12	<b><u>587</u></b>	<b><u>214</u></b>	586	215	590	213
456.hammer	12	424	264	<b><u>425</u></b>	<b><u>263</u></b>	426	263	6	<b><u>185</u></b>	<b><u>302</u></b>	185	302	185	303
458.sjeng	12	<b><u>734</u></b>	<b><u>198</u></b>	734	198	735	197	12	695	209	<b><u>692</u></b>	<b><u>210</u></b>	691	210
462.libquantum	12	<b><u>242</u></b>	<b><u>1030</u></b>	240	1040	244	1020	12	<b><u>242</u></b>	<b><u>1030</u></b>	240	1040	244	1020
464.h264ref	12	923	288	<b><u>927</u></b>	<b><u>286</u></b>	940	283	12	<b><u>923</u></b>	<b><u>288</u></b>	927	287	921	288
471.omnetpp	12	626	120	<b><u>626</u></b>	<b><u>120</u></b>	625	120	12	<b><u>606</u></b>	<b><u>124</u></b>	606	124	607	124
473.astar	12	<b><u>688</u></b>	<b><u>122</u></b>	687	123	688	122	12	<b><u>688</u></b>	<b><u>122</u></b>	687	123	688	122
483.xalancbmk	12	412	201	413	201	<b><u>412</u></b>	<b><u>201</u></b>	12	412	201	413	201	<b><u>412</u></b>	<b><u>201</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 214

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint\_rate\_base2006 = 202

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Aug-2011  
Hardware Availability: Jul-2011  
Software Availability: Jan-2011

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 214

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint\_rate\_base2006 = 202

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Aug-2011  
Hardware Availability: Jul-2011  
Software Availability: Jan-2011

## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 214

Servidor Itautec MX214 (Intel Xeon X5690)

SPECint\_rate\_base2006 = 202

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Aug-2011  
Hardware Availability: Jul-2011  
Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 21:39:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 27 September 2011.