



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 31.4

PRIMERGY RX600 S6, Intel Xeon E7-4850, 2.00 GHz

SPECint_base2006 = 28.7

CPU2006 license: 19

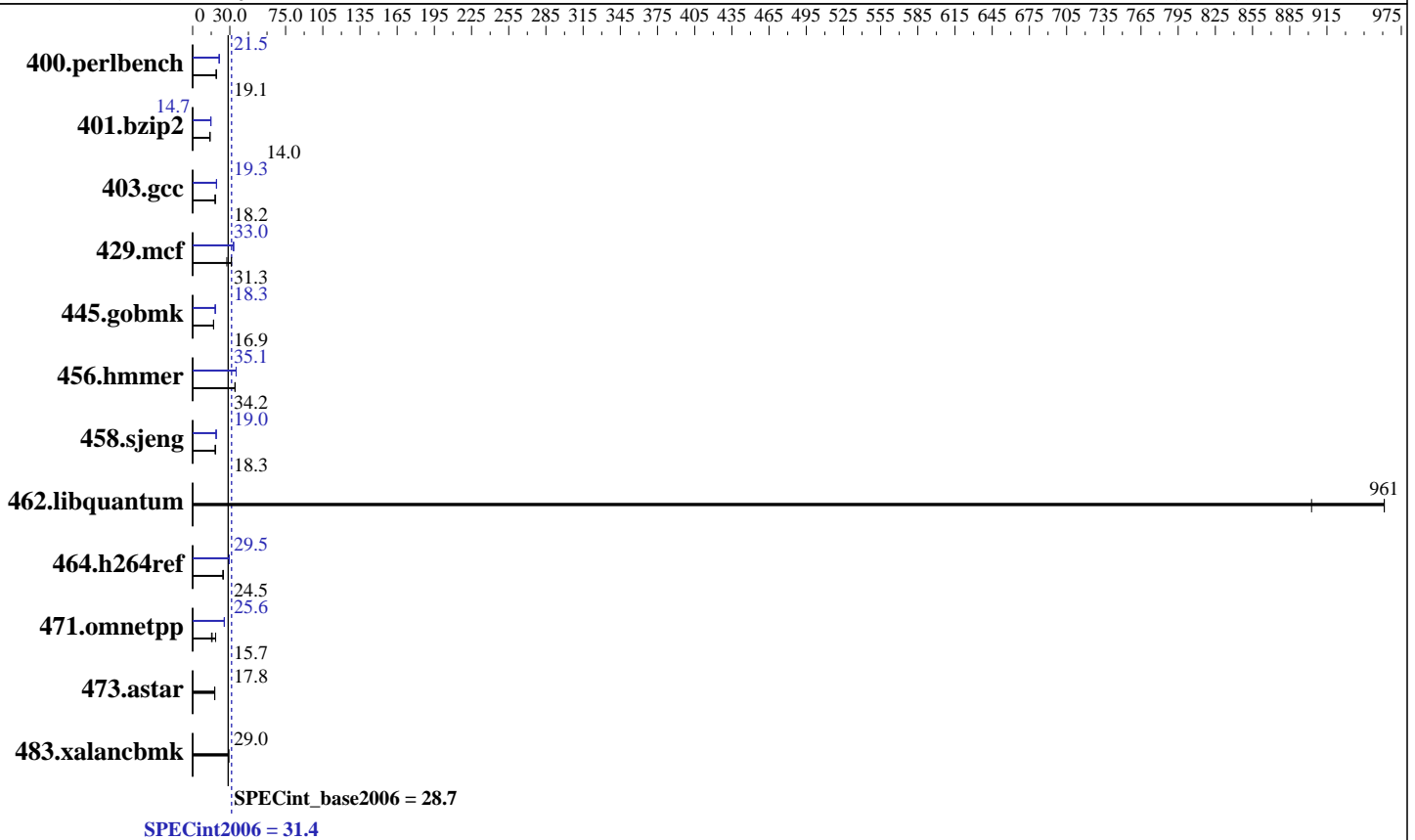
Test date: Aug-2011

Test sponsor: Fujitsu

Hardware Availability: Jul-2011

Tested by: Fujitsu

Software Availability: Jul-2011



Hardware

CPU Name: Intel Xeon E7-4850
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
 CPU(s) orderable: 2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC)
 Disk Subsystem: 1 x SAS, 600 GB, 10000 RPM
 Other Hardware: --

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.4.191 Build 20110427
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	513	19.0	<u>511</u>	<u>19.1</u>	511	19.1	<u>455</u>	<u>21.5</u>	455	21.5	455	21.5
401.bzip2	691	14.0	<u>691</u>	<u>14.0</u>	691	14.0	657	14.7	<u>657</u>	<u>14.7</u>	657	14.7
403.gcc	<u>442</u>	<u>18.2</u>	441	18.3	442	18.2	418	19.3	<u>418</u>	<u>19.3</u>	418	19.2
429.mcf	<u>292</u>	<u>31.3</u>	331	27.6	291	31.3	<u>276</u>	<u>33.0</u>	276	33.0	277	32.9
445.gobmk	619	17.0	622	16.9	<u>621</u>	<u>16.9</u>	574	18.3	<u>574</u>	<u>18.3</u>	574	18.3
456.hammer	<u>273</u>	<u>34.2</u>	273	34.2	273	34.2	266	35.1	<u>266</u>	<u>35.1</u>	266	35.1
458.sjeng	660	18.3	<u>660</u>	<u>18.3</u>	661	18.3	637	19.0	638	19.0	<u>638</u>	<u>19.0</u>
462.libquantum	21.6	961	<u>21.6</u>	<u>961</u>	23.0	903	21.6	961	<u>21.6</u>	<u>961</u>	23.0	903
464.h264ref	890	24.9	905	24.5	<u>904</u>	<u>24.5</u>	<u>749</u>	<u>29.5</u>	749	29.5	750	29.5
471.omnetpp	410	15.2	<u>398</u>	<u>15.7</u>	337	18.6	244	25.6	<u>244</u>	<u>25.6</u>	246	25.4
473.astar	394	17.8	394	17.8	<u>394</u>	<u>17.8</u>	394	17.8	394	17.8	<u>394</u>	<u>17.8</u>
483.xalancbmk	238	29.0	237	29.1	<u>238</u>	<u>29.0</u>	238	29.0	237	29.1	<u>238</u>	<u>29.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodetv /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 36000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional
Intel HT Technology = Disable

General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/smartheap/intel64 -lsmarheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64
400.perlbench: icc -m32

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Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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Peak Optimization Flags (Continued)

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/opt/smartheap/ia32 -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20110705.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20110705.xml>



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