



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 371

PowerEdge M710 (Intel Xeon X5660, 2.80 GHz)

SPECint_rate_base2006 = 345

CPU2006 license: 55

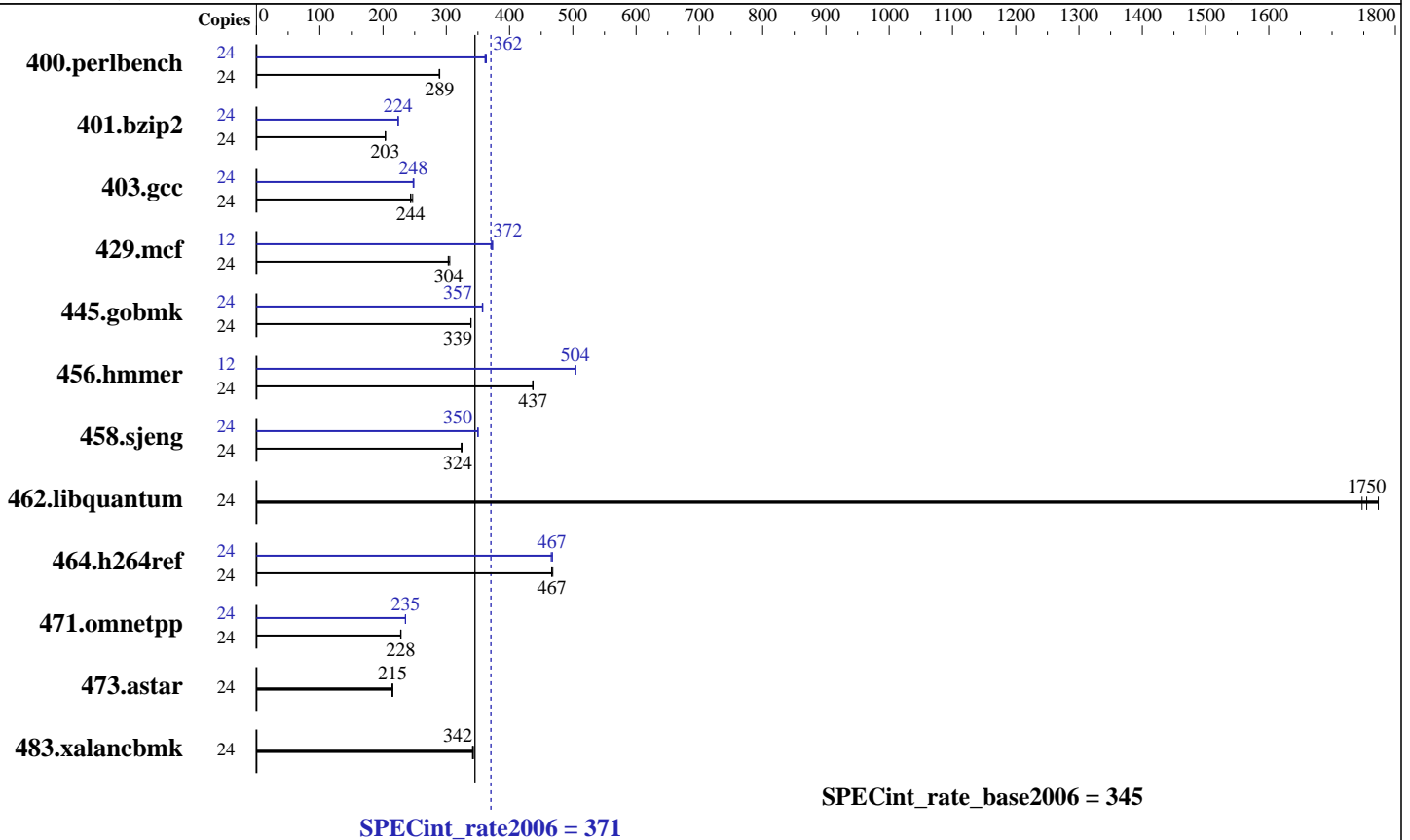
Test date: Jul-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|----------------|--------|------------|------------|-------------|------------|-------------|-------------|--------|------------|------------|------------|------------|-------------|-------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 400.perlbench | 24 | 812 | 289 | 810 | 290 | 810 | 289 | 24 | 649 | 361 | 648 | 362 | 645 | 363 | | |
| 401.bzip2 | 24 | 1138 | 203 | 1138 | 203 | 1135 | 204 | 24 | 1033 | 224 | 1035 | 224 | 1035 | 224 | | |
| 403.gcc | 24 | 783 | 247 | 792 | 244 | 794 | 243 | 24 | 778 | 248 | 780 | 248 | 777 | 249 | | |
| 429.mcf | 24 | 722 | 303 | 721 | 304 | 717 | 305 | 12 | 295 | 371 | 294 | 372 | 293 | 373 | | |
| 445.gobmk | 24 | 743 | 339 | 743 | 339 | 742 | 339 | 24 | 705 | 357 | 705 | 357 | 704 | 358 | | |
| 456.hammer | 24 | 512 | 437 | 512 | 437 | 513 | 436 | 12 | 222 | 504 | 222 | 504 | 222 | 504 | | |
| 458.sjeng | 24 | 896 | 324 | 894 | 325 | 897 | 324 | 24 | 830 | 350 | 829 | 350 | 830 | 350 | | |
| 462.libquantum | 24 | 280 | 1770 | 285 | 1750 | 283 | 1750 | 24 | 280 | 1770 | 285 | 1750 | 283 | 1750 | | |
| 464.h264ref | 24 | 1134 | 468 | 1139 | 466 | 1138 | 467 | 24 | 1140 | 466 | 1136 | 468 | 1137 | 467 | | |
| 471.omnetpp | 24 | 658 | 228 | 658 | 228 | 657 | 228 | 24 | 637 | 235 | 637 | 235 | 637 | 235 | | |
| 473.astar | 24 | 787 | 214 | 784 | 215 | 782 | 215 | 24 | 787 | 214 | 784 | 215 | 782 | 215 | | |
| 483.xalancbmk | 24 | 485 | 341 | 484 | 342 | 483 | 343 | 24 | 485 | 341 | 484 | 342 | 483 | 343 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 10800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries were compiled on RHEL5.5



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
462.libquantum: basepeak = yes

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Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 00:33:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 30 August 2011.