



# SPEC® CINT2006 Result

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## Intel Corporation

**SPECint®2006 = 31.0**

Intel DH57JG Motherboard (Intel Core i3-550)

**SPECint\_base2006 = 29.6**

CPU2006 license: 13

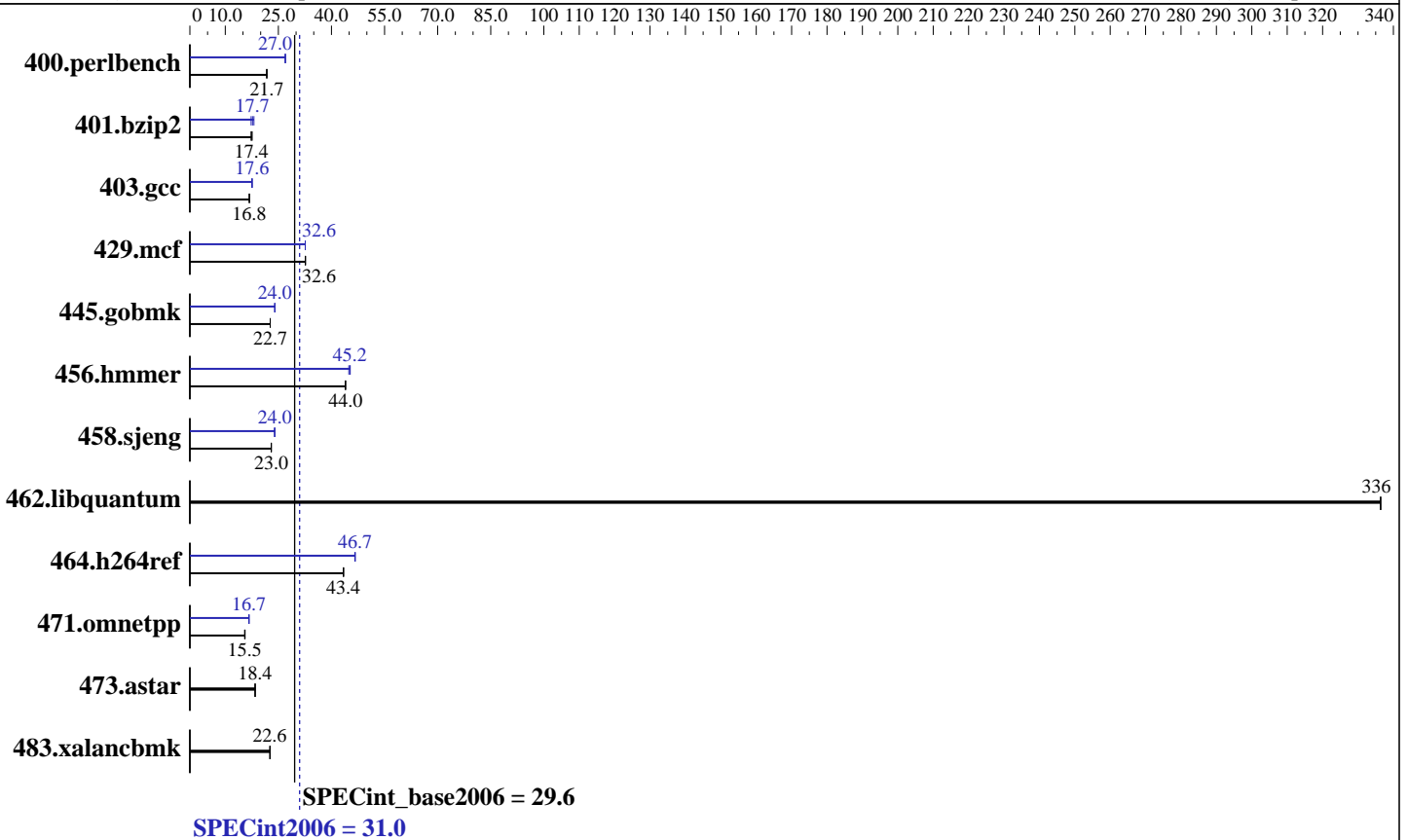
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: May-2010

Tested by: Intel Corporation

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i3-550  
 CPU Characteristics:  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)  
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows 7 Ultimate (64-bit)  
 Compiler: Intel C++ Compiler XE for Intel64  
 Version 12.0.3.176 Build 20110309  
 Microsoft Visual Studio 2008 Professional SP1  
 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 9.01 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	451	21.7	449	21.8	<b>450</b>	<b>21.7</b>	363	26.9	362	27.0	<b>362</b>	<b>27.0</b>
401.bzip2	548	17.6	<b>553</b>	<b>17.4</b>	559	17.3	<b>544</b>	<b>17.7</b>	556	17.3	532	18.1
403.gcc	480	16.8	<b>480</b>	<b>16.8</b>	481	16.8	459	17.5	458	17.6	<b>459</b>	<b>17.6</b>
429.mcf	<b>280</b>	<b>32.6</b>	280	32.6	279	32.7	280	32.6	280	32.6	<b>280</b>	<b>32.6</b>
445.gobmk	462	22.7	462	22.7	<b>462</b>	<b>22.7</b>	437	24.0	436	24.0	<b>436</b>	<b>24.0</b>
456.hmmmer	212	43.9	<b>212</b>	<b>44.0</b>	212	44.0	<b>206</b>	<b>45.2</b>	208	44.9	206	45.3
458.sjeng	527	23.0	<b>527</b>	<b>23.0</b>	527	23.0	507	23.9	505	24.0	<b>505</b>	<b>24.0</b>
462.libquantum	61.6	336	<b>61.6</b>	<b>336</b>	61.6	336	61.6	336	<b>61.6</b>	<b>336</b>	61.6	336
464.h264ref	<b>510</b>	<b>43.4</b>	510	43.4	510	43.4	474	46.7	<b>474</b>	<b>46.7</b>	475	46.6
471.omnetpp	401	15.6	<b>402</b>	<b>15.5</b>	403	15.5	<b>374</b>	<b>16.7</b>	374	16.7	375	16.7
473.astar	380	18.5	381	18.4	<b>381</b>	<b>18.4</b>	380	18.5	381	18.4	<b>381</b>	<b>18.4</b>
483.xalancbmk	305	22.7	<b>305</b>	<b>22.6</b>	306	22.6	305	22.7	<b>305</b>	<b>22.6</b>	306	22.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case,  
PC Power and Cooling 1200W power supply  
OMP\_NUM\_THREADS set to number of processors cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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## Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

```

## Base Optimization Flags

C benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000

```

C++ benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F512000000 shlw64M.lib

```

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## Peak Optimization Flags (Continued)

400.perlbench (continued):

-link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias  
-Qauto-ilp32 /F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
-Qauto-ilp32 /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32  
/F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000  
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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