



# SPEC® CINT2006 Result

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## Bull SAS

SPECint®2006 = 37.1

NovaScale T860 F2 (Intel Xeon E5649, 2.53 GHz)

SPECint\_base2006 = 35.1

CPU2006 license: 20

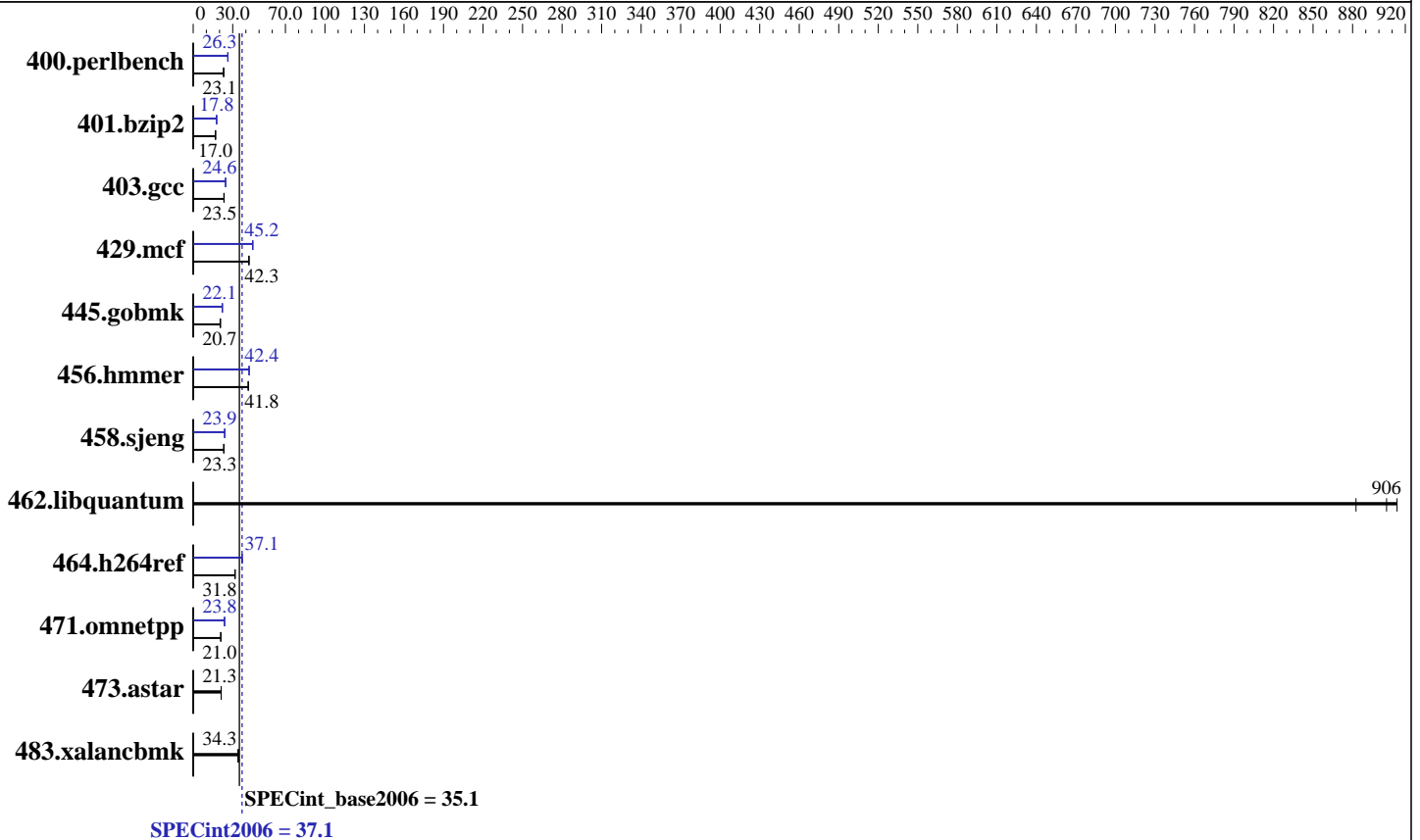
Test date: May-2011

Test sponsor: Bull SAS

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E5649  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>423</b>	<b>23.1</b>	422	23.2	423	23.1	372	26.3	371	26.3	<b>371</b>	<b>26.3</b>
401.bzip2	569	17.0	<b>569</b>	<b>17.0</b>	569	16.9	543	17.8	543	17.8	<b>543</b>	<b>17.8</b>
403.gcc	346	23.3	343	23.5	<b>343</b>	<b>23.5</b>	328	24.6	<b>328</b>	<b>24.6</b>	327	24.6
429.mcf	216	42.3	215	42.4	<b>216</b>	<b>42.3</b>	201	45.3	<b>202</b>	<b>45.2</b>	202	45.2
445.gobmk	507	20.7	513	20.4	<b>507</b>	<b>20.7</b>	<b>474</b>	<b>22.1</b>	474	22.1	474	22.1
456.hammer	223	41.8	<b>223</b>	<b>41.8</b>	223	41.8	220	42.4	220	42.5	<b>220</b>	<b>42.4</b>
458.sjeng	<b>520</b>	<b>23.3</b>	520	23.3	520	23.3	<b>506</b>	<b>23.9</b>	506	23.9	506	23.9
462.libquantum	22.7	914	<b>22.9</b>	<b>906</b>	23.5	883	22.7	914	<b>22.9</b>	<b>906</b>	23.5	883
464.h264ref	694	31.9	699	31.7	<b>696</b>	<b>31.8</b>	<b>596</b>	<b>37.1</b>	595	37.2	596	37.1
471.omnetpp	<b>298</b>	<b>21.0</b>	298	21.0	298	21.0	263	23.7	262	23.8	<b>263</b>	<b>23.8</b>
473.astar	<b>330</b>	<b>21.3</b>	331	21.2	330	21.3	<b>330</b>	<b>21.3</b>	331	21.2	330	21.3
483.xalancbmk	<b>201</b>	<b>34.3</b>	203	34.0	201	34.3	<b>201</b>	<b>34.3</b>	203	34.0	201	34.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)  
Logical Processor = Disabled (Default = Enabled)

## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5  
The Dell PowerEdge T710 and  
the Bull NovaScale T860 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge T710 model



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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 23:16:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 August 2011.