



# SPEC® CINT2006 Result

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## Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon E5630, 2.53 GHz)

SPECint®\_rate2006 = 241

SPECint\_rate\_base2006 = 229

CPU2006 license: 97

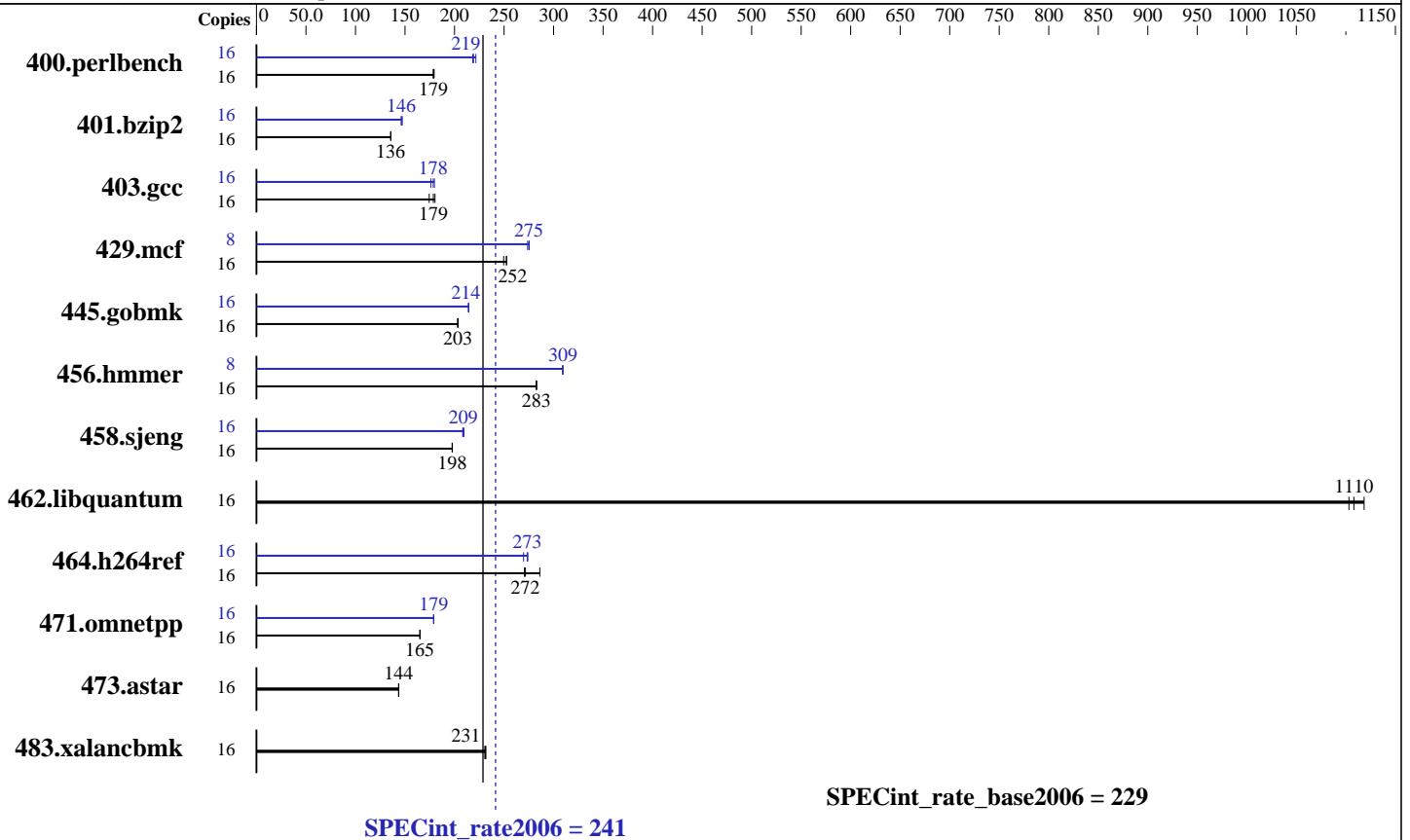
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2011

Hardware Availability: Aug-2010

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

| Benchmark      | Base   |            |             |             |            |             |            | Peak   |            |             |             |            |             |            |
|----------------|--------|------------|-------------|-------------|------------|-------------|------------|--------|------------|-------------|-------------|------------|-------------|------------|
|                | Copies | Seconds    | Ratio       | Seconds     | Ratio      | Seconds     | Ratio      | Copies | Seconds    | Ratio       | Seconds     | Ratio      | Seconds     | Ratio      |
| 400.perlbench  | 16     | 871        | 179         | <b>875</b>  | <b>179</b> | 875         | 179        | 16     | 716        | 218         | 706         | 222        | <b>713</b>  | <b>219</b> |
| 401.bzip2      | 16     | 1138       | 136         | <b>1138</b> | <b>136</b> | 1139        | 136        | 16     | 1049       | 147         | 1056        | 146        | <b>1055</b> | <b>146</b> |
| 403.gcc        | 16     | <b>721</b> | <b>179</b>  | 714         | 180        | 738         | 174        | 16     | 717        | 180         | <b>722</b>  | <b>178</b> | 731         | 176        |
| 429.mcf        | 16     | <b>578</b> | <b>252</b>  | 585         | 250        | 578         | 252        | 8      | <b>265</b> | <b>275</b>  | 266         | 274        | 265         | 275        |
| 445.gobmk      | 16     | 825        | 204         | 826         | 203        | <b>826</b>  | <b>203</b> | 16     | <b>784</b> | <b>214</b>  | 783         | 214        | 784         | 214        |
| 456.hammer     | 16     | 528        | 283         | 528         | 283        | <b>528</b>  | <b>283</b> | 8      | 241        | 309         | <b>241</b>  | <b>309</b> | 241         | 309        |
| 458.sjeng      | 16     | 980        | 198         | <b>979</b>  | <b>198</b> | 979         | 198        | 16     | 924        | 209         | <b>927</b>  | <b>209</b> | 930         | 208        |
| 462.libquantum | 16     | <b>299</b> | <b>1110</b> | 296         | 1120       | 301         | 1100       | 16     | <b>299</b> | <b>1110</b> | 296         | 1120       | 301         | 1100       |
| 464.h264ref    | 16     | 1238       | 286         | 1309        | 271        | <b>1304</b> | <b>272</b> | 16     | 1292       | 274         | <b>1295</b> | <b>273</b> | 1313        | 270        |
| 471.omnetpp    | 16     | <b>606</b> | <b>165</b>  | 605         | 165        | 606         | 165        | 16     | 559        | 179         | 559         | 179        | <b>559</b>  | <b>179</b> |
| 473.astar      | 16     | <b>782</b> | <b>144</b>  | 781         | 144        | 782         | 144        | 16     | <b>782</b> | <b>144</b>  | 781         | 144        | 782         | 144        |
| 483.xalancbmk  | 16     | 477        | 231         | 479         | 231        | <b>477</b>  | <b>231</b> | 16     | 477        | 231         | 479         | 231        | <b>477</b>  | <b>231</b> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.



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## Base Compiler Invocation

C benchmarks:

`icc -m32`

C++ benchmarks:

`icpc -m32`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch`  
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`  
`-L/smartheap -lsmartheap`  
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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