



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 35.0

Intel DX58SO2 Motherboard (Intel Core i7-930)

SPECint_base2006 = 33.1

CPU2006 license: 13

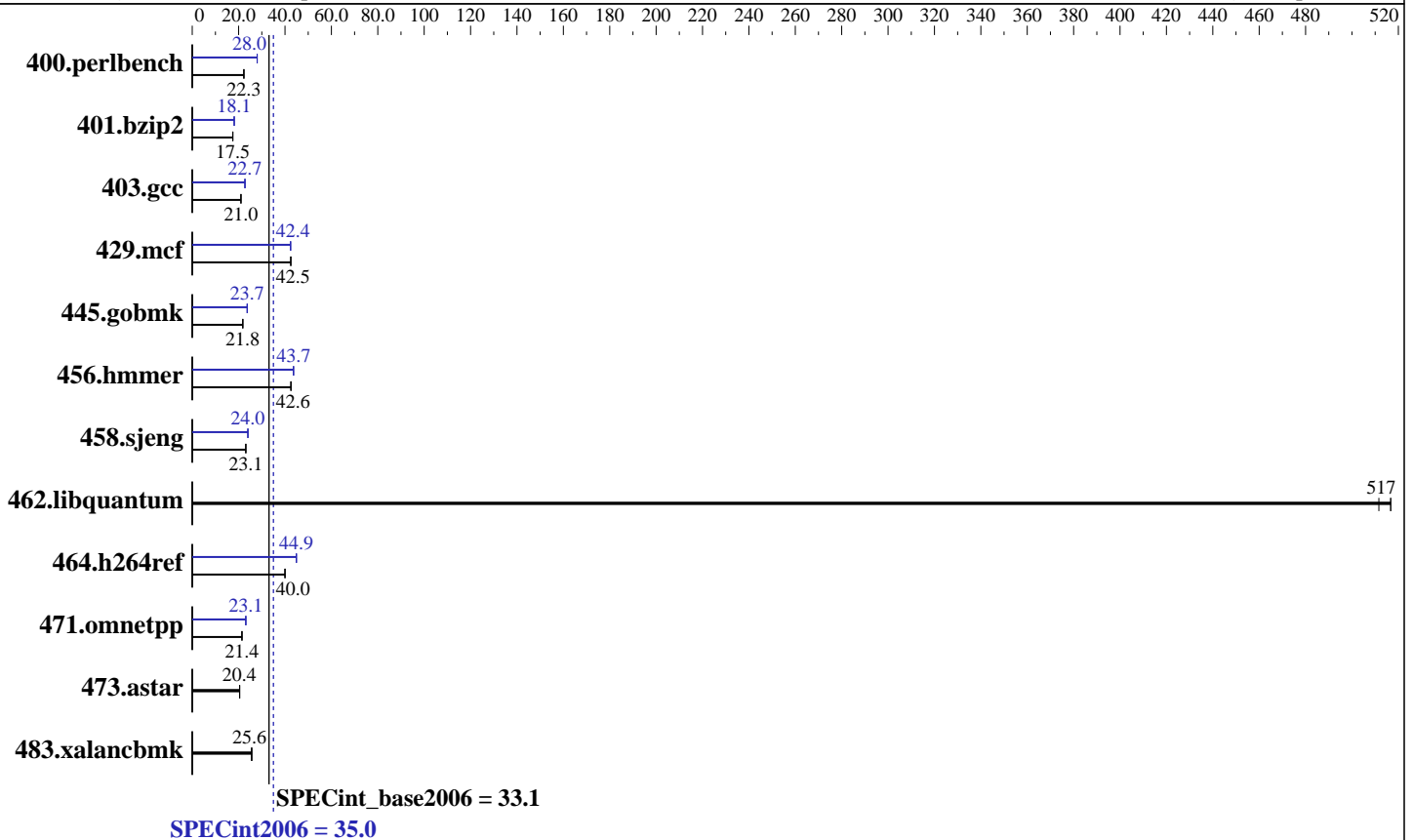
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i7-930
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (3 x 4 GB 2Rx8 PC3-8600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.163 Build 20110217
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	440	22.2	439	22.3	439	22.3	349	28.0	349	28.0	349	28.0
401.bzip2	552	17.5	552	17.5	552	17.5	533	18.1	533	18.1	533	18.1
403.gcc	383	21.0	384	21.0	383	21.0	353	22.8	354	22.7	355	22.7
429.mcf	215	42.5	215	42.5	215	42.5	215	42.4	215	42.4	215	42.4
445.gobmk	481	21.8	481	21.8	481	21.8	443	23.7	443	23.7	443	23.7
456.hmmer	219	42.6	220	42.5	219	42.6	214	43.6	214	43.7	213	43.7
458.sjeng	524	23.1	524	23.1	524	23.1	504	24.0	503	24.0	504	24.0
462.libquantum	40.1	517	40.5	512	40.1	517	40.1	517	40.5	512	40.1	517
464.h264ref	553	40.0	553	40.0	553	40.0	493	44.9	493	44.9	493	44.9
471.omnetpp	291	21.5	292	21.4	292	21.4	271	23.1	270	23.1	271	23.1
473.astar	343	20.5	343	20.4	343	20.4	343	20.5	343	20.4	343	20.4
483.xalancbmk	269	25.7	270	25.6	269	25.6	269	25.7	270	25.6	269	25.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply
 OMP_NUM_THREADS set to number of processors cores
 KMP_AFFINITY set to granularity=fine,scatter
 System was configured with an ATI HD5770 discrete graphics card

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
 -DSPEC_CPU_NO_NEED_VA_COPY

401.bzip2: -DSPEC_CPU_P64

403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64

429.mcf: -DSPEC_CPU_P64

445.gobmk: -DSPEC_CPU_P64

456.hmmer: -DSPEC_CPU_P64

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Base Portability Flags (Continued)

```

458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

```

Base Optimization Flags

C benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000

```

C++ benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F512000000 shlw64M.lib
-link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
-Qauto-ilp32 /F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qauto-ilp32 /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F512000000

456.hmmr: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlw64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml>

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For other inquiries, please contact webmaster@spec.org.

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