



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 742

PowerEdge M915 (AMD Opteron 6172, 2.10 GHz)

SPECint_rate_base2006 = 657

CPU2006 license: 55

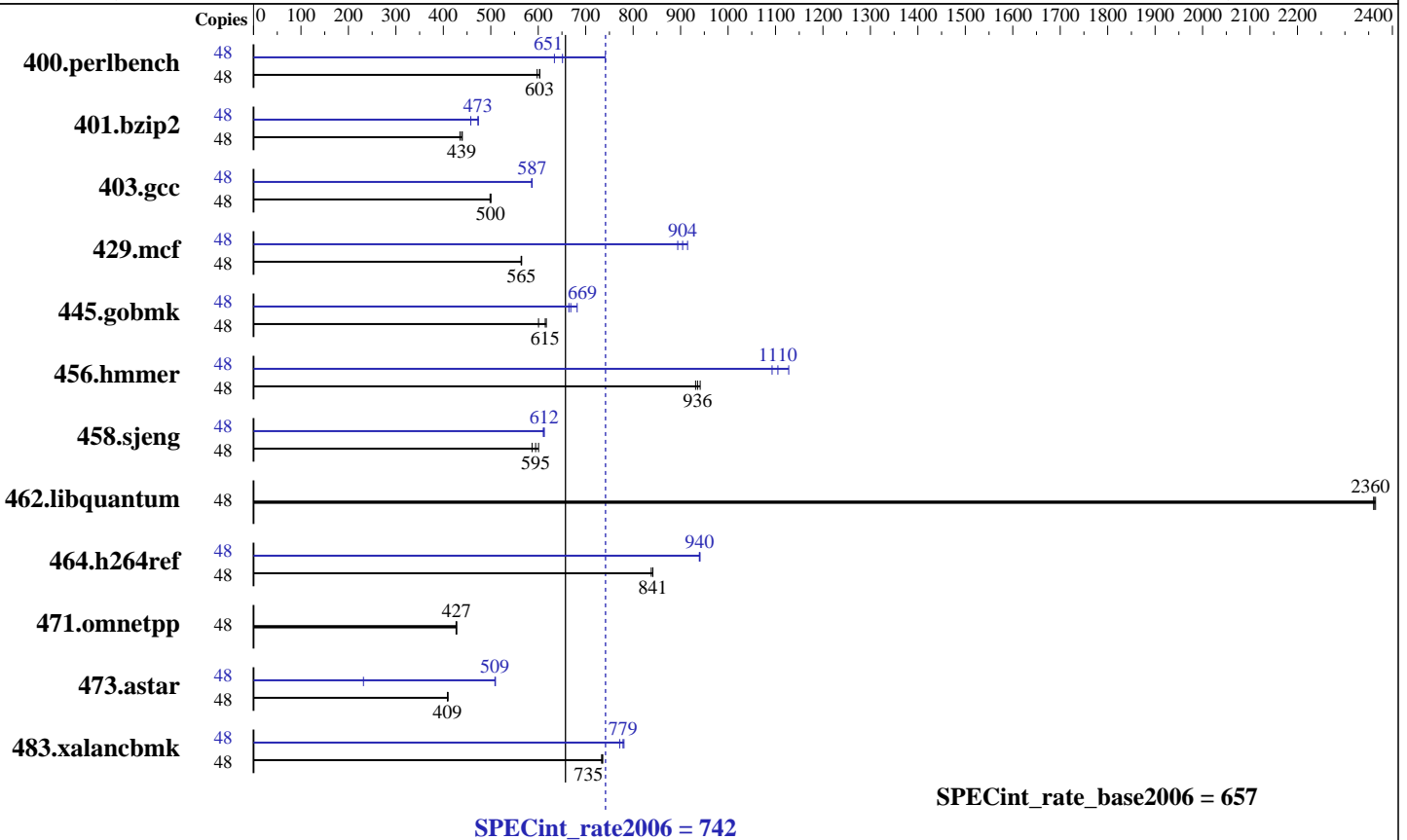
Test date: Jun-2011

Test sponsor: Dell Inc.

Hardware Availability: Jun-2011

Tested by: Dell Inc.

Software Availability: Jul-2010



Hardware

CPU Name: AMD Opteron 6172
 CPU Characteristics:
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores
 Other Cache: None
 Memory: 128 GB (32 x 4 GB 2Rx4 PC3L-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	<u>778</u>	<u>603</u>	777	603	785	598	48	633	741	740	634	<u>720</u>	<u>651</u>
401.bzip2	48	1065	435	1053	440	<u>1056</u>	<u>439</u>	48	1012	458	978	474	<u>980</u>	<u>473</u>
403.gcc	48	773	500	<u>773</u>	<u>500</u>	775	498	48	659	586	658	587	<u>658</u>	<u>587</u>
429.mcf	48	<u>775</u>	<u>565</u>	775	565	776	564	48	490	894	<u>484</u>	<u>904</u>	479	915
445.gobmk	48	816	617	838	601	<u>819</u>	<u>615</u>	48	<u>753</u>	<u>669</u>	758	664	738	682
456.hammer	48	481	932	<u>479</u>	<u>936</u>	476	941	48	<u>405</u>	<u>1110</u>	410	1090	397	1130
458.sjeng	48	<u>977</u>	<u>595</u>	989	587	967	601	48	948	613	<u>949</u>	<u>612</u>	951	611
462.libquantum	48	421	2360	<u>421</u>	<u>2360</u>	421	2360	48	421	2360	<u>421</u>	<u>2360</u>	421	2360
464.h264ref	48	<u>1263</u>	<u>841</u>	1262	842	1267	838	48	1131	940	1129	941	<u>1130</u>	<u>940</u>
471.omnetpp	48	700	428	<u>702</u>	<u>427</u>	703	427	48	700	428	<u>702</u>	<u>427</u>	703	427
473.astar	48	<u>824</u>	<u>409</u>	825	409	822	410	48	<u>662</u>	<u>509</u>	1455	232	661	510
483.xalancbmk	48	<u>451</u>	<u>735</u>	451	734	450	736	48	429	772	<u>425</u>	<u>779</u>	424	781

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=21600 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

General Notes

Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1002-rate-libs-revC/64:/root/cpu2006-1.1/amd1002-rate-libs-revC/32"
The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
<http://developer.amd.com/cpu/open64>
Binaries were compiled on SLES10 SP2 with binutils 2.18

Base Compiler Invocation

C benchmarks:
openc

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Base Compiler Invocation (Continued)

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
 -OPT:unroll_times_max=8 -OPT:unroll_size=256
 -OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
 -CG:local_sched_alg=1 -CG:unroll_fb_req=on
 -HP:bd=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
 -OPT:goto=off -CG:local_sched_alg=1 -HP:bd=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
 -LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
 -HP:bd=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
 -CG:gcm=off -GRA:prioritize_by_density=on -m32
 -HP:bd=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
 -OPT:unroll_times_max=8 -OPT:unroll_size=256
 -OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
 -IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
 -LNO:ignore_feedback=off -CG:p2align=on
 -CG:unroll_fb_req=on -HP:bd=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
 -OPT:alias=disjoint -OPT:unroll_times_max=8
 -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
 -CG:local_sched_alg=1 -CG:cflow=0
 -CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
 -HP:bd=2m:heap=2m

458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
 -LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
 -IPA:pu_reorder=2 -CG:ptr_load_use=0

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Peak Optimization Flags (Continued)

458.sjeng (continued):

-OPT:unroll_times_max=8 -INLINE:aggressive=on

462.libquantum: basepeak = yes

464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)

-fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000

-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0

-CG:push_pop_int_saved_regs=off

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)

-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off

-WOPT:if_conv=0 -GRA:optimize_boundary=on

-OPT:alias=disjoint -INLINE:aggressive=on

-IPA:small_pu=3000 -IPA:plimit=3000 -m32

-HP:bd=2m:heap=2m

483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32

-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off

-fno-emit-exceptions

-L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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