



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>\_rate2006 = 488

PowerEdge M915 (AMD Opteron 6132 HE, 2.20 GHz)

SPECfp\_rate\_base2006 = 440

CPU2006 license: 55

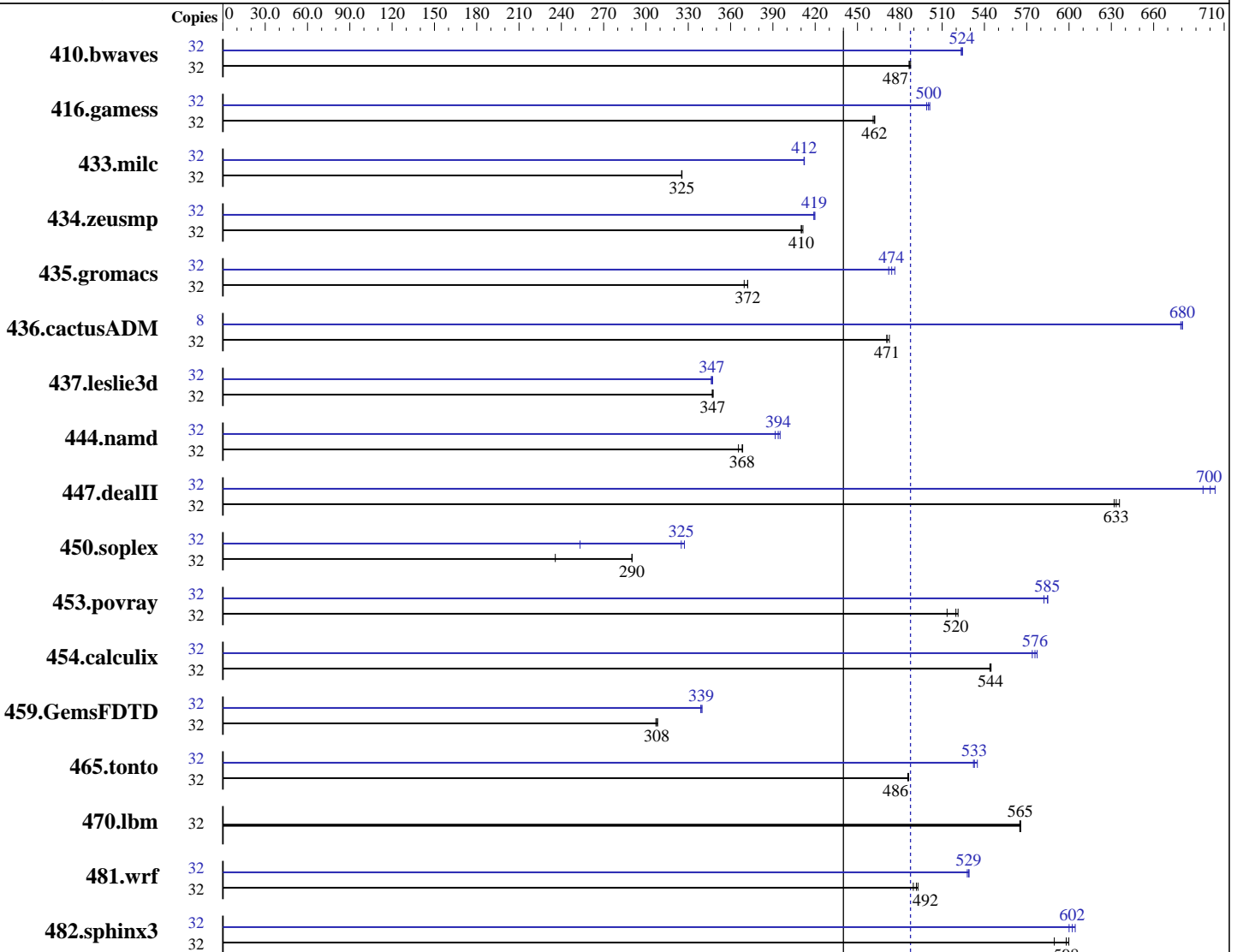
Test date: May-2011

Test sponsor: Dell Inc.

Hardware Availability: Jun-2011

Tested by: Dell Inc.

Software Availability: Jul-2010



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### Hardware

CPU Name: AMD Opteron 6132 HE  
 CPU Characteristics:  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores  
Other Cache: None  
Memory: 128 GB (32 x 4 GB 2Rx4 PC3L-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 10000 RPM SAS  
Other Hardware: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	<b>893</b>	<b>487</b>	892	488	894	487	32	<b>830</b>	<b>524</b>	831	523	829	524
416.gamess	32	1359	461	<b>1356</b>	<b>462</b>	1355	462	32	<b>1252</b>	<b>500</b>	1255	499	1250	501
433.milc	32	902	326	<b>903</b>	<b>325</b>	903	325	32	712	412	713	412	<b>713</b>	<b>412</b>
434.zeusmp	32	<b>710</b>	<b>410</b>	710	410	708	411	32	695	419	<b>695</b>	<b>419</b>	694	420
435.gromacs	32	618	370	614	372	<b>614</b>	<b>372</b>	32	<b>482</b>	<b>474</b>	480	476	484	472
436.cactusADM	32	809	473	812	471	<b>812</b>	<b>471</b>	8	140	680	141	679	<b>141</b>	<b>680</b>
437.leslie3d	32	867	347	<b>866</b>	<b>347</b>	865	348	32	869	346	<b>868</b>	<b>347</b>	866	347
444.namd	32	702	366	696	369	<b>697</b>	<b>368</b>	32	655	392	650	395	<b>652</b>	<b>394</b>
447.dealII	32	576	636	579	632	<b>578</b>	<b>633</b>	32	527	695	<b>523</b>	<b>700</b>	520	704
450.soplex	32	1132	236	920	290	<b>920</b>	<b>290</b>	32	1054	253	<b>821</b>	<b>325</b>	815	327
453.povray	32	332	514	<b>328</b>	<b>520</b>	327	521	32	292	582	291	585	<b>291</b>	<b>585</b>
454.calculix	32	<b>485</b>	<b>544</b>	485	545	485	544	32	460	574	457	577	<b>458</b>	<b>576</b>
459.GemsFDTD	32	1106	307	1101	308	<b>1104</b>	<b>308</b>	32	<b>1001</b>	<b>339</b>	999	340	1002	339
465.tonto	32	<b>648</b>	<b>486</b>	648	486	648	486	32	592	532	<b>591</b>	<b>533</b>	589	535
470.lbm	32	778	565	777	566	<b>778</b>	<b>565</b>	32	778	565	777	566	<b>778</b>	<b>565</b>
481.wrf	32	730	490	725	493	<b>727</b>	<b>492</b>	32	677	528	<b>676</b>	<b>529</b>	675	529
482.sphinx3	32	1058	590	<b>1043</b>	<b>598</b>	1040	600	32	1039	600	<b>1035</b>	<b>602</b>	1032	604

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr\_hugepages=28672 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages



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## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "896"

LD\_LIBRARY\_PATH = "/root/cpu2006-1.1./amd1002-rate-libs-revC/64:/root/cpu2006-1.1./amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on SLES10 SP2 with binutils 2.18 OMP\_NUM\_THREADS = "4"

## Base Compiler Invocation

C benchmarks:

openc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

openc openf95

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64  
 436.cactusADM: -DSPEC\_CPU\_LP64 -fno-second-underscore  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX -DSPEC\_CPU\_CASE\_FLAG  
 -fno-second-underscore  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-march=barcelona -mso -Ofast -OPT:malloc\_alg=1 -HP:bdt=2m

C++ benchmarks:

-march=barcelona -mso -Ofast -static -INLINE:aggressive=on  
-OPT:malloc\_alg=1 -HP:bdt=2m

Fortran benchmarks:

-march=barcelona -mso -Ofast -HP

Benchmarks using both Fortran and C:

-march=barcelona -mso -Ofast -OPT:malloc\_alg=1 -HP:bdt=2m -HP

## Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64  
436.cactusADM: -DSPEC\_CPU\_LP64 -fno-second-underscore  
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444.namd: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
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## Peak Optimization Flags

### C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1  
-CG:local\_sched\_alg=1 -CG:locs\_shallow\_depth=1  
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -OPT:malloc\_alg=2  
-CG:sse\_cse\_regs=0 -CG:locs\_shallow\_depth=1 -CG:cmp\_peep=on  
-CG:local\_sched\_alg=1 -INLINE:aggressive=on

### C++ benchmarks:

444.namd: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -LNO:ignore\_feedback=off  
-CG:local\_sched\_alg=2 -CG:load\_exe=0 -CG:compute\_to=on  
-OPT:unroll\_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.deallI: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on  
-LNO:opt=0 -fno-emit-exceptions -m32  
-OPT:unroll\_times\_max=8 -OPT:unroll\_size=256  
-OPT:unroll\_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on  
-CG:cmp\_peep=on -TENV:frame\_pointer=off

450.soplex: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -O3 -INLINE:aggressive=on  
-OPT:IEEE\_arith=3 -OPT:IEEE\_NaN\_Inf=off  
-OPT:fold\_unsigned\_relops=on -OPT:malloc\_alg=1  
-CG:load\_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

### Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on  
-LNO:blocking=off -LNO:prefetch\_ahead=5  
-LNO:ignore\_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m  
-CG:cmp\_peep=on

416.gamess: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0  
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll\_size=256  
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off  
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll\_size=256  
-CG:cmp\_peep=on -GRA:prioritize\_by\_density=on -HP

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## Peak Optimization Flags (Continued)

437.leslie3d: -march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2  
-LNO:prefetch\_ahead=1 -CG:load\_exe=0 -CG:local\_sched\_alg=1  
-HP

465.tonto: -march=barcelona -mso -Ofast  
-OPT:alias=no\_f90\_pointer\_alias -LNO:blocking=off  
-CG:load\_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2  
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch\_ahead=1  
-HP:bdt=2m:heap=2m -LANG:heap\_allocation\_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load\_exe=0  
-CG:ptr\_load\_use=0 -CG:local\_sched\_alg=2 -CG:compute\_to=on  
-LNO:prefetch\_ahead=30 -WOPT:unroll=2  
-GRA:optimize\_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off  
-LNO:prefetch\_ahead=10 -LANG:copyinout=off  
-IPA:callee\_limit=5000 -GRA:prioritize\_by\_density=on -m3dnow  
-HP

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>  
<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>  
<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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