



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = Not Run

PowerEdge R810 (Intel Xeon E7-2803, 1.73 GHz)

SPECint\_rate\_base2006 = 216

CPU2006 license: 55

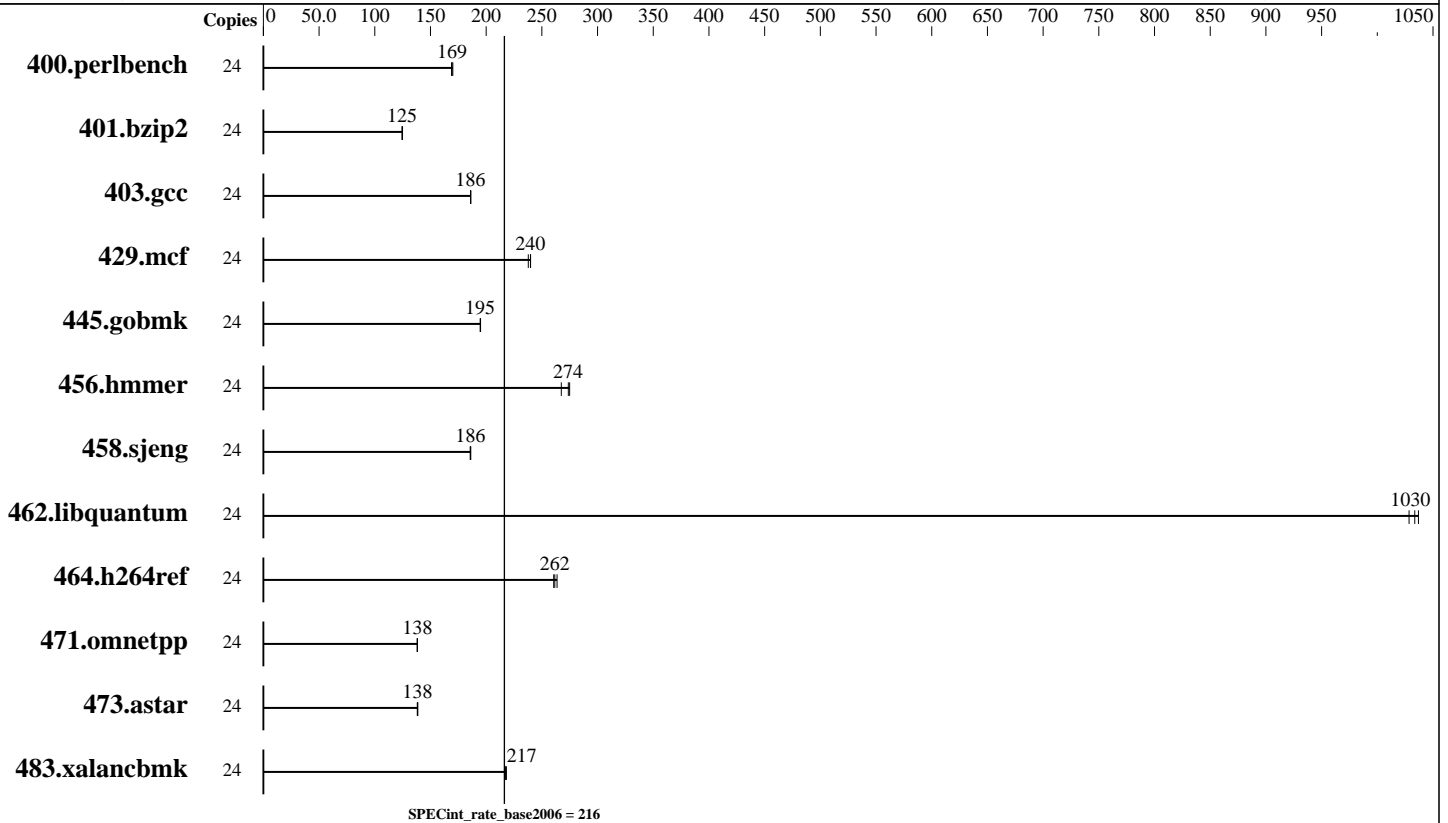
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011



## Hardware

CPU Name: Intel Xeon E7-2803  
 CPU Characteristics: 1733  
 CPU MHz: 1733  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 18 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC, running at 800 MHz)  
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b><u>1385</u></b>	<b><u>169</u></b>	1388	169	1379	170							
401.bzip2	24	1854	125	<b><u>1858</u></b>	<b><u>125</u></b>	1860	125							
403.gcc	24	1038	186	<b><u>1038</u></b>	<b><u>186</u></b>	1038	186							
429.mcf	24	<b><u>913</u></b>	<b><u>240</u></b>	912	240	920	238							
445.gobmk	24	1291	195	<b><u>1293</u></b>	<b><u>195</u></b>	1293	195							
456.hammer	24	<b><u>818</u></b>	<b><u>274</u></b>	837	267	814	275							
458.sjeng	24	1560	186	1564	186	<b><u>1562</u></b>	<b><u>186</u></b>							
462.libquantum	24	483	1030	<b><u>481</u></b>	<b><u>1030</u></b>	480	1040							
464.h264ref	24	2015	264	<b><u>2030</u></b>	<b><u>262</u></b>	2038	261							
471.omnetpp	24	1086	138	1085	138	<b><u>1085</u></b>	<b><u>138</u></b>							
473.astar	24	1216	139	<b><u>1217</u></b>	<b><u>138</u></b>	1219	138							
483.xalancbmk	24	763	217	759	218	<b><u>761</u></b>	<b><u>217</u></b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 10800> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)

## General Notes

Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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