



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge R810 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 438

CPU2006 license: 55

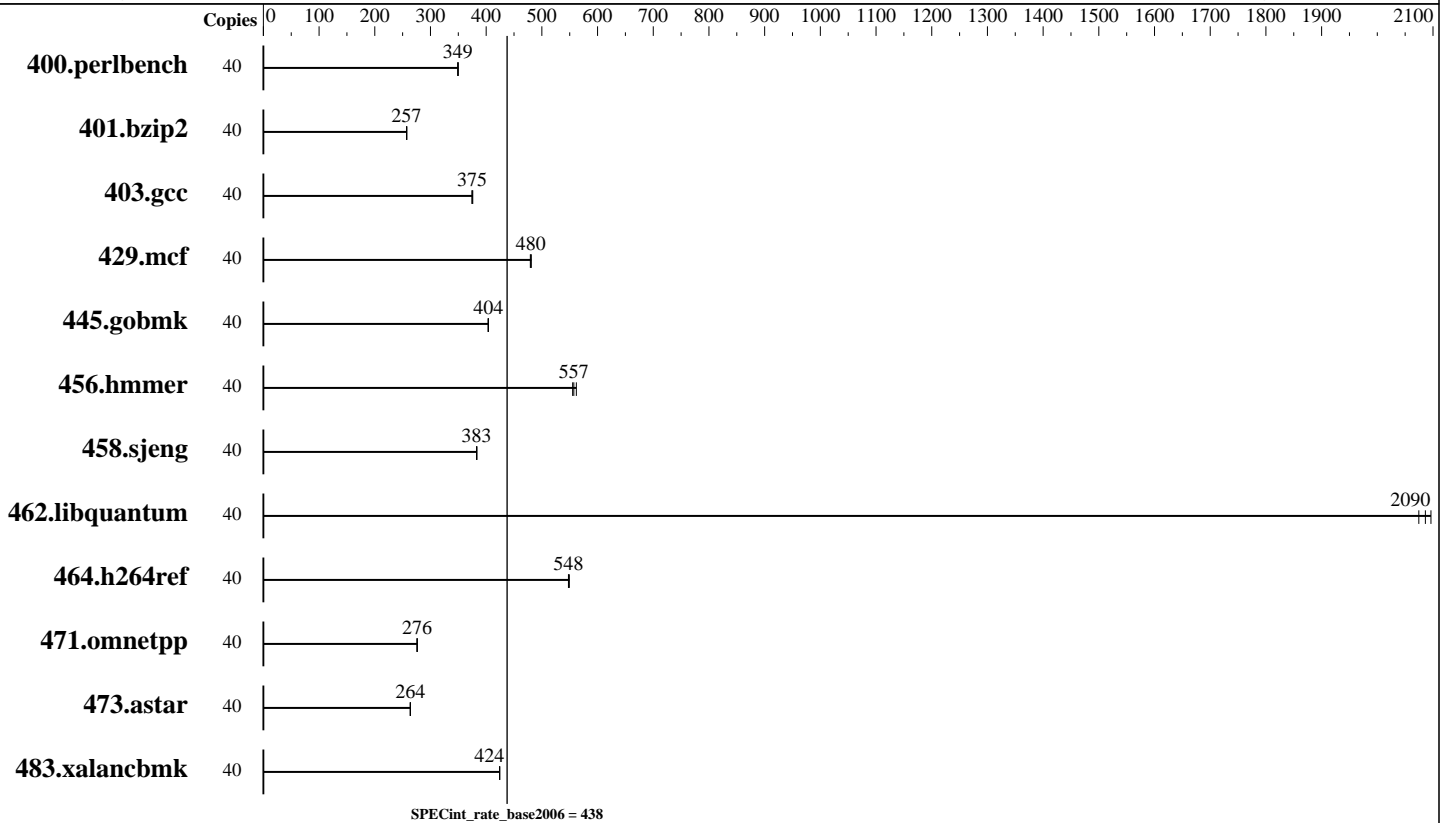
Test date: Feb-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E7-8867L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC)
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	1117	350	<u>1119</u>	<u>349</u>	1120	349							
401.bzip2	40	1499	258	1501	257	<u>1501</u>	<u>257</u>							
403.gcc	40	<u>859</u>	<u>375</u>	857	376	860	375							
429.mcf	40	758	481	761	479	<u>760</u>	<u>480</u>							
445.gobmk	40	1038	404	1040	403	<u>1038</u>	<u>404</u>							
456.hammer	40	672	555	664	562	<u>670</u>	<u>557</u>							
458.sjeng	40	<u>1264</u>	<u>383</u>	1264	383	1262	383							
462.libquantum	40	400	2070	395	2100	<u>397</u>	<u>2090</u>							
464.h264ref	40	1616	548	1612	549	<u>1615</u>	<u>548</u>							
471.omnetpp	40	905	276	907	276	<u>905</u>	<u>276</u>							
473.astar	40	1065	264	<u>1064</u>	<u>264</u>	1063	264							
483.xalancbmk	40	<u>651</u>	<u>424</u>	651	424	651	424							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 18000> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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