



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

### SPECfp<sup>®</sup>\_rate2006 = 101

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

### SPECfp\_rate\_base2006 = 98.4

CPU2006 license: 20

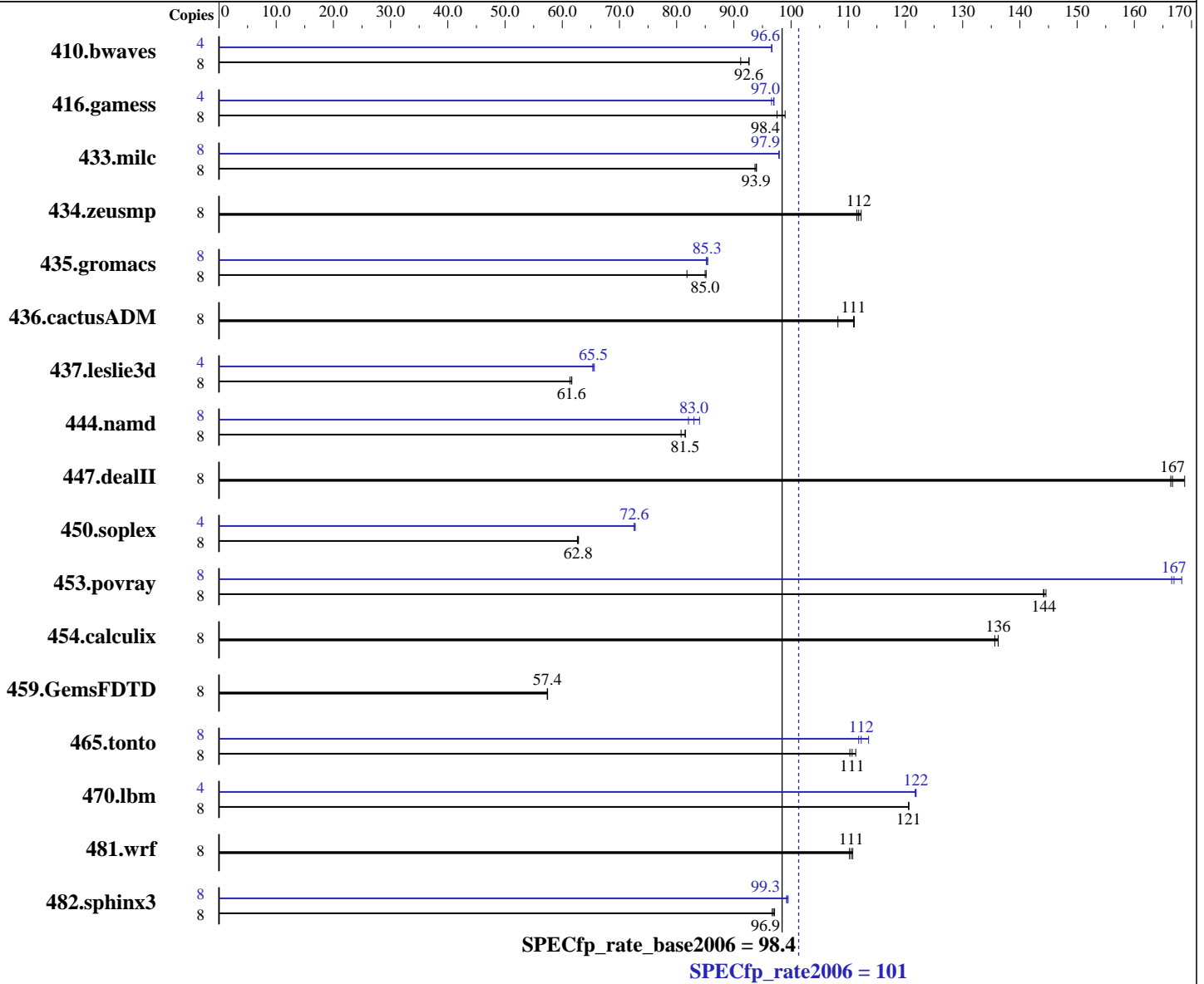
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E3-1260L  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp\_rate2006 = 101

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

SPECfp\_rate\_base2006 = 98.4

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2011

Hardware Availability: May-2011

Software Availability: Apr-2011

L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 8 GB (4 x 2 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1192	91.2	1173	92.7	<b>1174</b>	<b>92.6</b>	4	562	96.6	<b>563</b>	<b>96.6</b>	563	96.6
416.gamess	8	1606	97.5	<b>1591</b>	<b>98.4</b>	1583	99.0	4	811	96.6	<b>808</b>	<b>97.0</b>	807	97.0
433.milc	8	784	93.7	782	94.0	<b>782</b>	<b>93.9</b>	8	<b>750</b>	<b>97.9</b>	750	97.9	750	97.9
434.zeusmp	8	653	112	649	112	<b>651</b>	<b>112</b>	8	653	112	649	112	<b>651</b>	<b>112</b>
435.gromacs	8	698	81.8	671	85.2	<b>672</b>	<b>85.0</b>	8	671	85.2	<b>670</b>	<b>85.3</b>	669	85.4
436.cactusADM	8	884	108	<b>862</b>	<b>111</b>	861	111	8	884	108	<b>862</b>	<b>111</b>	861	111
437.leslie3d	8	1219	61.7	1225	61.4	<b>1220</b>	<b>61.6</b>	4	576	65.3	573	65.6	<b>574</b>	<b>65.5</b>
444.namd	8	787	81.5	794	80.8	<b>787</b>	<b>81.5</b>	8	764	84.0	<b>773</b>	<b>83.0</b>	782	82.1
447.dealII	8	542	169	<b>549</b>	<b>167</b>	550	166	8	542	169	<b>549</b>	<b>167</b>	550	166
450.soplex	8	<b>1063</b>	<b>62.8</b>	1062	62.8	1065	62.6	4	<b>459</b>	<b>72.6</b>	458	72.8	460	72.6
453.povray	8	294	145	295	144	<b>295</b>	<b>144</b>	8	<b>255</b>	<b>167</b>	253	168	256	167
454.calculix	8	<b>485</b>	<b>136</b>	487	136	485	136	8	<b>485</b>	<b>136</b>	487	136	485	136
459.GemsFDTD	8	1479	57.4	1478	57.4	<b>1478</b>	<b>57.4</b>	8	1479	57.4	1478	57.4	<b>1478</b>	<b>57.4</b>
465.tonto	8	<b>711</b>	<b>111</b>	707	111	714	110	8	704	112	693	114	<b>701</b>	<b>112</b>
470.lbm	8	<b>912</b>	<b>121</b>	912	121	912	121	4	451	122	<b>451</b>	<b>122</b>	452	122
481.wrf	8	811	110	<b>809</b>	<b>111</b>	807	111	8	811	110	<b>809</b>	<b>111</b>	807	111
482.sphinx3	8	1605	97.1	1612	96.7	<b>1609</b>	<b>96.9</b>	8	1567	99.5	<b>1570</b>	<b>99.3</b>	1572	99.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 3600> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp\_rate2006 = 101

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

SPECfp\_rate\_base2006 = 98.4

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Dell Inc.

Test date: Mar-2011  
Hardware Availability: May-2011  
Software Availability: Apr-2011

### Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)

### General Notes

The Dell PowerEdge R210 II and the Bull NovaScale R410B F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R210 II model. Binaries were compiled on RHEL5.5

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp\_rate2006 = 101

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

SPECfp\_rate\_base2006 = 98.4

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Dell Inc.

Test date: Mar-2011  
Hardware Availability: May-2011  
Software Availability: Apr-2011

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:  
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp\_rate2006 = 101

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

SPECfp\_rate\_base2006 = 98.4

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Dell Inc.

Test date: Mar-2011  
Hardware Availability: May-2011  
Software Availability: Apr-2011

## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -auto-ilp32  
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32  
447.dealII: basepeak = yes  
450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT  
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static  
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static  
434.zeusmp: basepeak = yes  
437.leslie3d: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
459.GemsFDTD: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

**SPECfp\_rate2006 = 101**

NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

**SPECfp\_rate\_base2006 = 98.4**

**CPU2006 license:** 20

**Test date:** Mar-2011

**Test sponsor:** Bull SAS

**Hardware Availability:** May-2011

**Tested by:** Dell Inc.

**Software Availability:** Apr-2011

## Peak Optimization Flags (Continued)

```

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

Benchmarks using both Fortran and C:

```

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
            -static -auto-ilp32

```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 18:14:59 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 June 2011.