



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint®2006 = 43.4

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

SPECint\_base2006 = 41.6

CPU2006 license: 20

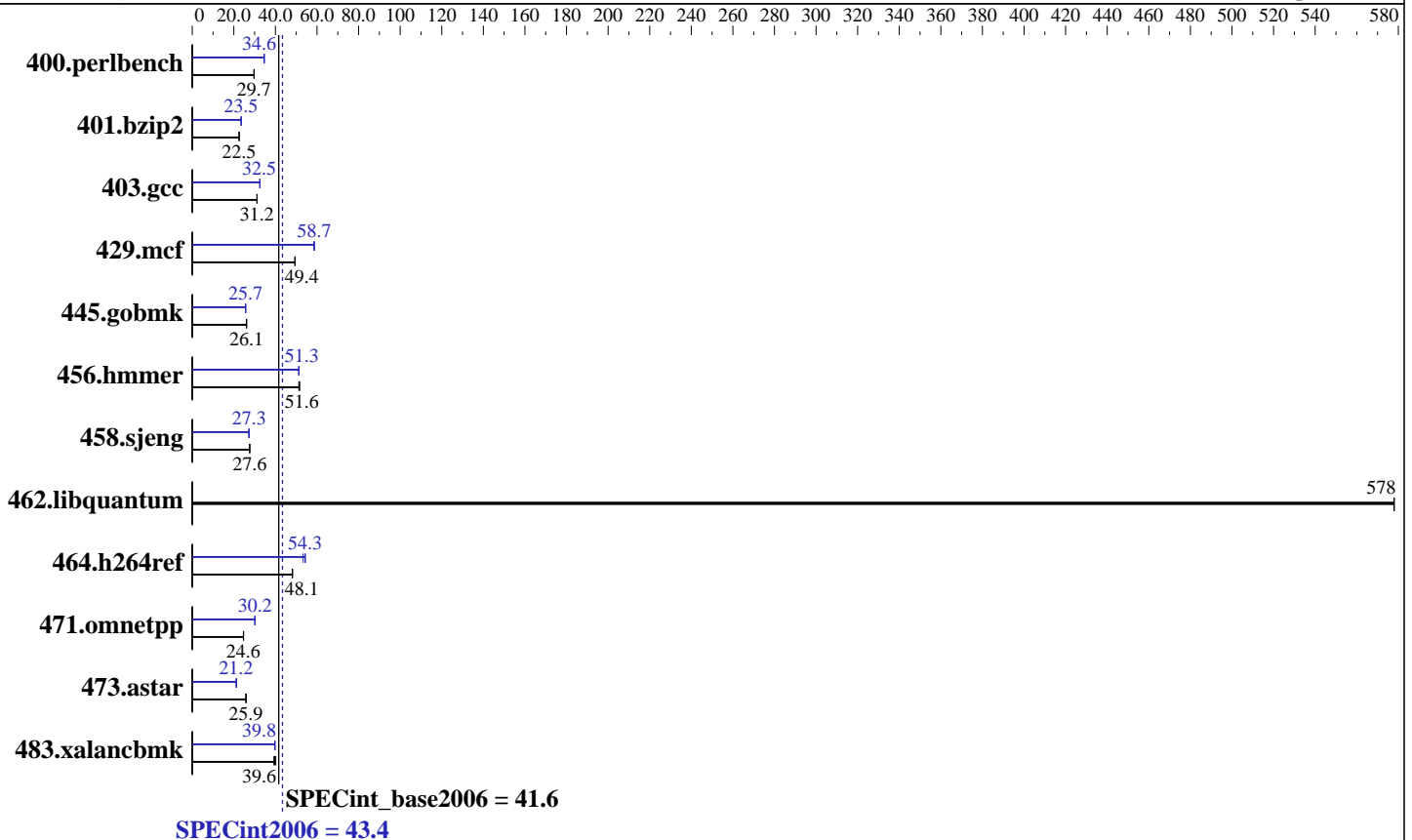
Test date: Mar-2011

Test sponsor: Bull SAS

Hardware Availability: May-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E3-1240  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (4 x 2 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 8.1 32-bit Library for Linux



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint2006 = **43.4**

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

SPECint\_base2006 = **41.6**

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Dell Inc.

Test date: Mar-2011  
Hardware Availability: May-2011  
Software Availability: Apr-2011

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	328	29.7	<b><u>329</u></b>	<b><u>29.7</u></b>	329	29.7	282	34.7	283	34.5	<b><u>282</u></b>	<b><u>34.6</u></b>
401.bzip2	<b><u>428</u></b>	<b><u>22.5</u></b>	427	22.6	428	22.5	410	23.5	<b><u>410</u></b>	<b><u>23.5</u></b>	411	23.5
403.gcc	<b><u>258</u></b>	<b><u>31.2</u></b>	258	31.2	258	31.2	247	32.6	<b><u>248</u></b>	<b><u>32.5</u></b>	248	32.5
429.mcf	185	49.4	<b><u>185</u></b>	<b><u>49.4</u></b>	184	49.4	155	58.7	<b><u>155</u></b>	<b><u>58.7</u></b>	156	58.6
445.gobmk	402	26.1	<b><u>402</u></b>	<b><u>26.1</u></b>	402	26.1	408	25.7	409	25.7	<b><u>409</u></b>	<b><u>25.7</u></b>
456.hammer	<b><u>181</u></b>	<b><u>51.6</u></b>	182	51.3	181	51.6	<b><u>182</u></b>	<b><u>51.3</u></b>	182	51.3	182	51.3
458.sjeng	438	27.6	<b><u>438</u></b>	<b><u>27.6</u></b>	437	27.7	443	27.3	<b><u>443</u></b>	<b><u>27.3</u></b>	444	27.3
462.libquantum	35.8	578	35.9	578	<b><u>35.8</u></b>	<b><u>578</u></b>	35.8	578	35.9	578	<b><u>35.8</u></b>	<b><u>578</u></b>
464.h264ref	460	48.1	458	48.3	<b><u>460</u></b>	<b><u>48.1</u></b>	415	53.3	407	54.4	<b><u>407</u></b>	<b><u>54.3</u></b>
471.omnetpp	<b><u>254</u></b>	<b><u>24.6</u></b>	254	24.6	253	24.7	207	30.1	<b><u>207</u></b>	<b><u>30.2</u></b>	207	30.2
473.astar	271	25.9	272	25.8	<b><u>272</u></b>	<b><u>25.9</u></b>	<b><u>332</u></b>	<b><u>21.2</u></b>	329	21.3	332	21.1
483.xalancbmk	172	40.2	<b><u>174</u></b>	<b><u>39.6</u></b>	176	39.3	174	39.8	174	39.7	<b><u>174</u></b>	<b><u>39.8</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Logical Processor = Disabled (Default = Enabled)

## General Notes

OMP\_NUM\_THREADS set to number of cores  
The Dell PowerEdge T110 II and  
the Bull NovaScale T810B F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge T110 II model  
Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Bull SAS**

**SPECint2006 = 43.4**

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

**SPECint\_base2006 = 41.6**

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Dell Inc.

**Test date:** Mar-2011  
**Hardware Availability:** May-2011  
**Software Availability:** Apr-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Bull SAS**

**SPECint2006 = 43.4**

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

**SPECint\_base2006 = 41.6**

**CPU2006 license:** 20

**Test date:** Mar-2011

**Test sponsor:** Bull SAS

**Hardware Availability:** May-2011

**Tested by:** Dell Inc.

**Software Availability:** Apr-2011

## Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Bull SAS**

**SPECint2006 = 43.4**

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

**SPECint\_base2006 = 41.6**

**CPU2006 license:** 20

**Test date:** Mar-2011

**Test sponsor:** Bull SAS

**Hardware Availability:** May-2011

**Tested by:** Dell Inc.

**Software Availability:** Apr-2011

## Peak Optimization Flags (Continued)

456.hmmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

**SPECint2006 = 43.4**

NovaScale T810B F2 (Intel Xeon E3-1240, 3.30 GHz)

**SPECint\_base2006 = 41.6**

**CPU2006 license:** 20

**Test sponsor:** Bull SAS

**Tested by:** Dell Inc.

**Test date:** Mar-2011

**Hardware Availability:** May-2011

**Software Availability:** Apr-2011

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 18:08:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 7 June 2011.