



# SPEC® CINT2006 Result

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## IBM Corporation

SPECint®\_rate2006 = 511

### IBM BladeCenter HX5 (Intel Xeon E7-2860)

SPECint\_rate\_base2006 = 477

CPU2006 license: 11

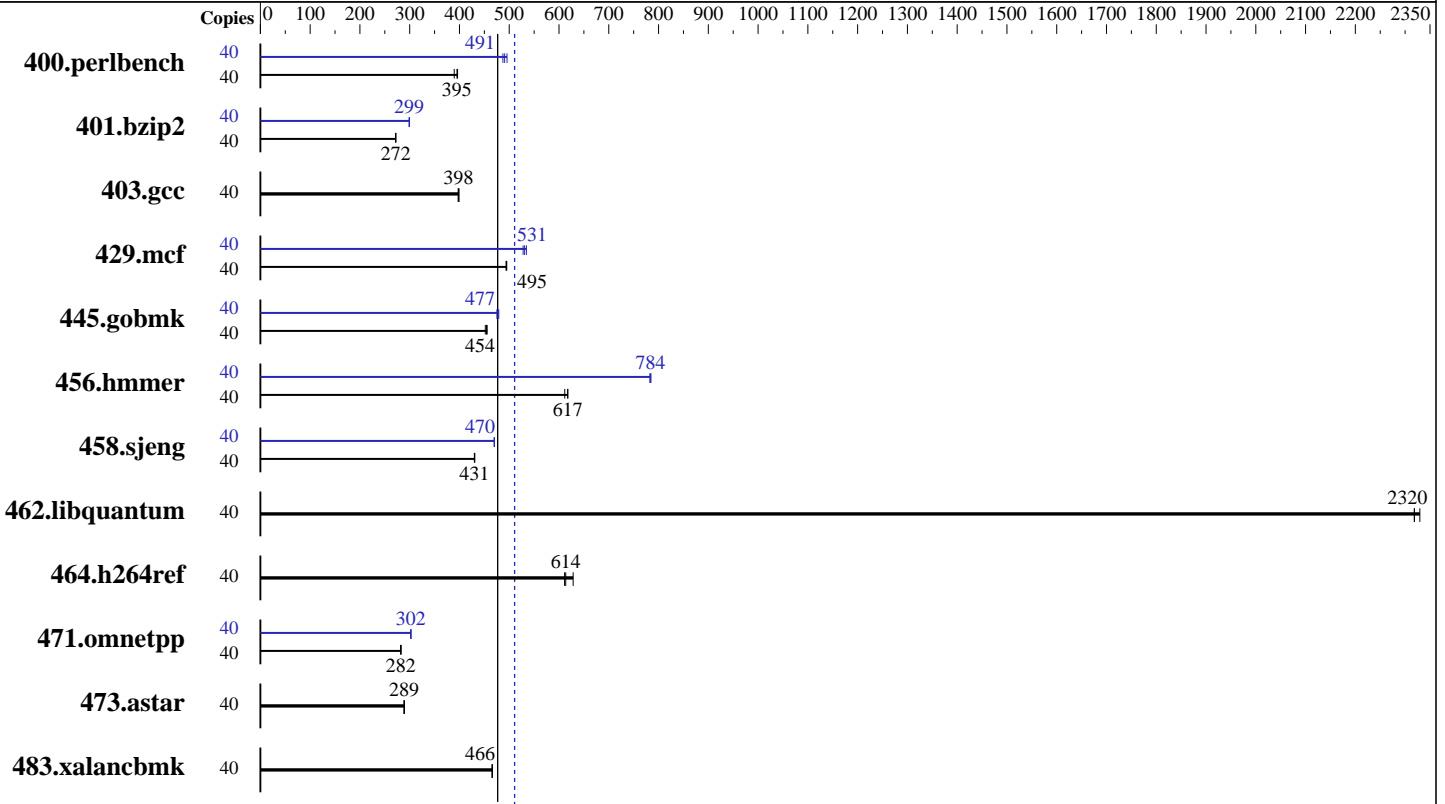
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2011

Hardware Availability: May-2011

Software Availability: Jan-2011



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### Hardware

CPU Name: Intel Xeon E7-2860  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
 Disk Subsystem: 2 x 50 GB SSD, RAID 0  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	986	396	1003	389	<u>989</u>	<u>395</u>	40	802	487	<u>796</u>	<u>491</u>	789	496
401.bzip2	40	1422	271	<u>1419</u>	<u>272</u>	1418	272	40	1291	299	1288	300	<u>1291</u>	<u>299</u>
403.gcc	40	807	399	<u>810</u>	<u>398</u>	810	398	40	807	399	<u>810</u>	<u>398</u>	810	398
429.mcf	40	<u>737</u>	<u>495</u>	739	494	737	495	40	<u>687</u>	<u>531</u>	691	528	683	535
445.gobmk	40	921	456	928	452	<u>924</u>	<u>454</u>	40	876	479	<u>879</u>	<u>477</u>	883	475
456.hammer	40	610	612	604	618	<u>604</u>	<u>617</u>	40	<u>476</u>	<u>784</u>	477	783	476	785
458.sjeng	40	1124	431	1126	430	<u>1124</u>	<u>431</u>	40	1030	470	1029	470	<u>1030</u>	<u>470</u>
462.libquantum	40	356	2330	<u>357</u>	<u>2320</u>	357	2320	40	356	2330	<u>357</u>	<u>2320</u>	357	2320
464.h264ref	40	1409	628	<u>1443</u>	<u>614</u>	1448	611	40	1409	628	<u>1443</u>	<u>614</u>	1448	611
471.omnetpp	40	885	283	<u>885</u>	<u>282</u>	887	282	40	827	302	<u>827</u>	<u>302</u>	826	302
473.aster	40	972	289	<u>972</u>	<u>289</u>	970	289	40	972	289	<u>972</u>	<u>289</u>	970	289
483.xalancbmk	40	593	466	<u>592</u>	<u>466</u>	592	466	40	593	466	<u>592</u>	<u>466</u>	592	466

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 26000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Turbo Boost Power Optimization set to Traditional

## General Notes

Binaries were compiled on RHEL5.5



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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