



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 40.0**

PRIMERGY TX120 S3, Intel Core i3-2120, 3.30 GHz

**SPECint\_base2006 = 38.3**

CPU2006 license: 19

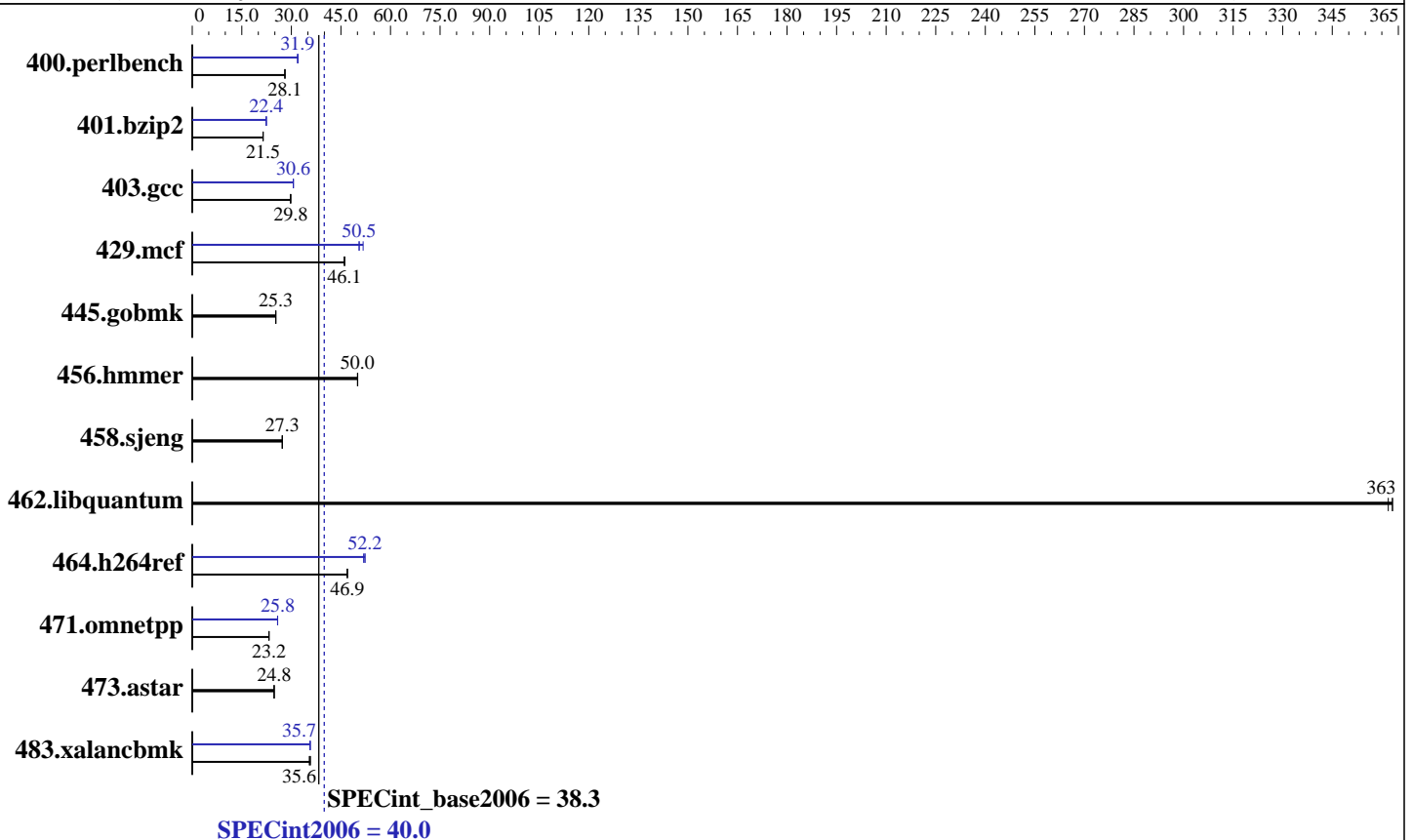
Test date: May-2011

Test sponsor: Fujitsu

Hardware Availability: Jun-2011

Tested by: Fujitsu

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Core i3-2120  
 CPU Characteristics:  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	348	28.1	349	28.0	<b>348</b>	<b>28.1</b>	306	31.9	306	32.0	<b>306</b>	<b>31.9</b>
401.bzip2	450	21.5	<b>450</b>	<b>21.5</b>	450	21.4	431	22.4	431	22.4	<b>431</b>	<b>22.4</b>
403.gcc	<b>270</b>	<b>29.8</b>	270	29.8	271	29.7	263	30.6	263	30.6	<b>263</b>	<b>30.6</b>
429.mcf	198	46.0	<b>198</b>	<b>46.1</b>	198	46.1	176	51.7	<b>180</b>	<b>50.5</b>	181	50.5
445.gobmk	415	25.3	<b>415</b>	<b>25.3</b>	415	25.3	415	25.3	<b>415</b>	<b>25.3</b>	415	25.3
456.hammer	<b>187</b>	<b>50.0</b>	186	50.1	187	50.0	<b>187</b>	<b>50.0</b>	186	50.1	187	50.0
458.sjeng	444	27.3	<b>444</b>	<b>27.3</b>	444	27.2	444	27.3	<b>444</b>	<b>27.3</b>	444	27.2
462.libquantum	57.2	362	<b>57.0</b>	<b>363</b>	57.0	363	57.2	362	<b>57.0</b>	<b>363</b>	57.0	363
464.h264ref	472	46.9	471	47.0	<b>472</b>	<b>46.9</b>	423	52.3	427	51.9	<b>424</b>	<b>52.2</b>
471.omnetpp	269	23.2	<b>269</b>	<b>23.2</b>	269	23.3	<b>242</b>	<b>25.8</b>	242	25.8	242	25.9
473.astar	283	24.8	<b>283</b>	<b>24.8</b>	283	24.8	283	24.8	<b>283</b>	<b>24.8</b>	283	24.8
483.xalancbmk	193	35.8	195	35.4	<b>194</b>	<b>35.6</b>	193	35.7	<b>193</b>	<b>35.7</b>	194	35.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable

## General Notes

OMP\_NUM\_THREADS set to number of cores  
For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5  
This result was measured on the PRIMERGY TX140 S1. The PRIMERGY TX120 S3 and the PRIMERGY TX140 S1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32

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## Peak Compiler Invocation (Continued)

429.mcf: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: basepeak = yes

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## Peak Optimization Flags (Continued)

456.hmmr: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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