SPEC® CFP2006 Result

Fujitsu

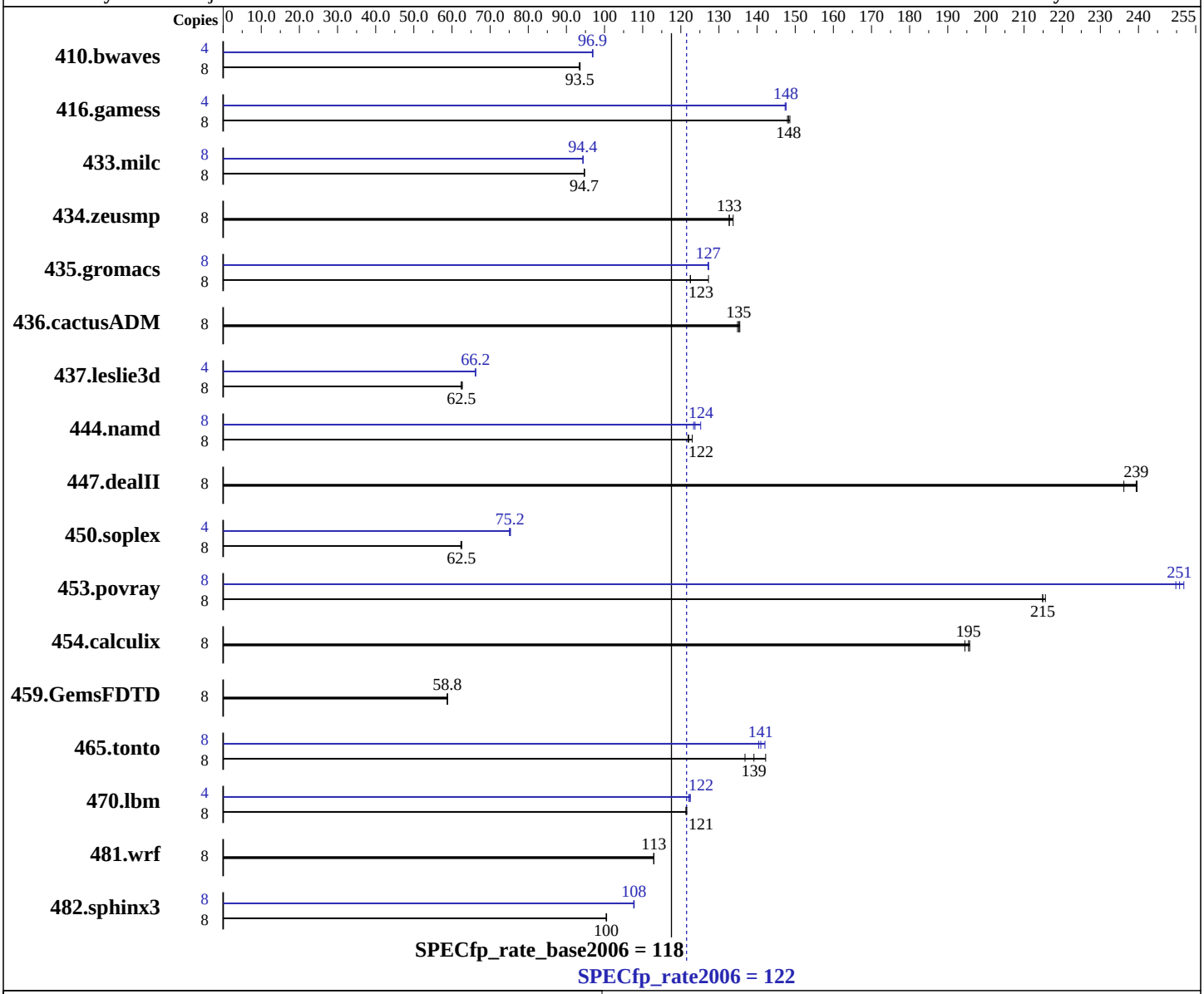
CELSIUS W510, Intel Xeon E3-1280

SPECfp®_rate2006 = 122

SPECfp_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E3-1280
CPU Characteristics:	Intel Turbo Boost Technology up to 3.9 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, kernel 2.6.32.12-0.6-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.2.137 Build 20110112
Auto Parallel:	No
File System:	ext3
System State:	Run Level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9, non-ECC)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1164	93.4	1161	93.6	**1163**	**93.5**	4	562	96.8	561	97.0	**561**	**96.9**
416.gamess	8	1054	149	**1057**	**148**	1059	148	4	531	148	**531**	**148**	532	147
433.milc	8	775	94.7	**775**	**94.7**	776	94.7	8	**778**	**94.4**	778	94.3	778	94.4
434.zeusmp	8	544	134	549	133	**549**	**133**	8	544	134	549	133	**549**	**133**
435.gromacs	8	449	127	**466**	**123**	466	122	8	449	127	449	127	**449**	**127**
436.cactusADM	8	706	135	709	135	**707**	**135**	8	706	135	709	135	**707**	**135**
437.leslie3d	8	1199	62.7	**1203**	**62.5**	1204	62.4	4	569	66.1	568	66.2	**568**	**66.2**
444.namd	8	526	122	522	123	**526**	**122**	8	512	125	**519**	**124**	520	123
447.dealII	8	382	240	388	236	**382**	**239**	8	382	240	388	236	**382**	**239**
450.soplex	8	**1067**	**62.5**	1067	62.5	1071	62.3	4	**443**	**75.2**	444	75.1	443	75.3
453.povray	8	198	215	197	216	**198**	**215**	8	169	252	**170**	**251**	170	250
454.calculix	8	337	196	339	194	**338**	**195**	8	337	196	339	194	**338**	**195**
459.GemsFDTD	8	1443	58.8	**1444**	**58.8**	1444	58.8	8	1443	58.8	**1444**	**58.8**	1444	58.8
465.tonto	8	553	142	**566**	**139**	575	137	8	**558**	**141**	561	140	554	142
470.lbm	8	904	122	**905**	**121**	906	121	4	449	123	**450**	**122**	450	122
481.wrf	8	**791**	**113**	791	113	792	113	8	**791**	**113**	791	113	792	113
482.sphinx3	8	**1552**	**100**	1551	101	1553	100	8	1447	108	**1447**	**108**	1449	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 Huge pages were not configured for this run
```

Base Compiler Invocation

C benchmarks:

```
icc   -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Platform.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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