



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]_rate2006 = 118

Acer AT350 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 112

CPU2006 license: 97

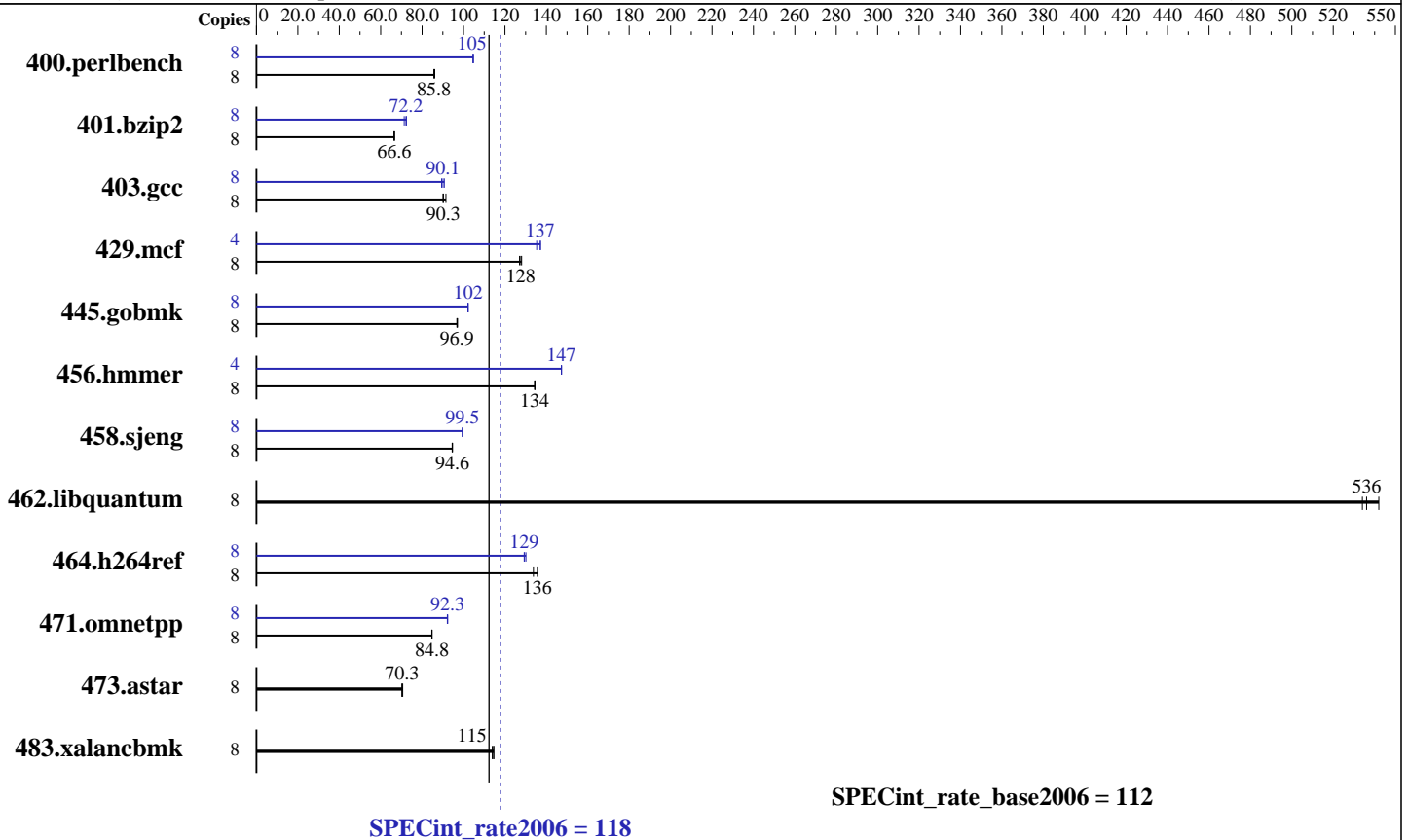
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM
 Other Hardware: None

Software

Operating System: Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32
 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	912	85.7	<u>911</u>	<u>85.8</u>	908	86.1	8	<u>747</u>	<u>105</u>	747	105	746	105
401.bzip2	8	1162	66.4	<u>1160</u>	<u>66.6</u>	1157	66.7	8	1066	72.4	1082	71.4	<u>1069</u>	<u>72.2</u>
403.gcc	8	715	90.1	704	91.5	<u>713</u>	<u>90.3</u>	8	720	89.4	710	90.7	<u>715</u>	<u>90.1</u>
429.mcf	8	574	127	570	128	<u>571</u>	<u>128</u>	4	270	135	266	137	<u>266</u>	<u>137</u>
445.gobmk	8	864	97.1	<u>866</u>	<u>96.9</u>	867	96.8	8	822	102	<u>821</u>	<u>102</u>	821	102
456.hammer	8	556	134	555	134	<u>555</u>	<u>134</u>	4	<u>253</u>	<u>147</u>	253	147	253	147
458.sjeng	8	1023	94.7	<u>1023</u>	<u>94.6</u>	1024	94.5	8	<u>973</u>	<u>99.5</u>	973	99.4	971	99.6
462.libquantum	8	310	534	<u>309</u>	<u>536</u>	306	542	8	310	534	<u>309</u>	<u>536</u>	306	542
464.h264ref	8	<u>1305</u>	<u>136</u>	1303	136	1324	134	8	<u>1368</u>	<u>129</u>	1360	130	1369	129
471.omnetpp	8	<u>590</u>	<u>84.8</u>	590	84.8	590	84.7	8	541	92.3	<u>542</u>	<u>92.3</u>	542	92.2
473.astar	8	799	70.3	795	70.6	<u>799</u>	<u>70.3</u>	8	799	70.3	795	70.6	<u>799</u>	<u>70.3</u>
483.xalancbmk	8	482	115	<u>482</u>	<u>115</u>	485	114	8	482	115	<u>482</u>	<u>115</u>	485	114

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Balanced)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
This result was measured on the Gateway GT350 F1.
The Acer AT350 F1 is electronically equivalent

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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