



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 38.5

Intel DH67BLB3 Motherboard (Intel Core i5-2310)

SPECint_base2006 = 36.5

CPU2006 license: 13

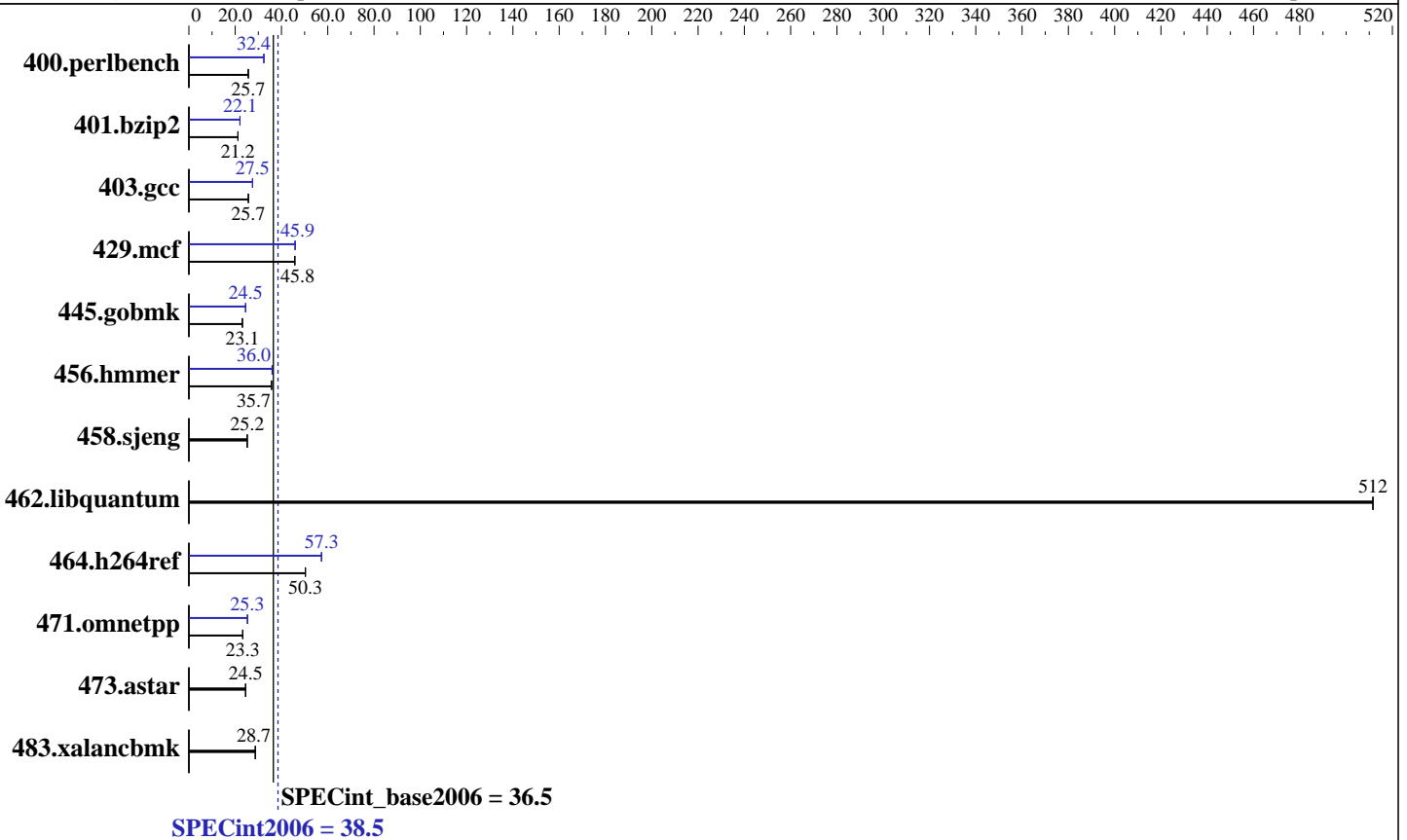
Test date: Apr-2011

Test sponsor: Intel Corporation

Hardware Availability: Jun-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i5-2310
 CPU Characteristics: Intel Turbo Boost Technology up to 3.2 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.163 Build 20110217
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	381	25.6	<u>381</u>	<u>25.7</u>	380	25.7	301	32.4	301	32.4	<u>301</u>	<u>32.4</u>
401.bzip2	455	21.2	<u>455</u>	<u>21.2</u>	455	21.2	438	22.0	437	22.1	<u>438</u>	<u>22.1</u>
403.gcc	313	25.7	314	25.7	<u>314</u>	<u>25.7</u>	293	27.5	<u>293</u>	<u>27.5</u>	293	27.5
429.mcf	199	45.7	199	45.8	<u>199</u>	<u>45.8</u>	<u>199</u>	<u>45.9</u>	199	45.9	199	45.9
445.gobmk	<u>454</u>	<u>23.1</u>	454	23.1	454	23.1	428	24.5	428	24.5	<u>428</u>	<u>24.5</u>
456.hmmer	261	35.7	<u>261</u>	<u>35.7</u>	261	35.7	260	36.0	260	35.9	<u>260</u>	<u>36.0</u>
458.sjeng	481	25.2	480	25.2	<u>480</u>	<u>25.2</u>	481	25.2	480	25.2	<u>480</u>	<u>25.2</u>
462.libquantum	40.5	512	<u>40.5</u>	<u>512</u>	40.5	512	<u>40.5</u>	<u>512</u>	<u>40.5</u>	<u>512</u>	40.5	512
464.h264ref	<u>440</u>	<u>50.3</u>	440	50.3	439	50.4	386	57.3	386	57.3	<u>386</u>	<u>57.3</u>
471.omnetpp	269	23.3	269	23.2	<u>269</u>	<u>23.3</u>	247	25.3	<u>247</u>	<u>25.3</u>	247	25.3
473.astar	287	24.5	287	24.5	<u>287</u>	<u>24.5</u>	287	24.5	287	24.5	<u>287</u>	<u>24.5</u>
483.xalancbmk	240	28.7	<u>240</u>	<u>28.7</u>	241	28.7	<u>240</u>	<u>28.7</u>	<u>240</u>	<u>28.7</u>	241	28.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

```

Base Optimization Flags

C benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000

```

C++ benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

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Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For other inquiries, please contact webmaster@spec.org.

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