



# SPEC® CINT2006 Result

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## Intel Corporation

**SPECint®2006 = 38.7**

Intel DH67BLB3 Motherboard (Intel Core i5-2500T)

**SPECint\_base2006 = 35.5**

CPU2006 license: 13

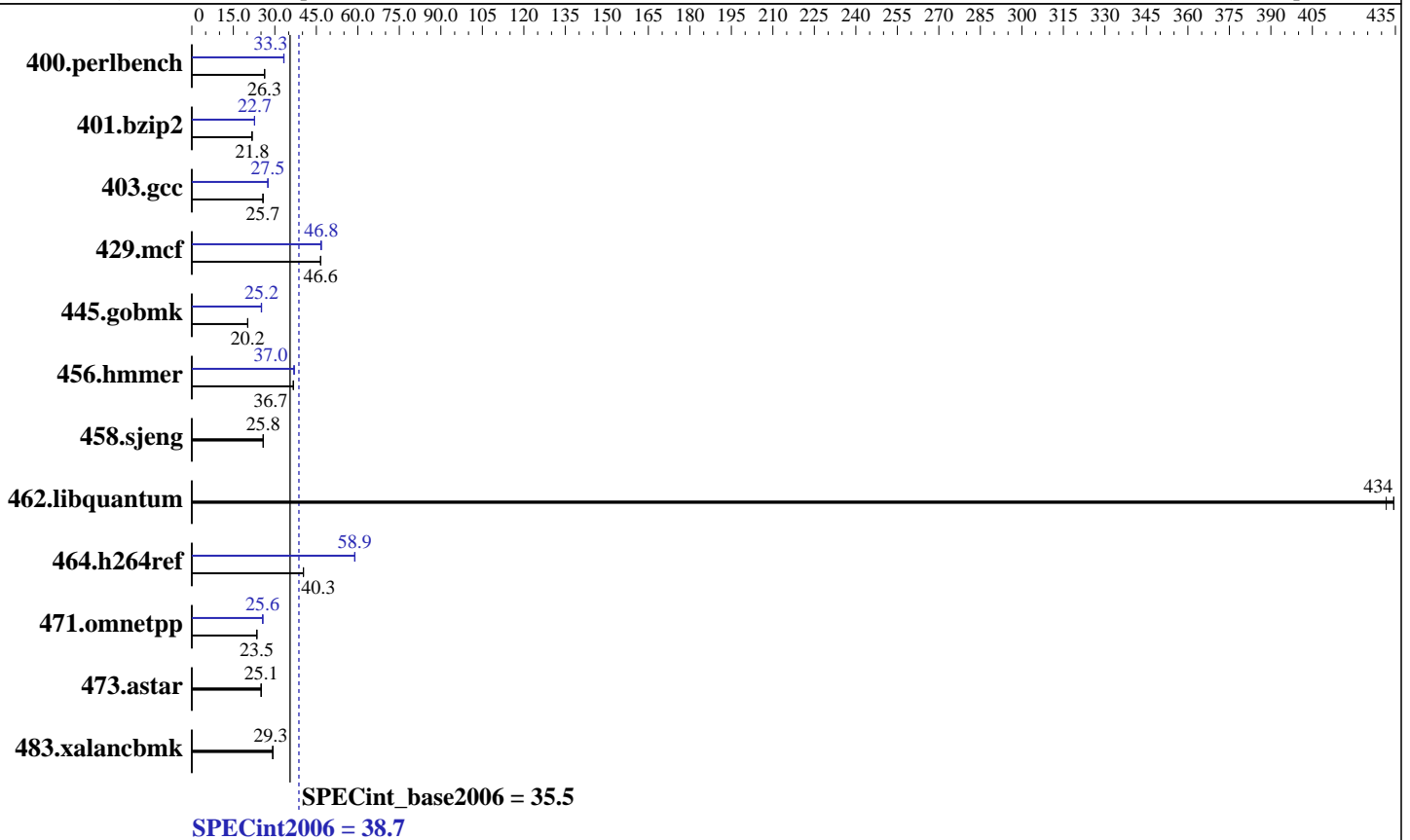
Test date: Apr-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i5-2500T  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.3 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 6 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)  
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows 7 Ultimate (64-bit)  
 Compiler: Intel C++ Compiler XE for Intel64  
 Version 12.0.3.163 Build 20110217  
 Microsoft Visual Studio 2008 Professional SP1  
 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 9.01 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>372</u></b>	<b><u>26.3</u></b>	372	26.3	372	26.3	294	33.2	<b><u>294</u></b>	<b><u>33.3</u></b>	293	33.3
401.bzip2	443	21.8	<b><u>444</u></b>	<b><u>21.8</u></b>	444	21.7	427	22.6	<b><u>426</u></b>	<b><u>22.7</u></b>	425	22.7
403.gcc	312	25.8	<b><u>313</u></b>	<b><u>25.7</u></b>	313	25.7	<b><u>293</u></b>	<b><u>27.5</u></b>	293	27.5	292	27.5
429.mcf	196	46.6	<b><u>196</u></b>	<b><u>46.6</u></b>	196	46.5	196	46.6	<b><u>195</u></b>	<b><u>46.8</u></b>	195	46.8
445.gobmk	<b><u>519</u></b>	<b><u>20.2</u></b>	519	20.2	518	20.2	417	25.1	<b><u>417</u></b>	<b><u>25.2</u></b>	416	25.2
456.hmmer	254	36.7	254	36.7	<b><u>254</u></b>	<b><u>36.7</u></b>	253	36.9	252	37.0	<b><u>252</u></b>	<b><u>37.0</u></b>
458.sjeng	468	25.8	<b><u>468</u></b>	<b><u>25.8</u></b>	469	25.8	468	25.8	<b><u>468</u></b>	<b><u>25.8</u></b>	469	25.8
462.libquantum	48.0	432	47.7	434	<b><u>47.7</u></b>	<b><u>434</u></b>	48.0	432	47.7	434	<b><u>47.7</u></b>	<b><u>434</u></b>
464.h264ref	548	40.4	<b><u>549</u></b>	<b><u>40.3</u></b>	549	40.3	376	58.9	<b><u>376</u></b>	<b><u>58.9</u></b>	376	58.9
471.omnetpp	266	23.5	266	23.5	<b><u>266</u></b>	<b><u>23.5</u></b>	244	25.6	<b><u>244</u></b>	<b><u>25.6</u></b>	244	25.7
473.astar	280	25.1	<b><u>280</u></b>	<b><u>25.1</u></b>	280	25.0	280	25.1	<b><u>280</u></b>	<b><u>25.1</u></b>	280	25.0
483.xalancbmk	235	29.3	<b><u>235</u></b>	<b><u>29.3</u></b>	236	29.2	235	29.3	<b><u>235</u></b>	<b><u>29.3</u></b>	236	29.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case,  
PC Power and Cooling 1200W power supply  
OMP\_NUM\_THREADS set to number of processors cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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## Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

```

## Base Optimization Flags

C benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000

```

C++ benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

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## Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32  
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32  
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000  
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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