



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 29.0

Intel DH67BLB3 Motherboard (Intel Core i3-2100T)

SPECint_base2006 = 27.8

CPU2006 license: 13

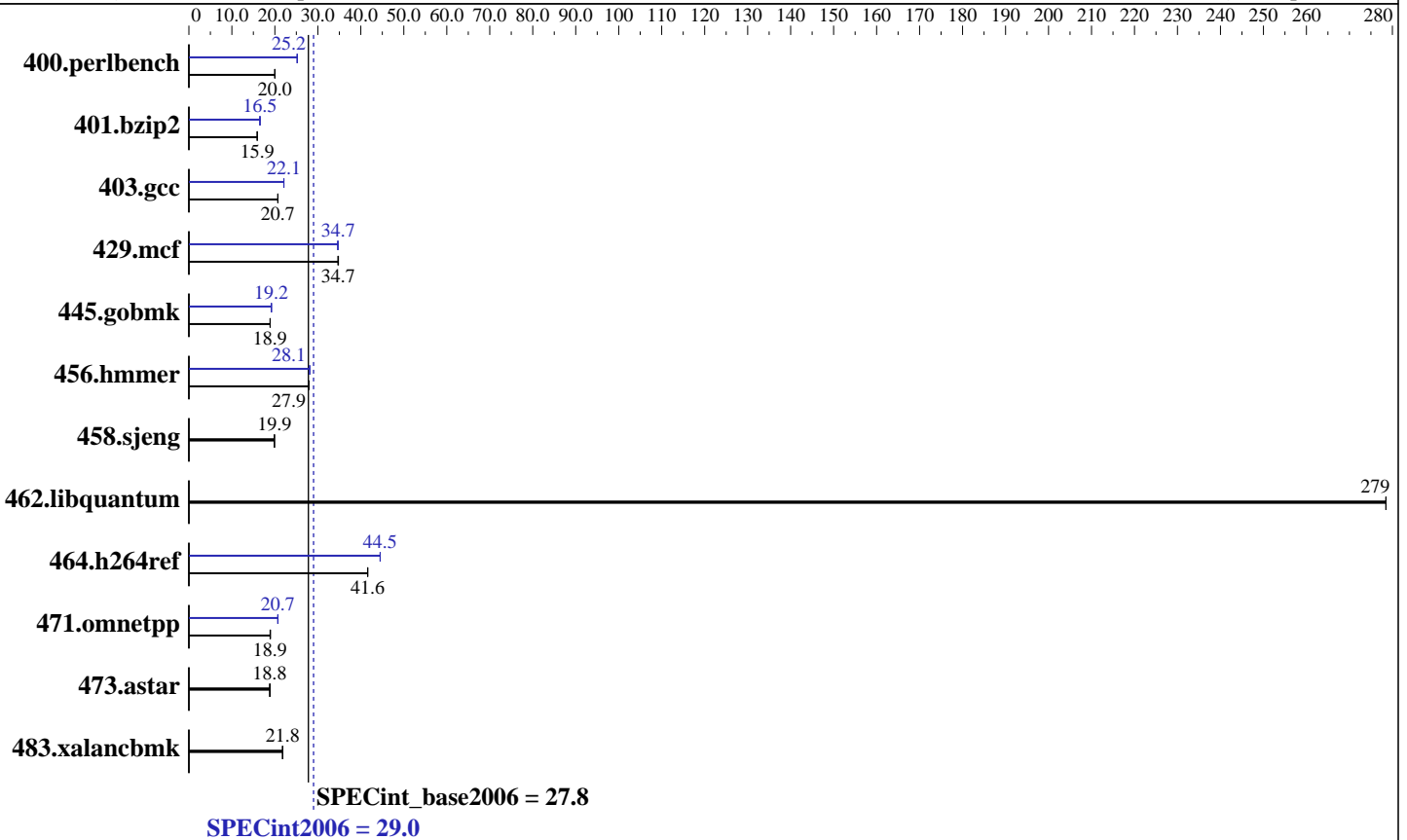
Test date: Apr-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i3-2100T
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.163 Build 20110217
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 488 | 20.0 | 487 | 20.0 | 487 | 20.0 | 387 | 25.2 | 387 | 25.2 | 387 | 25.2 |
| 401.bzip2 | 608 | 15.9 | 608 | 15.9 | 608 | 15.9 | 584 | 16.5 | 583 | 16.6 | 584 | 16.5 |
| 403.gcc | 390 | 20.7 | 390 | 20.7 | 389 | 20.7 | 364 | 22.1 | 364 | 22.1 | 364 | 22.1 |
| 429.mcf | 262 | 34.8 | 263 | 34.7 | 263 | 34.7 | 264 | 34.6 | 263 | 34.7 | 263 | 34.7 |
| 445.gobmk | 556 | 18.9 | 556 | 18.9 | 556 | 18.9 | 546 | 19.2 | 546 | 19.2 | 546 | 19.2 |
| 456.hmmer | 334 | 27.9 | 334 | 27.9 | 334 | 27.9 | 332 | 28.1 | 332 | 28.1 | 332 | 28.1 |
| 458.sjeng | 609 | 19.9 | 609 | 19.9 | 609 | 19.9 | 609 | 19.9 | 609 | 19.9 | 609 | 19.9 |
| 462.libquantum | 74.4 | 279 | 74.4 | 279 | 74.4 | 279 | 74.4 | 279 | 74.4 | 279 | 74.4 | 279 |
| 464.h264ref | 532 | 41.6 | 532 | 41.6 | 532 | 41.6 | 497 | 44.5 | 497 | 44.5 | 497 | 44.5 |
| 471.omnetpp | 330 | 18.9 | 330 | 18.9 | 330 | 19.0 | 302 | 20.7 | 302 | 20.7 | 302 | 20.7 |
| 473.astar | 373 | 18.8 | 372 | 18.9 | 373 | 18.8 | 373 | 18.8 | 372 | 18.9 | 373 | 18.8 |
| 483.xalancbmk | 316 | 21.8 | 316 | 21.8 | 316 | 21.8 | 316 | 21.8 | 316 | 21.8 | 316 | 21.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

```

Base Optimization Flags

C benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000

```

C++ benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

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Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For other inquiries, please contact webmaster@spec.org.

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