



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®\_rate2006 = 84.4**

## Gateway GT350 F1 (Intel Xeon E5606)

**SPECint\_rate\_base2006 = 79.6**

CPU2006 license: 97

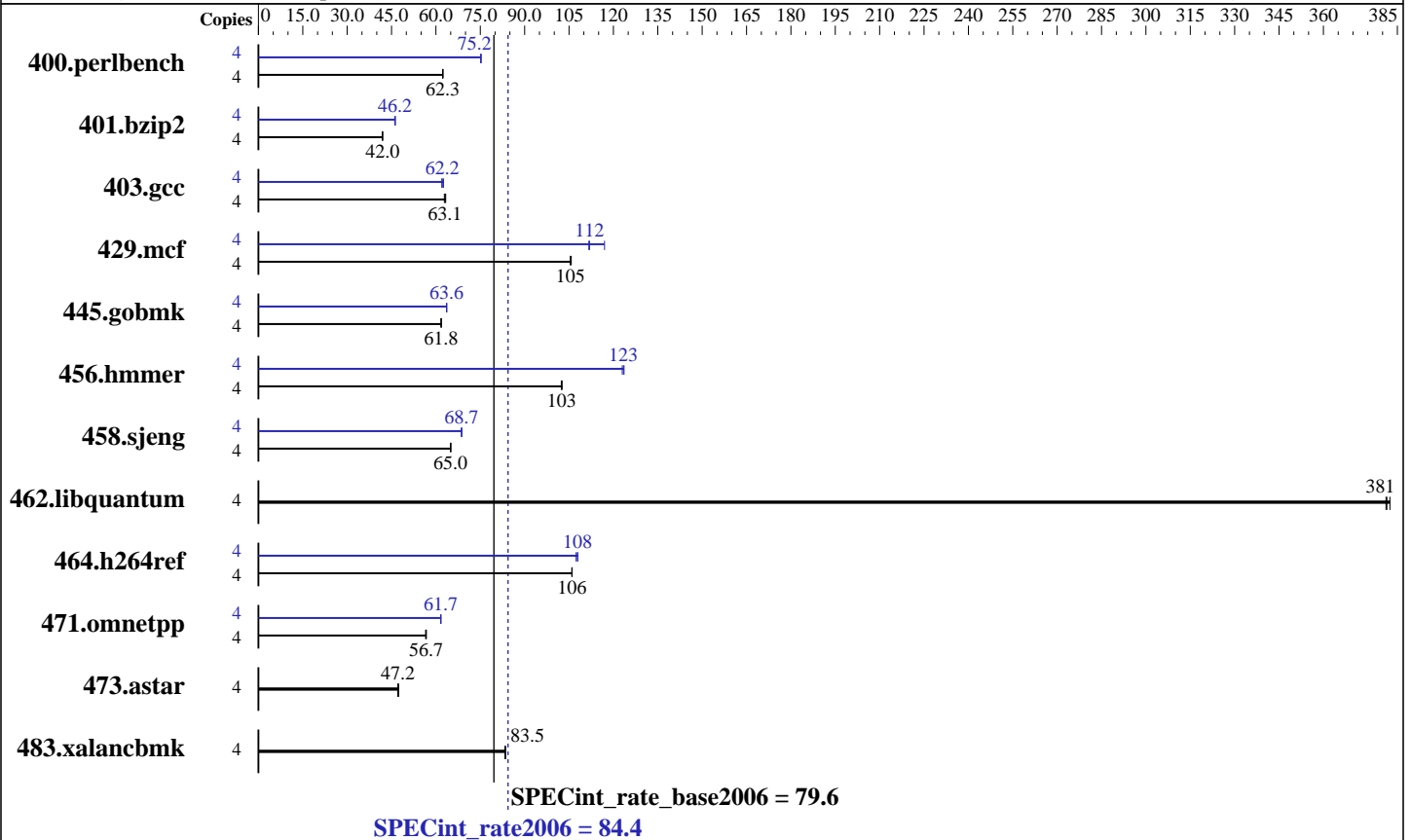
Test date: Apr-2011

Test sponsor: Acer Incorporated

Hardware Availability: Mar-2010

Tested by: Acer Incorporated

Software Availability: Nov-2010



### Hardware

CPU Name: Intel Xeon E5606  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB 2Rx8 PC3-10600R-9,ECC)  
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	627	62.3	<b>627</b>	<b>62.3</b>	626	62.4	4	519	75.3	520	75.1	<b>520</b>	<b>75.2</b>
401.bzip2	4	919	42.0	<b>920</b>	<b>42.0</b>	921	41.9	4	<b>835</b>	<b>46.2</b>	834	46.3	837	46.1
403.gcc	4	512	62.9	509	63.3	<b>510</b>	<b>63.1</b>	4	<b>518</b>	<b>62.2</b>	515	62.6	520	62.0
429.mcf	4	<b>346</b>	<b>105</b>	345	106	346	105	4	312	117	327	112	<b>326</b>	<b>112</b>
445.gobmk	4	<b>679</b>	<b>61.8</b>	678	61.9	680	61.7	4	<b>660</b>	<b>63.6</b>	660	63.6	659	63.7
456.hammer	4	364	102	364	103	<b>364</b>	<b>103</b>	4	302	124	303	123	<b>302</b>	<b>123</b>
458.sjeng	4	745	65.0	744	65.0	<b>745</b>	<b>65.0</b>	4	704	68.7	705	68.7	<b>705</b>	<b>68.7</b>
462.libquantum	4	217	381	217	382	<b>217</b>	<b>381</b>	4	217	381	217	382	<b>217</b>	<b>381</b>
464.h264ref	4	<b>835</b>	<b>106</b>	835	106	835	106	4	820	108	824	107	<b>821</b>	<b>108</b>
471.omnetpp	4	440	56.8	442	56.5	<b>441</b>	<b>56.7</b>	4	406	61.6	405	61.7	<b>405</b>	<b>61.7</b>
473.astar	4	596	47.2	592	47.4	<b>595</b>	<b>47.2</b>	4	596	47.2	592	47.4	<b>595</b>	<b>47.2</b>
483.xalancbmk	4	331	83.4	<b>331</b>	<b>83.5</b>	330	83.5	4	331	83.4	<b>331</b>	<b>83.5</b>	330	83.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
This result was measured on the Gateway GT350 F1.  
The Acer AT350 F1 is electronically equivalent

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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