



# SPEC® CINT2006 Result

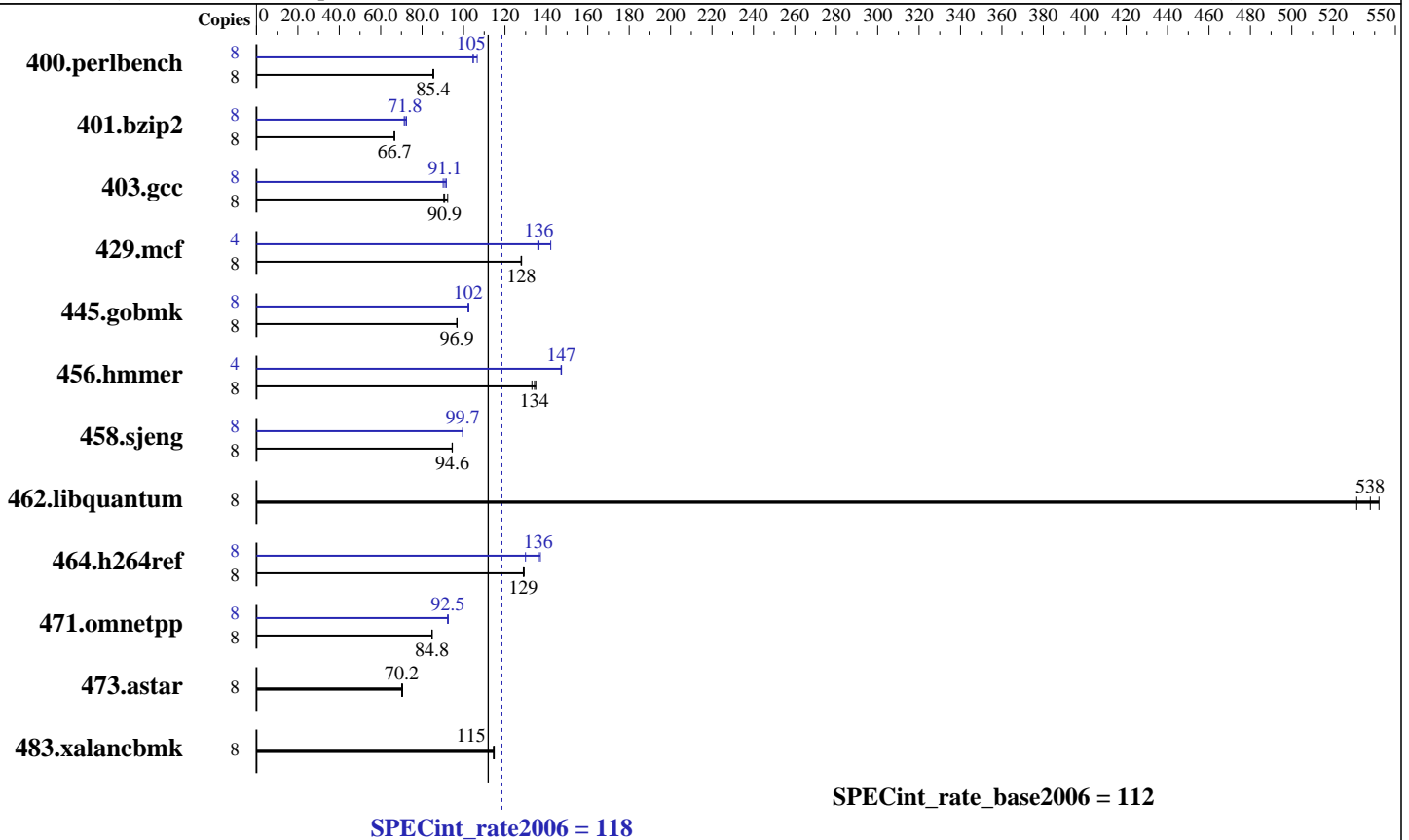
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**Acer Incorporated**  
**Acer AT350 F1 (Xeon E5620)**

**SPECint®\_rate2006 = 118**  
**SPECint\_rate\_base2006 = 112**

**CPU2006 license:** 97  
**Test sponsor:** Acer Incorporated  
**Tested by:** Acer Incorporated

**Test date:** Mar-2011  
**Hardware Availability:** Apr-2010  
**Software Availability:** Nov-2010



## Hardware

**CPU Name:** Intel Xeon E5620  
**CPU Characteristics:** Intel Turbo Boost Technology up to 2.67 GHz  
**CPU MHz:** 2400  
**FPU:** Integrated  
**CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1,2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 12 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 24 GB (6 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz)  
**Disk Subsystem:** 1 x 300 GB SATA, 10000 RPM SATA HDD  
**Other Hardware:** None

## Software

**Operating System:** SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
**Compiler:** Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
**Auto Parallel:** No  
**File System:** ext3  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	913	85.6	<u>915</u>	<u>85.4</u>	917	85.2	8	733	107	747	105	<u>746</u>	<u>105</u>
401.bzip2	8	<u>1157</u>	<u>66.7</u>	1162	66.5	1157	66.7	8	1066	72.5	1082	71.3	<u>1075</u>	<u>71.8</u>
403.gcc	8	<u>709</u>	<u>90.9</u>	697	92.4	712	90.5	8	702	91.7	714	90.2	<u>707</u>	<u>91.1</u>
429.mcf	8	570	128	571	128	<u>570</u>	<u>128</u>	4	268	136	257	142	<u>267</u>	<u>136</u>
445.gobmk	8	866	96.9	867	96.8	<u>866</u>	<u>96.9</u>	8	<u>820</u>	<u>102</u>	821	102	818	103
456.hammer	8	<u>556</u>	<u>134</u>	561	133	553	135	4	253	147	254	147	<u>254</u>	<u>147</u>
458.sjeng	8	<u>1023</u>	<u>94.6</u>	1023	94.6	1025	94.5	8	972	99.6	971	99.7	<u>971</u>	<u>99.7</u>
462.libquantum	8	<u>308</u>	<u>538</u>	312	531	306	542	8	<u>308</u>	<u>538</u>	312	531	306	542
464.h264ref	8	1374	129	<u>1373</u>	<u>129</u>	1369	129	8	<u>1300</u>	<u>136</u>	1291	137	1362	130
471.omnetpp	8	590	84.8	590	84.8	<u>590</u>	<u>84.8</u>	8	<u>541</u>	<u>92.5</u>	540	92.6	541	92.4
473.astar	8	<u>800</u>	<u>70.2</u>	796	70.6	800	70.2	8	<u>800</u>	<u>70.2</u>	796	70.6	800	70.2
483.xalancbmk	8	481	115	<u>481</u>	<u>115</u>	482	114	8	481	115	<u>481</u>	<u>115</u>	482	114

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
This result was measured on the Gateway GT350 F1.  
The Acer AT350 F1 is electronically equivalent

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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