



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 46.6

PowerEdge T710 (Intel Xeon X5690, 3.46 GHz)

SPECint_base2006 = 44.2

CPU2006 license: 55

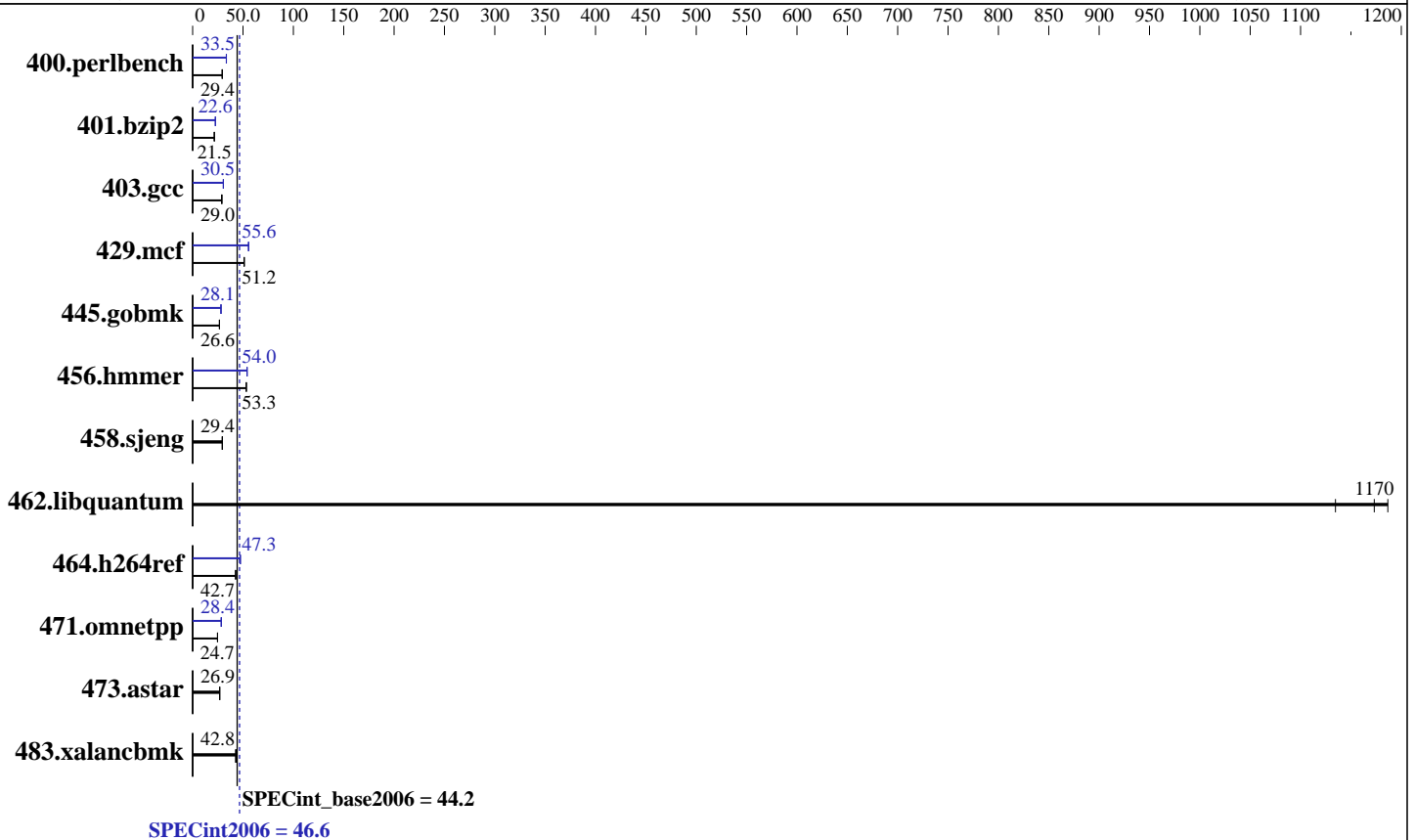
Test date: Feb-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | <u>332</u> | <u>29.4</u> | 332 | 29.4 | 333 | 29.4 | <u>292</u> | <u>33.5</u> | 292 | 33.4 | 291 | 33.5 |
| 401.bzip2 | <u>448</u> | <u>21.5</u> | 448 | 21.5 | 448 | 21.5 | 427 | 22.6 | <u>427</u> | <u>22.6</u> | 427 | 22.6 |
| 403.gcc | <u>278</u> | <u>29.0</u> | 277 | 29.0 | 279 | 28.8 | <u>264</u> | <u>30.5</u> | 264 | 30.5 | 264 | 30.5 |
| 429.mcf | <u>178</u> | <u>51.2</u> | 178 | 51.3 | 179 | 51.0 | 164 | 55.6 | 164 | 55.5 | <u>164</u> | <u>55.6</u> |
| 445.gobmk | <u>394</u> | <u>26.6</u> | 394 | 26.6 | 394 | 26.6 | <u>373</u> | <u>28.1</u> | 372 | 28.2 | 373 | 28.1 |
| 456.hammer | 175 | 53.3 | <u>175</u> | <u>53.3</u> | 175 | 53.3 | 173 | 54.0 | <u>173</u> | <u>54.0</u> | 173 | 54.1 |
| 458.sjeng | 412 | 29.4 | <u>412</u> | <u>29.4</u> | 409 | 29.6 | 412 | 29.4 | <u>412</u> | <u>29.4</u> | 409 | 29.6 |
| 462.libquantum | <u>17.7</u> | <u>1170</u> | 17.5 | 1190 | 18.3 | 1130 | <u>17.7</u> | <u>1170</u> | 17.5 | 1190 | 18.3 | 1130 |
| 464.h264ref | 520 | 42.6 | <u>519</u> | <u>42.7</u> | 517 | 42.8 | 468 | 47.3 | <u>468</u> | <u>47.3</u> | 467 | 47.3 |
| 471.omnetpp | 253 | 24.7 | 253 | 24.7 | <u>253</u> | <u>24.7</u> | <u>220</u> | <u>28.4</u> | 220 | 28.5 | 220 | 28.4 |
| 473.astar | <u>261</u> | <u>26.9</u> | 262 | 26.8 | 261 | 26.9 | <u>261</u> | <u>26.9</u> | 262 | 26.8 | 261 | 26.9 |
| 483.xalancbmk | <u>161</u> | <u>42.8</u> | 160 | 43.1 | 161 | 42.8 | <u>161</u> | <u>42.8</u> | 160 | 43.1 | 161 | 42.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)
Logical Processor = Disabled (Default = Enabled)

General Notes

OMP_NUM_THREADS set to number of cores
The Dell PowerEdge T710 and
the Bull NovaScale T860 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T710 model.
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64
400.perlbench: icc -m32

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Peak Compiler Invocation (Continued)

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

456.hammer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.xml>



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For other inquiries, please contact webmaster@spec.org.

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