SPEC® CINT2006 Result

Fujitsu

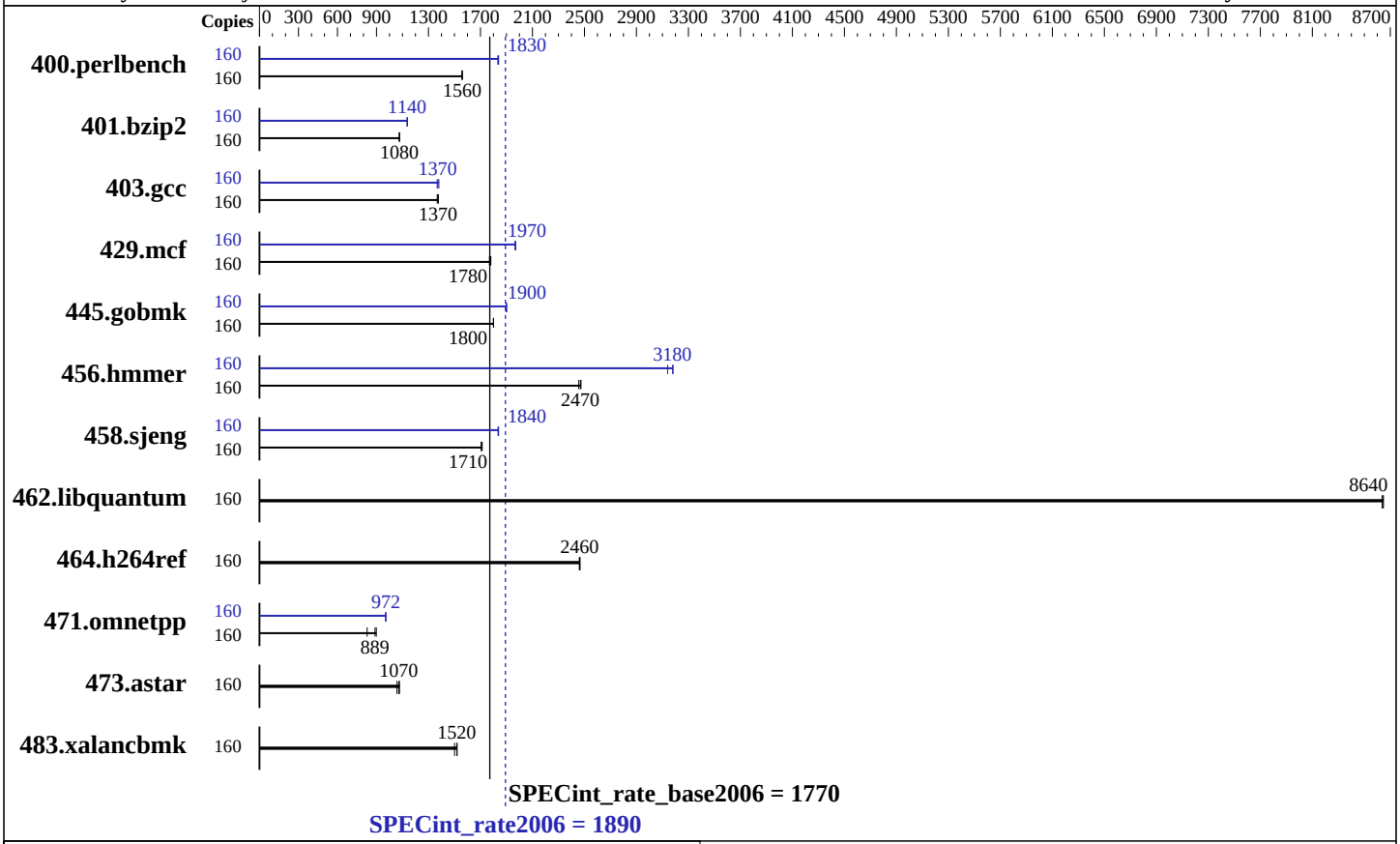
PRIMEQUEST 1800E2, Intel Xeon E7-8870, 2.40 GHz

SPECint®_rate2006 = 1890

SPECint_rate_base2006 = 1770

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2011
Hardware Availability: May-2011
Software Availability: Nov-2010



Hardware

CPU Name:	Intel Xeon E7-8870
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	80 cores, 8 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1-8 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (128 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	2 x 147 GB (SAS, 15000RPM, RAID0)
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.0(x86_64), Kernel 2.6.32-71.el6.x86_64
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01 Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	160	**1002**	**1560**	1002	1560	1003	1560	160	852	1830	850	1840	**852**	**1830**
401.bzip2	160	1434	1080	1435	1080	**1435**	**1080**	160	1356	1140	1359	1140	**1358**	**1140**
403.gcc	160	**938**	**1370**	942	1370	936	1380	160	933	1380	942	1370	**938**	**1370**
429.mcf	160	**821**	**1780**	820	1780	825	1770	160	740	1970	742	1970	**741**	**1970**
445.gobmk	160	933	1800	932	1800	**932**	**1800**	160	883	1900	**883**	**1900**	884	1900
456.hmmer	160	608	2460	**605**	**2470**	604	2470	160	475	3140	469	3180	**470**	**3180**
458.sjeng	160	**1135**	**1710**	1130	1710	1135	1710	160	**1053**	**1840**	1052	1840	1055	1840
462.libquantum	160	384	8640	**384**	**8640**	383	8650	160	384	8640	**384**	**8640**	383	8650
464.h264ref	160	1439	2460	1436	2470	**1438**	**2460**	160	1439	2460	1436	2470	**1438**	**2460**
471.omnetpp	160	**1125**	**889**	1113	899	1207	828	160	1030	971	1029	972	**1029**	**972**
473.astar	160	1062	1060	**1049**	**1070**	1043	1080	160	1062	1060	**1049**	**1070**	1043	1080
483.xalancbmk	160	736	1500	726	1520	**727**	**1520**	160	736	1500	726	1520	**727**	**1520**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
Large pages were not enabled for this run

The following command was used prior to run
   ulimit -s unlimited
   echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

Continued on next page

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/RX900S2_Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/RX900S2_Platform.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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