



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp[®]_rate2006 = 128

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = 122

CPU2006 license: 11

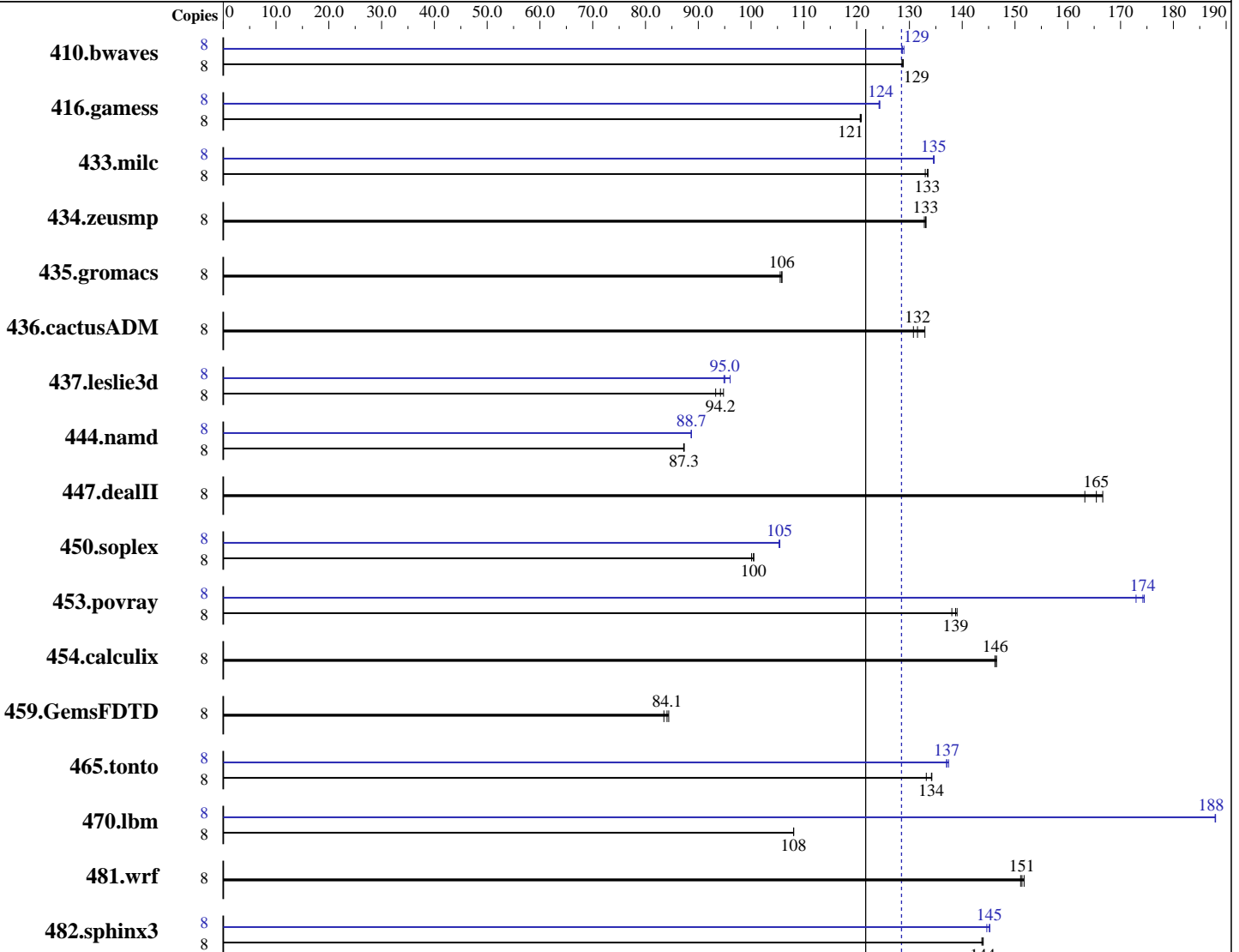
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



SPECfp_rate_base2006 = 122

SPECfp_rate2006 = 128

Hardware

CPU Name: Intel Xeon L5609
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = **128**

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = **122**

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|------------|--------|-------------|-------------|-------------|-------------|-------------|------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 8 | 844 | 129 | 845 | 129 | 845 | 129 | 8 | 843 | 129 | 846 | 129 | 845 | 129 | | |
| 416.gamess | 8 | 1298 | 121 | 1298 | 121 | 1296 | 121 | 8 | 1259 | 124 | 1261 | 124 | 1260 | 124 | | |
| 433.milc | 8 | 552 | 133 | 550 | 134 | 550 | 133 | 8 | 545 | 135 | 546 | 135 | 546 | 135 | | |
| 434.zeusmp | 8 | 547 | 133 | 547 | 133 | 548 | 133 | 8 | 547 | 133 | 547 | 133 | 548 | 133 | | |
| 435.gromacs | 8 | 540 | 106 | 541 | 106 | 540 | 106 | 8 | 540 | 106 | 541 | 106 | 540 | 106 | | |
| 436.cactusADM | 8 | 731 | 131 | 719 | 133 | 727 | 132 | 8 | 731 | 131 | 719 | 133 | 727 | 132 | | |
| 437.leslie3d | 8 | 798 | 94.2 | 806 | 93.3 | 793 | 94.8 | 8 | 783 | 96.0 | 791 | 95.0 | 793 | 94.9 | | |
| 444.namd | 8 | 735 | 87.3 | 736 | 87.2 | 735 | 87.3 | 8 | 724 | 88.7 | 724 | 88.7 | 724 | 88.7 | | |
| 447.dealII | 8 | 553 | 165 | 561 | 163 | 549 | 167 | 8 | 553 | 165 | 561 | 163 | 549 | 167 | | |
| 450.soplex | 8 | 664 | 100 | 664 | 101 | 667 | 100 | 8 | 633 | 105 | 633 | 105 | 634 | 105 | | |
| 453.povray | 8 | 306 | 139 | 308 | 138 | 307 | 139 | 8 | 244 | 174 | 244 | 174 | 246 | 173 | | |
| 454.calculix | 8 | 450 | 147 | 451 | 146 | 451 | 146 | 8 | 450 | 147 | 451 | 146 | 451 | 146 | | |
| 459.GemsFDTD | 8 | 1006 | 84.4 | 1010 | 84.1 | 1017 | 83.5 | 8 | 1006 | 84.4 | 1010 | 84.1 | 1017 | 83.5 | | |
| 465.tonto | 8 | 586 | 134 | 587 | 134 | 591 | 133 | 8 | 573 | 137 | 574 | 137 | 574 | 137 | | |
| 470.lbm | 8 | 1017 | 108 | 1017 | 108 | 1017 | 108 | 8 | 585 | 188 | 585 | 188 | 585 | 188 | | |
| 481.wrf | 8 | 591 | 151 | 592 | 151 | 589 | 152 | 8 | 591 | 151 | 592 | 151 | 589 | 152 | | |
| 482.sphinx3 | 8 | 1085 | 144 | 1083 | 144 | 1084 | 144 | 8 | 1074 | 145 | 1078 | 145 | 1074 | 145 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 128

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = 122

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

Platform Notes

Power C-State enabled in BIOS
Data Reuse disabled in BIOS
Demand Scrub disabled in BIOS

General Notes

Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 128

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = 122

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 128

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = 122

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 128

IBM System x3630 M3 (Intel Xeon L5609)

SPECfp_rate_base2006 = 122

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 19:51:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 April 2011.