SPEC® CINT2006 Result

Itautec

Servidor Itautec MP224 (Intel Xeon X5670)

SPECint®_rate2006 = 343

SPECint_rate_base2006 = 319

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2010
Hardware Availability: Feb-2011
Software Availability: Apr-2010

Benchmark	Copies	Result
400.perlbench	24	350
400.perlbench	24	296
401.bzip2	24	218
401.bzip2	24	204
403.gcc	24	243
403.gcc	24	244
429.mcf	12	359
429.mcf	24	285
445.gobmk	24	373
445.gobmk	24	341
456.hmmer	12	488
456.hmmer	24	446
458.sjeng	24	348
458.sjeng	24	318
462.libquantum	24	824
462.libquantum	24	821
464.h264ref	24	444
464.h264ref	24	445
471.omnetpp	24	232
471.omnetpp	24	223
473.astar	24	216
473.astar	24	201
483.xalancbmk	24	340

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Hardware

CPU Name: Intel Xeon X5670
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1

SPEC CINT2006 Result
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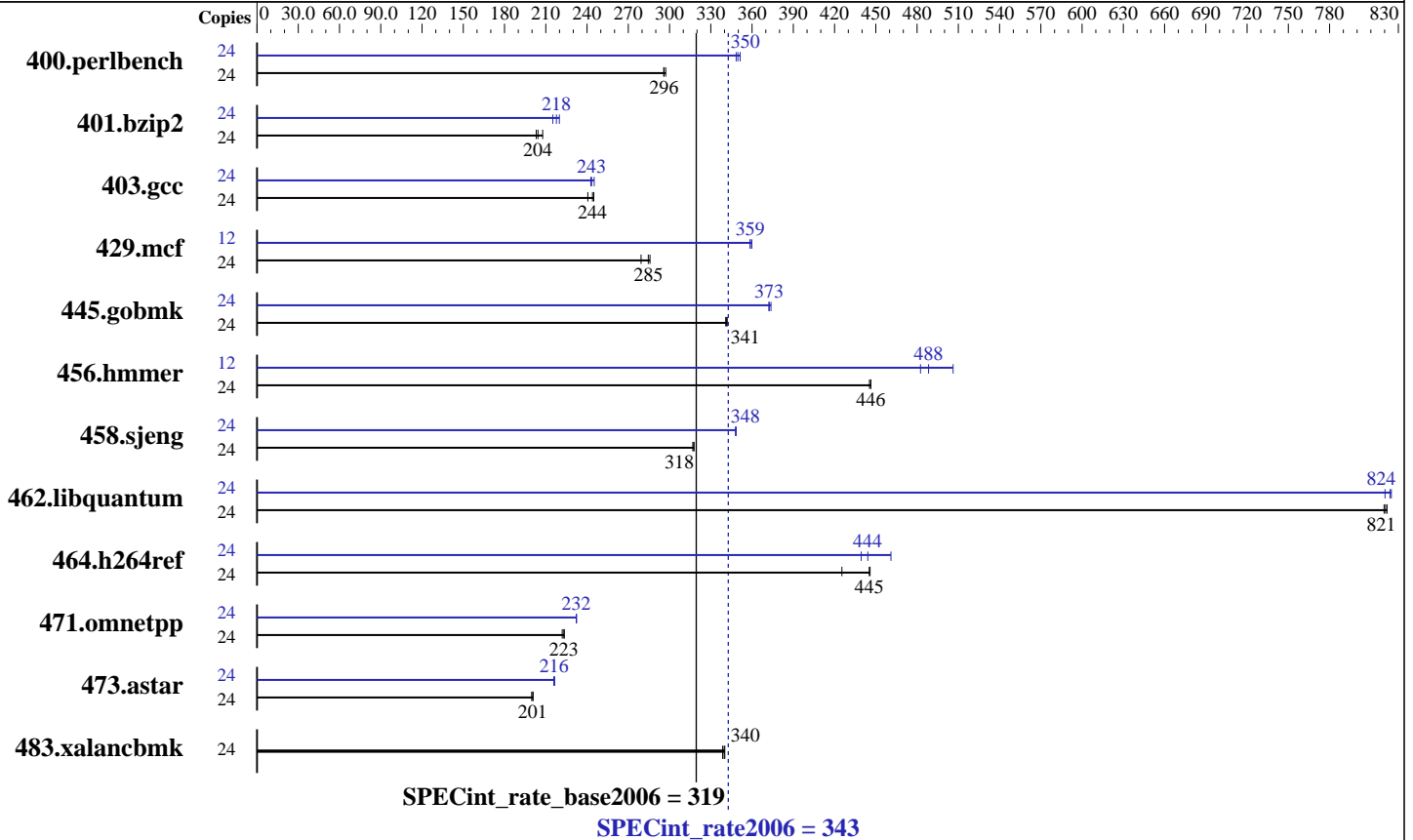
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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	789	297	**792**	**296**	793	296	24	673	348	667	351	**670**	**350**
401.bzip2	24	**1134**	**204**	1115	208	1141	203	24	1077	215	1053	220	**1064**	**218**
403.gcc	24	789	245	**792**	**244**	803	241	24	**794**	**243**	788	245	796	243
429.mcf	24	784	279	**769**	**285**	766	286	12	**305**	**359**	304	360	305	359
445.gobmk	24	739	341	**738**	**341**	736	342	24	674	374	**676**	**373**	676	372
456.hmmer	24	502	446	503	445	**502**	**446**	12	**229**	**488**	232	483	221	506
458.sjeng	24	**914**	**318**	914	318	916	317	24	834	348	835	348	**834**	**348**
462.libquantum	24	607	820	**606**	**821**	605	822	24	**604**	**824**	606	820	603	825
464.h264ref	24	**1193**	**445**	1191	446	1249	425	24	1152	461	**1196**	**444**	1209	439
471.omnetpp	24	676	222	**672**	**223**	671	223	24	646	232	646	232	**646**	**232**
473.astar	24	839	201	844	200	**840**	**201**	24	778	216	**780**	**216**	781	216
483.xalancbmk	24	**488**	**340**	489	339	487	340	24	**488**	**340**	489	339	487	340

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
```

Base Compiler Invocation

C benchmarks:
```
      icc  -m32
```

C++ benchmarks:
```
      icpc -m32
```

Base Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

> 401.bzip2: `icc -m64`

> 456.hmmer: `icc -m64`

> 458.sjeng: `icc -m64`

> 462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

> 473.astar: `icpc -m64`

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
      401.bzip2: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

      429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
               -opt-prefetch

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
               -L/opt/sh/SmartHeap_8.1/lib -lsmartheap64

  483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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