



SPEC® CINT2006 Result

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Intel Corporation

SPECint_rate2006 = 60.5

Intel DH57JG Motherboard (Intel Core i5-650)

SPECint_rate_base2006 = 58.1

CPU2006 license: 13

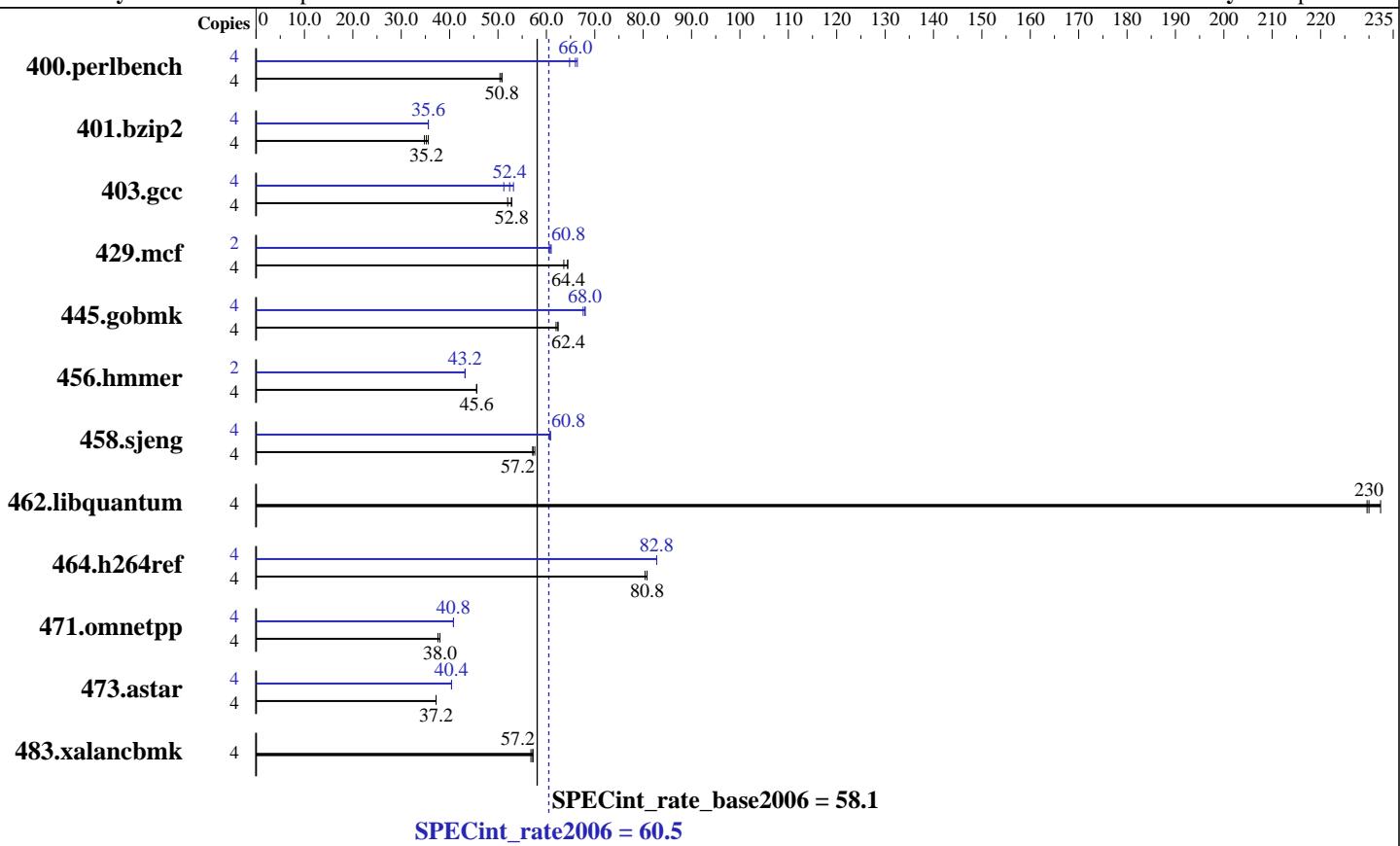
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Sep-2010

Hardware Availability: Jan-2010

Software Availability: Sep-2009



Hardware

CPU Name: Intel Core i5-650
CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
Compiler: Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	778	50.4	771	50.8	770	50.8	4	588	66.4	591	66.0	604	64.8
401.bzip2	4	1112	34.8	1093	35.2	1090	35.6	4	1087	35.6	1090	35.6	1090	35.6
403.gcc	4	611	52.8	619	52.0	612	52.8	4	616	52.4	629	51.2	603	53.2
429.mcf	4	567	64.4	565	64.4	575	63.6	2	301	60.6	299	61.0	300	60.8
445.gobmk	4	676	62.0	674	62.4	675	62.4	4	618	68.0	621	67.6	617	68.0
456.hammer	4	815	45.6	815	45.6	815	45.6	2	432	43.2	432	43.2	432	43.2
458.sjeng	4	847	57.2	846	57.2	841	57.6	4	794	60.8	796	60.8	795	60.8
462.libquantum	4	360	230	361	230	357	232	4	360	230	361	230	357	232
464.h264ref	4	1096	80.8	1099	80.4	1096	80.8	4	1069	82.8	1069	82.8	1067	82.8
471.omnetpp	4	663	37.6	659	38.0	659	38.0	4	613	40.8	612	40.8	612	40.8
473.astar	4	756	37.2	756	37.2	757	37.2	4	693	40.4	694	40.4	696	40.4
483.xalancbmk	4	484	57.2	484	57.2	484	56.8	4	484	57.2	484	57.2	484	56.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES

483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F5120000000 shlw32M.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch  
/F5120000000 shlw32M.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias  
/F512000000
```

```
403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gnu: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.xml>



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For other inquiries, please contact webmaster@spec.org.

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