



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 383

ProLiant BL620c G7
(2.27 GHz, Intel Xeon X7560)

SPECint_rate_base2006 = 361

CPU2006 license: 3

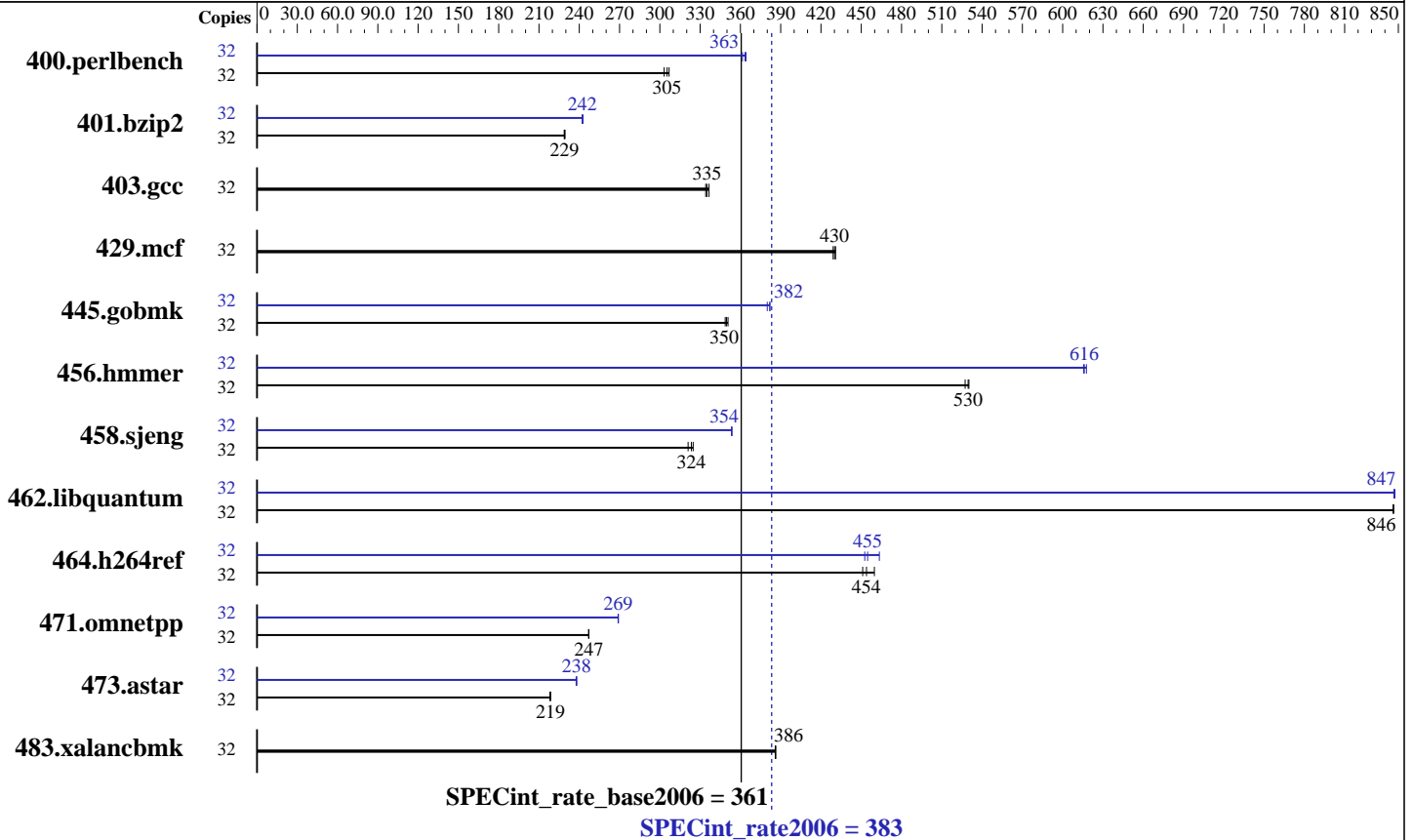
Test date: Oct-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2010

Tested by: Hewlett-Packard Company

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X7560
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (32 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1x146 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5
 Kernel 2.6.18-194.el5
 Compiler: Intel C++ Compiler 11.1 for Linux
 Build 20100414 Package ID: l_cproc_p_11.1.072
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.6-14.el5



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1019	307	1031	303	<u>1024</u>	<u>305</u>	32	865	362	859	364	<u>860</u>	<u>363</u>
401.bzip2	32	1350	229	1346	229	<u>1348</u>	<u>229</u>	32	1275	242	1272	243	<u>1274</u>	<u>242</u>
403.gcc	32	765	337	<u>769</u>	<u>335</u>	771	334	32	765	337	<u>769</u>	<u>335</u>	771	334
429.mcf	32	680	429	677	431	<u>679</u>	<u>430</u>	32	680	429	677	431	<u>679</u>	<u>430</u>
445.gobmk	32	963	349	<u>960</u>	<u>350</u>	957	351	32	879	382	883	380	<u>879</u>	<u>382</u>
456.hammer	32	563	530	566	527	<u>564</u>	<u>530</u>	32	483	618	485	616	<u>485</u>	<u>616</u>
458.sjeng	32	1206	321	<u>1197</u>	<u>324</u>	1192	325	32	1095	354	1096	353	<u>1095</u>	<u>354</u>
462.libquantum	32	784	846	783	846	<u>783</u>	<u>846</u>	32	783	847	<u>783</u>	<u>847</u>	782	847
464.h264ref	32	1570	451	<u>1560</u>	<u>454</u>	1540	460	32	1528	463	1565	453	<u>1557</u>	<u>455</u>
471.omnetpp	32	809	247	810	247	<u>810</u>	<u>247</u>	32	743	269	<u>744</u>	<u>269</u>	744	269
473.astar	32	1028	219	<u>1028</u>	<u>219</u>	1030	218	32	943	238	<u>944</u>	<u>238</u>	945	238
483.xalancbmk	32	572	386	<u>572</u>	<u>386</u>	572	386	32	572	386	<u>572</u>	<u>386</u>	572	386

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.1/072/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.1/072/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.1/072/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmarheap

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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