



SPEC® CINT2006 Result

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Oracle Corporation

Sun Blade X6275 M2 Server Module (Intel Xeon X5670 2.93 GHz)

SPECint_rate2006 = 348

SPECint_rate_base2006 = 326

CPU2006 license: 6

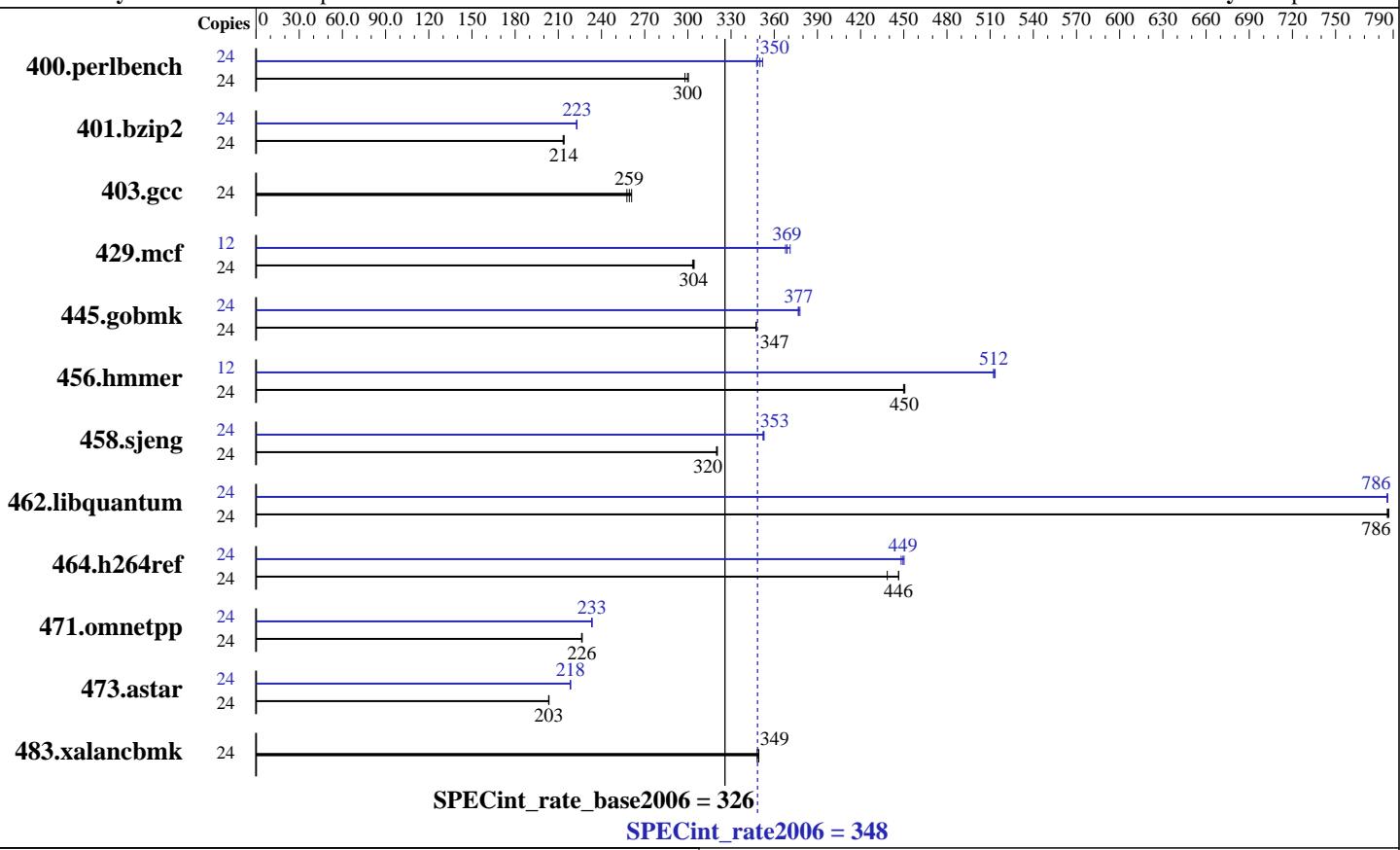
Test sponsor: Oracle Corporation

Tested by: Oracle Corporation

Test date: Oct-2010

Hardware Availability: Dec-2010

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X5670
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1 or 2 chips per Sun Blade X6275 M2 node
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: Sun Storage 7410 System via NFS
(See additional details below)
Other Hardware: None

Software

Operating System: Oracle Enterprise Linux Server release 5.5 kernel 2.6.18-194.el5
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: No
File System: NFSv4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	781	300	787	298	782	300	24	670	350	674	348	666	352
401.bzip2	24	1084	214	1082	214	1086	213	24	1039	223	1039	223	1040	223
403.gcc	24	745	259	740	261	750	258	24	745	259	740	261	750	258
429.mcf	24	720	304	722	303	719	304	12	298	368	297	369	295	371
445.gobmk	24	725	347	725	347	724	348	24	668	377	668	377	666	378
456.hammer	24	497	450	497	451	498	450	12	218	512	218	513	218	512
458.sjeng	24	908	320	906	321	907	320	24	825	352	823	353	823	353
462.libquantum	24	633	786	632	786	632	787	24	633	786	633	786	633	786
464.h264ref	24	1211	439	1190	446	1190	446	24	1182	449	1180	450	1185	448
471.omnetpp	24	662	226	662	227	663	226	24	642	233	643	233	643	233
473.astar	24	828	204	829	203	829	203	24	771	218	771	218	772	218
483.xalancbmk	24	474	349	474	349	476	348	24	474	349	474	349	476	348

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Load Default BIOS Settings and then change the following
Data Reuse Optimization Disabled
Hardware Prefetch Enabled
Adjacent Cache Line Prefetch Enabled
L1 Data Prefetch Enabled

Storage Configuration for Disk Subsystem:
Sun Storage 7410 with 7200 RPM Disks under RAID-1 configuration
mounted over 10GBE network interface.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L(path to library) -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

401.bzip2: `icc -m64`

456.hmmr: `icc -m64`

458.sjeng: `icc -m64`

462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`



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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12
               -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll14 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                  -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll12 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L(path to library) -lsmartheap
```

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L(path to library) -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.html>
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.xml>
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.xml

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For other inquiries, please contact webmaster@spec.org.

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