SPEC® CINT2006 Result
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Oracle Corporation

Sun Fire X2270 M2 (Intel Xeon E5620 2.4 GHz)

$SPECint®_rate2006 = $ 223

$SPECint_rate_base2006 = $ 209

CPU2006 license: 6
Test sponsor: Oracle Corporation
Tested by: Oracle Corporation

Copies 0 20.0 50.0 80.0 110 130 150 170 190 210 230 250 270 290 310 330 350 370 390 410 430 450 470 490 510 530 550 570 590 610 640

- **400.perlbench** 16 — 201 / 16 — 170
- **401.bzip2** 16 — 142 / 16 — 133
- **403.gcc** 16 — 178 / 16 — 178
- **429.mcf** 8 — 252 / 16 — 243
- **445.gobmk** 16 — 212 / 16 — 196
- **456.hmmer** 8 — 293 / 16 — 270
- **458.sjeng** 16 — 200 / 16 — 183
- **462.libquantum** 16 — 639 / 16 — 636
- **464.h264ref** 16 — 255 / 16 — 246
- **471.omnetpp** 16 — 176 / 16 — 162
- **473.astar** 16 — 146 / 16 — 129
- **483.xalancbmk** 16 — 229

SPECint_rate_base2006 = 209

SPECint_rate2006 = 223

Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 or 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	1 x 1 TB, SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Oracle Enterprise Linux Server release 5.5 kernel 2.6.18-194.el5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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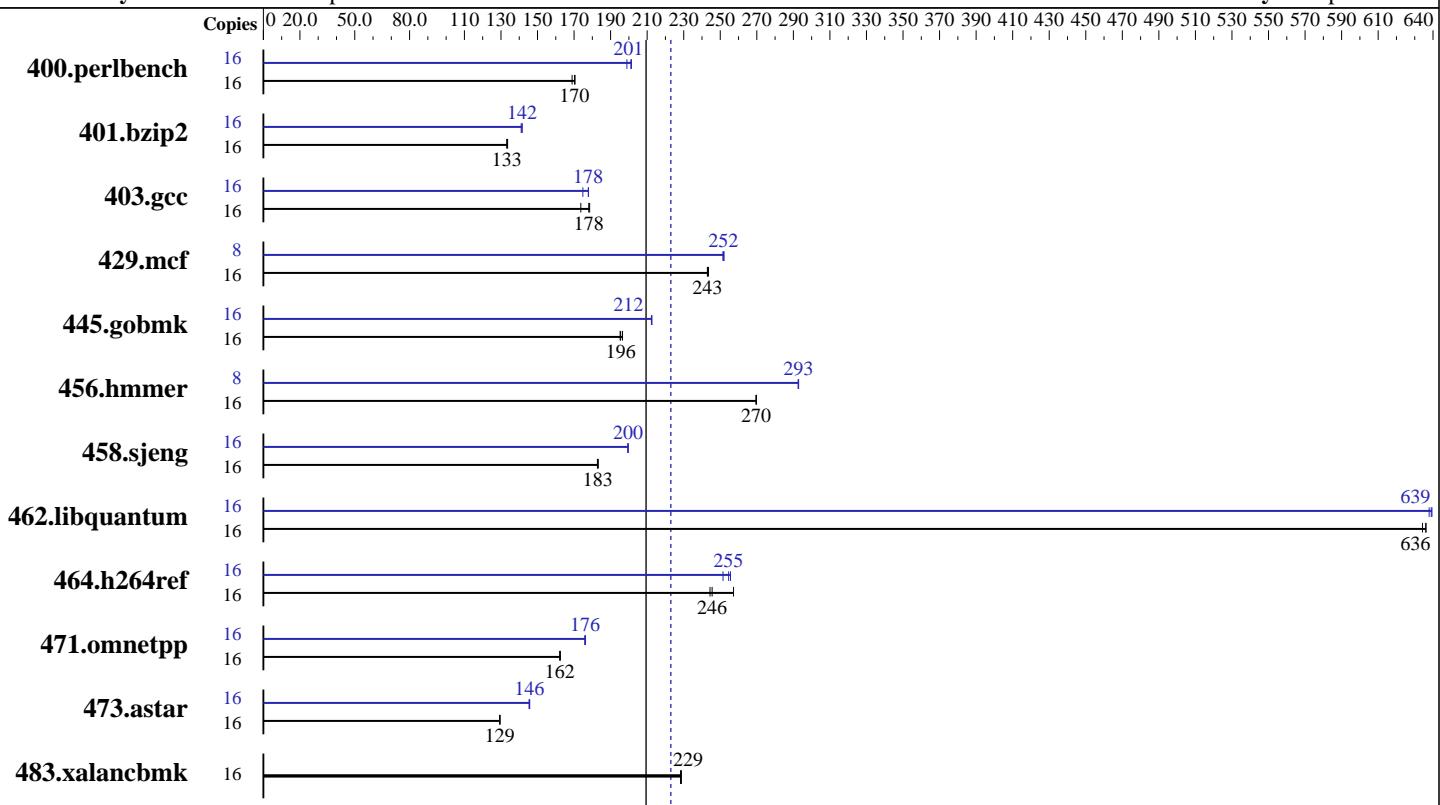SPECint_rate_base2006 = 209

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Test date: Nov-2010
Hardware Availability: Jun-2010
Software Availability: Apr-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	917	171	925	169	**918**	**170**	16	**777**	**201**	786	199	776	201
401.bzip2	16	1155	134	**1159**	**133**	1159	133	16	1091	142	1095	141	**1091**	**142**
403.gcc	16	721	179	**723**	**178**	742	174	16	737	175	724	178	**724**	**178**
429.mcf	16	599	244	601	243	**600**	**243**	8	**290**	**252**	289	252	290	251
445.gobmk	16	**859**	**196**	854	197	860	195	16	**790**	**212**	790	212	790	213
456.hmmer	16	**553**	**270**	554	270	553	270	8	255	293	**255**	**293**	255	293
458.sjeng	16	1057	183	1059	183	**1058**	**183**	16	**970**	**200**	970	200	971	199
462.libquantum	16	523	634	521	636	**521**	**636**	16	518	639	**519**	**639**	520	638
464.h264ref	16	1449	244	1376	257	**1442**	**246**	16	**1391**	**255**	1408	252	1385	256
471.omnetpp	16	**616**	**162**	615	162	617	162	16	569	176	567	176	**568**	**176**
473.astar	16	868	129	869	129	**868**	**129**	16	771	146	**771**	**146**	773	145
483.xalancbmk	16	483	229	**483**	**229**	484	228	16	483	229	**483**	**229**	484	228

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    Default BIOS Settings were used.

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
        -L(path to library) -lsmartheap

Base Other Flags

C benchmarks:

            403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

            401.bzip2: icc -m64

            456.hmmer: icc -m64

            458.sjeng: icc -m64

        462.libquantum: icc -m64

C++ benchmarks (except as noted below):
        icpc -m32

            473.astar: icpc -m64

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
            401.bzip2: -DSPEC_CPU_LP64
            456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                 -opt-prefetch

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L(path to library) -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L(path to library) -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.html
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.xml
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.xml

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