



# SPEC<sup>®</sup> CINT2006 Result

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## Acer Incorporated

Acer AW2000h-AW170h F1 (Intel Xeon X5570, 2.93 GHz)

SPECint<sup>®</sup>\_rate2006 = 268

SPECint\_rate\_base2006 = 253

CPU2006 license: 97

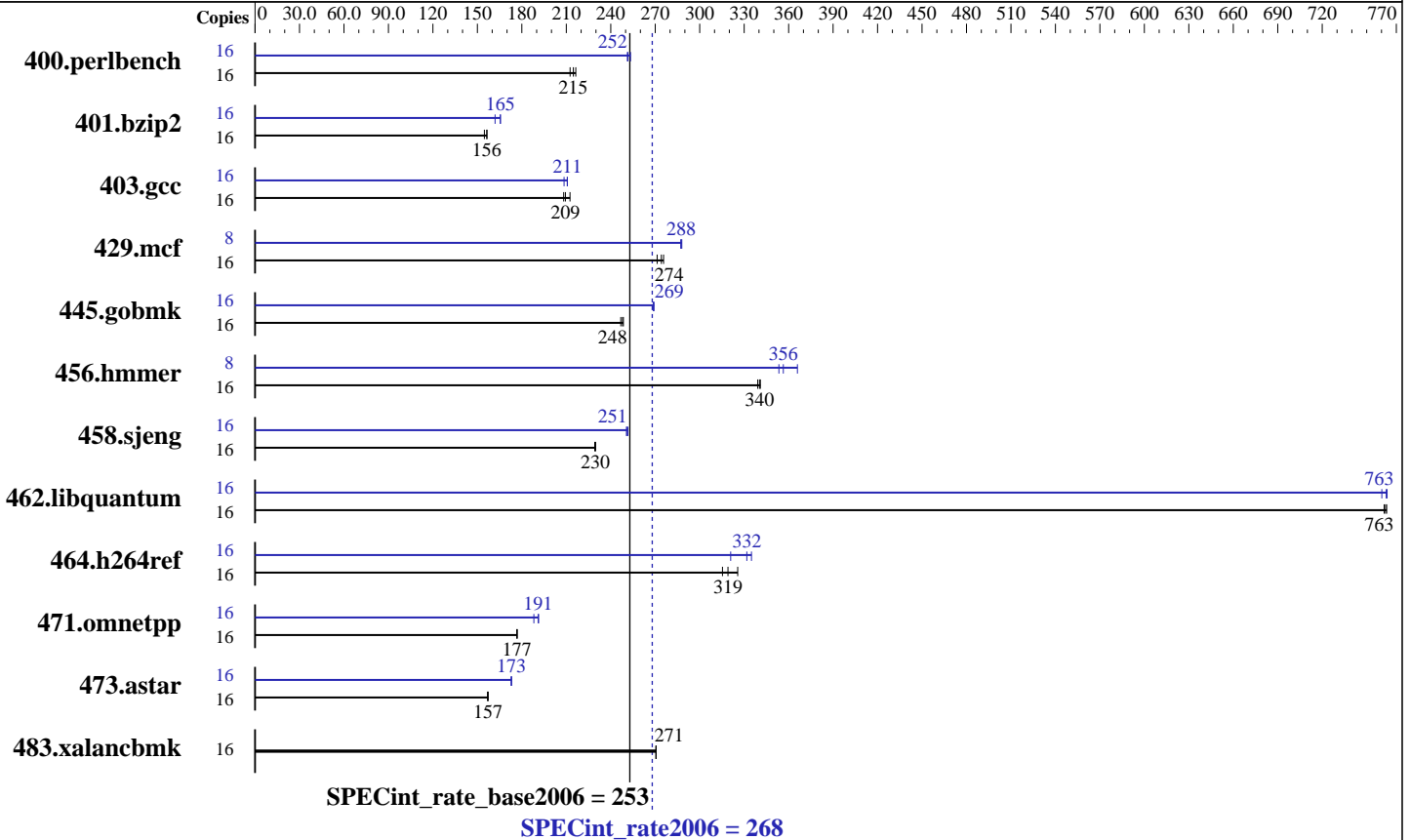
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

| Benchmark      | Base   |            |            |            |            |             |            | Peak   |            |            |             |            |            |            |
|----------------|--------|------------|------------|------------|------------|-------------|------------|--------|------------|------------|-------------|------------|------------|------------|
|                | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds     | Ratio      | Copies | Seconds    | Ratio      | Seconds     | Ratio      | Seconds    | Ratio      |
| 400.perlbench  | 16     | 722        | 216        | 735        | 213        | <b>728</b>  | <b>215</b> | 16     | <b>621</b> | <b>252</b> | 622         | 251        | 617        | 253        |
| 401.bzip2      | 16     | 998        | 155        | 987        | 156        | <b>987</b>  | <b>156</b> | 16     | 953        | 162        | 933         | 166        | <b>933</b> | <b>165</b> |
| 403.gcc        | 16     | 606        | 212        | 618        | 208        | <b>615</b>  | <b>209</b> | 16     | 611        | 211        | 618         | 208        | <b>611</b> | <b>211</b> |
| 429.mcf        | 16     | <b>532</b> | <b>274</b> | 538        | 271        | 529         | 276        | 8      | 254        | 287        | <b>254</b>  | <b>288</b> | 253        | 288        |
| 445.gobmk      | 16     | 680        | 247        | <b>677</b> | <b>248</b> | 675         | 249        | 16     | 625        | 269        | <b>624</b>  | <b>269</b> | 624        | 269        |
| 456.hammer     | 16     | <b>439</b> | <b>340</b> | 438        | 341        | 440         | 339        | 8      | 204        | 366        | <b>209</b>  | <b>356</b> | 211        | 353        |
| 458.sjeng      | 16     | 843        | 230        | 845        | 229        | <b>844</b>  | <b>230</b> | 16     | <b>772</b> | <b>251</b> | 769         | 252        | 772        | 251        |
| 462.libquantum | 16     | 435        | 762        | <b>435</b> | <b>763</b> | 434         | 763        | 16     | 436        | 760        | <b>434</b>  | <b>763</b> | 434        | 764        |
| 464.h264ref    | 16     | 1087       | 326        | 1123       | 315        | <b>1110</b> | <b>319</b> | 16     | 1057       | 335        | <b>1067</b> | <b>332</b> | 1103       | 321        |
| 471.omnetpp    | 16     | 566        | 177        | <b>566</b> | <b>177</b> | 565         | 177        | 16     | 523        | 191        | <b>523</b>  | <b>191</b> | 532        | 188        |
| 473.astar      | 16     | 714        | 157        | <b>715</b> | <b>157</b> | 716         | 157        | 16     | 649        | 173        | 650         | 173        | <b>650</b> | <b>173</b> |
| 483.xalancbmk  | 16     | <b>408</b> | <b>271</b> | 408        | 271        | 408         | 270        | 16     | <b>408</b> | <b>271</b> | 408         | 271        | 408        | 270        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent.  
This result was measured on GW2000ht-GW170ht F1.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarthearp64`

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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