# SPEC® CINT2006 Result



## Supermicro

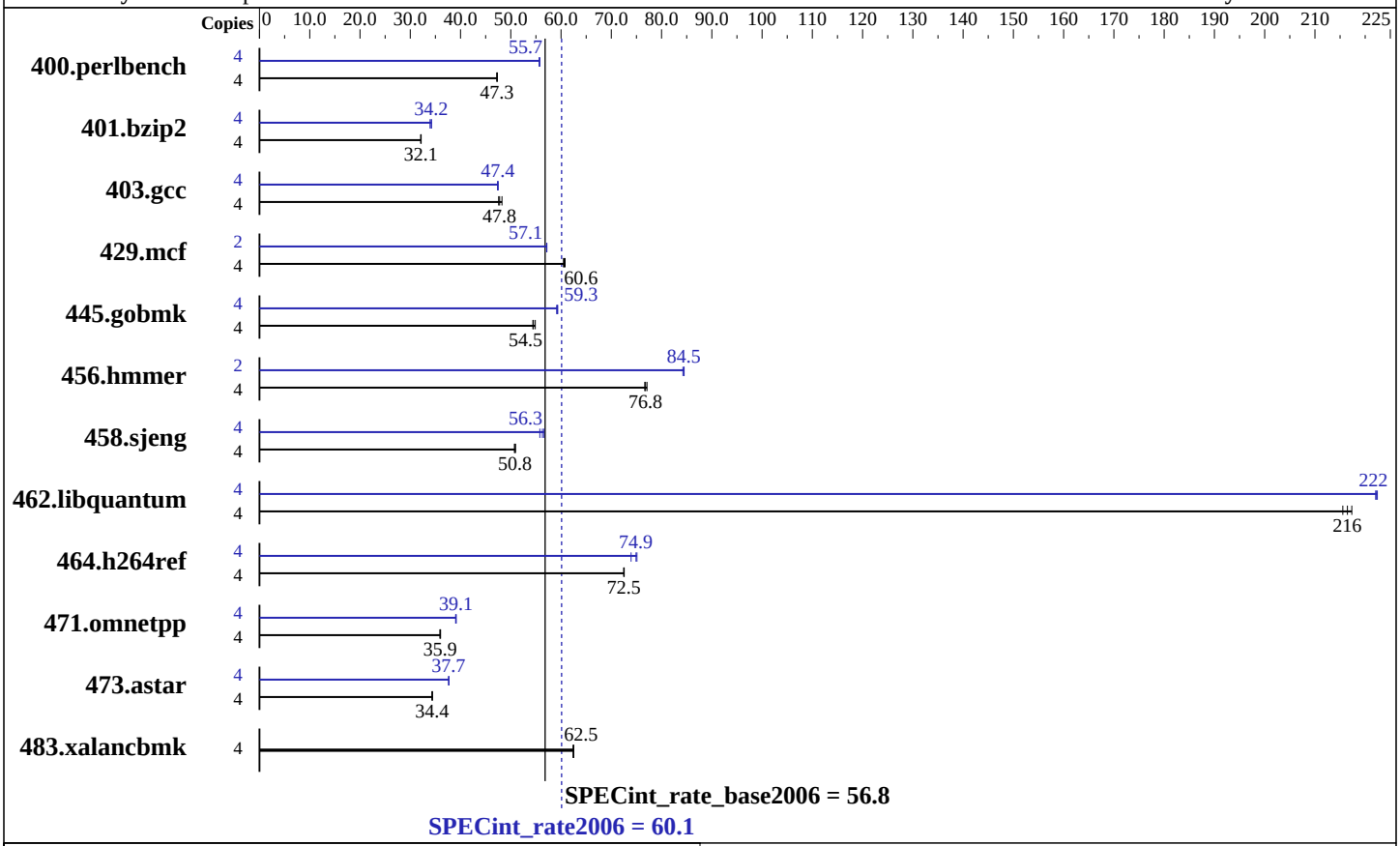
Motherboard X8SIT-F (Intel Core i3-530, 2.93 GHz)

SPECint®_rate2006 = 60.1

SPECint_rate_base2006 = 56.8

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** Oct-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Jan-2010




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core i3-530 |
| CPU Characteristics: | |
| CPU MHz: | 2933 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 4 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 16 GB (4 x 4 GB 2Rx8 PC3-10600-9, ECC) |
| Disk Subsystem: | 1 x 500 GB SATA II, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | **827** | **47.3** | 826 | 47.3 | 828 | 47.2 | 4 | 702 | 55.7 | 701 | 55.8 | **702** | **55.7** |
| 401.bzip2 | 4 | 1200 | 32.2 | **1201** | **32.1** | 1204 | 32.1 | 4 | 1128 | 34.2 | 1137 | 33.9 | **1130** | **34.2** |
| 403.gcc | 4 | 667 | 48.2 | **674** | **47.8** | 677 | 47.6 | 4 | 680 | 47.4 | 678 | 47.5 | **679** | **47.4** |
| 429.mcf | 4 | 603 | 60.5 | 600 | 60.8 | **602** | **60.6** | 2 | 319 | 57.1 | **319** | **57.1** | 319 | 57.2 |
| 445.gobmk | 4 | 765 | 54.9 | **770** | **54.5** | 771 | 54.4 | 4 | **708** | **59.3** | 710 | 59.1 | 707 | 59.3 |
| 456.hmmer | 4 | **486** | **76.8** | 487 | 76.7 | 484 | 77.1 | 2 | 221 | 84.5 | 221 | 84.2 | **221** | **84.5** |
| 458.sjeng | 4 | **952** | **50.8** | 950 | 50.9 | 954 | 50.7 | 4 | **860** | **56.3** | 855 | 56.6 | 868 | 55.8 |
| 462.libquantum | 4 | 385 | 216 | 381 | 217 | **383** | **216** | 4 | 373 | 222 | 373 | 222 | **373** | **222** |
| 464.h264ref | 4 | **1221** | **72.5** | 1220 | 72.6 | 1222 | 72.5 | 4 | **1182** | **74.9** | 1179 | 75.1 | 1197 | 73.9 |
| 471.omnetpp | 4 | 694 | 36.0 | 696 | 35.9 | **696** | **35.9** | 4 | **639** | **39.1** | 639 | 39.1 | 641 | 39.0 |
| 473.astar | 4 | 816 | 34.4 | 818 | 34.3 | **817** | **34.4** | 4 | 746 | 37.6 | 745 | 37.7 | **746** | **37.7** |
| 483.xalancbmk | 4 | **442** | **62.5** | 442 | 62.4 | 441 | 62.5 | 4 | **442** | **62.5** | 442 | 62.4 | 441 | 62.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

## Platform Notes

```
Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro CSE-827H-R920B chassis.
The chassis is bundled with a PWS-920P-1R power supply, SNK-P0046P heatsink,
and 4 FAN-00111L4 cooling fans.
```

## General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20101028.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20101028.xml`



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:00:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 November 2010.